



PSMN2R6-60PS

N-channel 60 V, 2.6 mΩ standard level MOSFET in SOT78

5 February 2013

Product data sheet

1. General description

Standard level N-channel MOSFET in SOT78 using TrenchMOS technology. Product design and manufacture has been optimized for use in battery operated power tools.

2. Features and benefits

- High efficiency due to low switching & conduction losses
- Robust construction for demanding applications
- Standard level gate

3. Applications

- Battery-powered tools
- Load switching
- Motor control
- Uninterruptible power supplies

4. Quick reference data

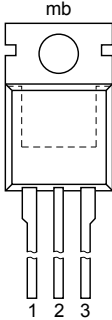
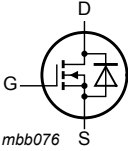
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25\text{ °C}$; $T_j \leq 175\text{ °C}$		-	-	60	V
I_D	drain current	$V_{GS} = 10\text{ V}$; $T_{mb} = 25\text{ °C}$; Fig. 1	[1]	-	-	150	A
P_{tot}	total power dissipation	$T_{mb} = 25\text{ °C}$; Fig. 2		-	-	326	W
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10\text{ V}$; $I_D = 25\text{ A}$; $T_j = 25\text{ °C}$; Fig. 11		-	1.97	2.6	mΩ
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	$I_D = 25\text{ A}$; $V_{DS} = 48\text{ V}$; $V_{GS} = 10\text{ V}$; Fig. 13 ; Fig. 14		-	140	-	nC
Q_{GD}	gate-drain charge			-	43.7	-	nC

[1] Continuous current is limited by package.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 TO-220AB (SOT78)	
2	D	drain		
3	S	source		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN2R6-60PS	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

7. Marking

Table 4. Marking codes

Type number	Marking code
PSMN2R6-60PS	PSMN2R6-60PS

8. Limiting values

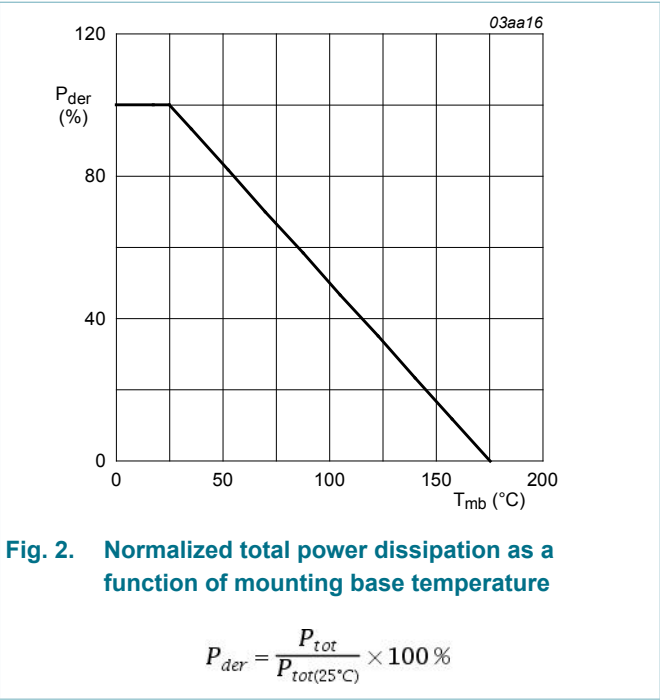
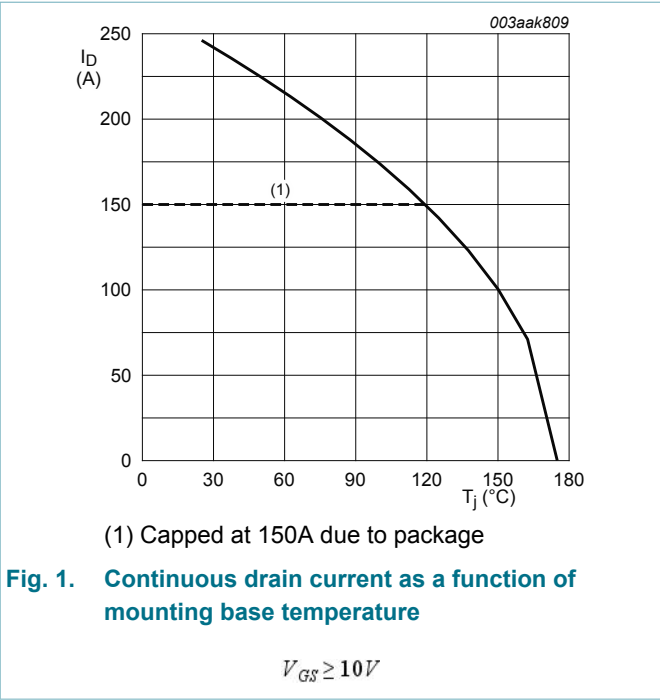
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25\text{ °C}$; $T_j \leq 175\text{ °C}$		-	60	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20\text{ k}\Omega$		-	60	V
V_{GS}	gate-source voltage			-20	20	V
I_D	drain current	$T_{mb} = 25\text{ °C}$; $V_{GS} = 10\text{ V}$; Fig. 1	[1]	-	150	A
		$T_{mb} = 100\text{ °C}$; $V_{GS} = 10\text{ V}$; Fig. 1	[1]	-	150	A
I_{DM}	peak drain current	$T_{mb} = 25\text{ °C}$; pulsed; $t_p \leq 10\text{ }\mu\text{s}$; Fig. 4		-	961	A

Symbol	Parameter	Conditions		Min	Max	Unit
P _{tot}	total power dissipation	T _{mb} = 25 °C; Fig. 2		-	326	W
T _{stg}	storage temperature			-55	175	°C
T _j	junction temperature			-55	175	°C
T _{sld(M)}	peak soldering temperature			-	260	°C
Source-drain diode						
I _S	source current	T _{mb} = 25 °C	[1]	-	150	A
I _{SM}	peak source current	pulsed; t _p ≤ 10 μs; T _{mb} = 25 °C		-	961	A
Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I _D = 150 A; V _{sup} ≤ 60 V; R _{GS} = 50 Ω; V _{GS} = 60 V; T _{j(init)} = 25 °C; unclamped; Fig. 3		-	411	mJ

[1] Continuous current is limited by package.



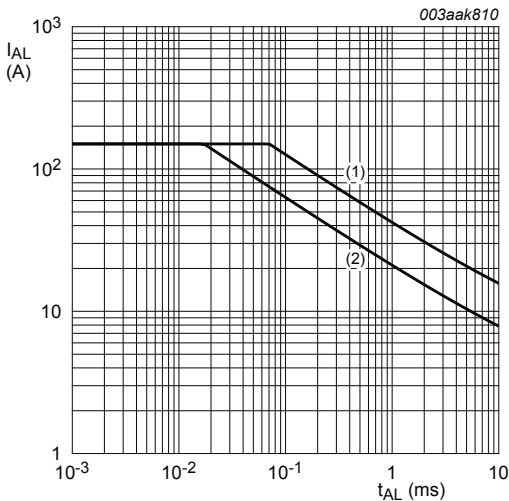


Fig. 3. Avalanche rating; avalanche current as a function of avalanche time

(1) $T_{j(junction)} = 25^{\circ}C$; (2) $T_{j(junction)} = 100^{\circ}C$

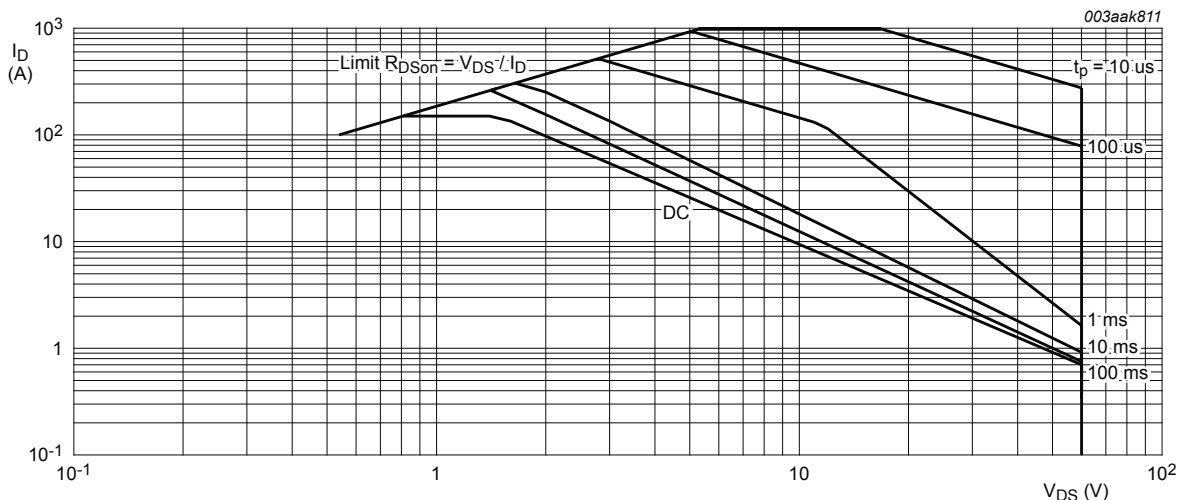


Fig. 4. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$T_{mb} = 25^{\circ}C$; I_{DM} is a single pulse

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 5	-	0.39	0.46	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	vertical in still air	-	60	-	K/W

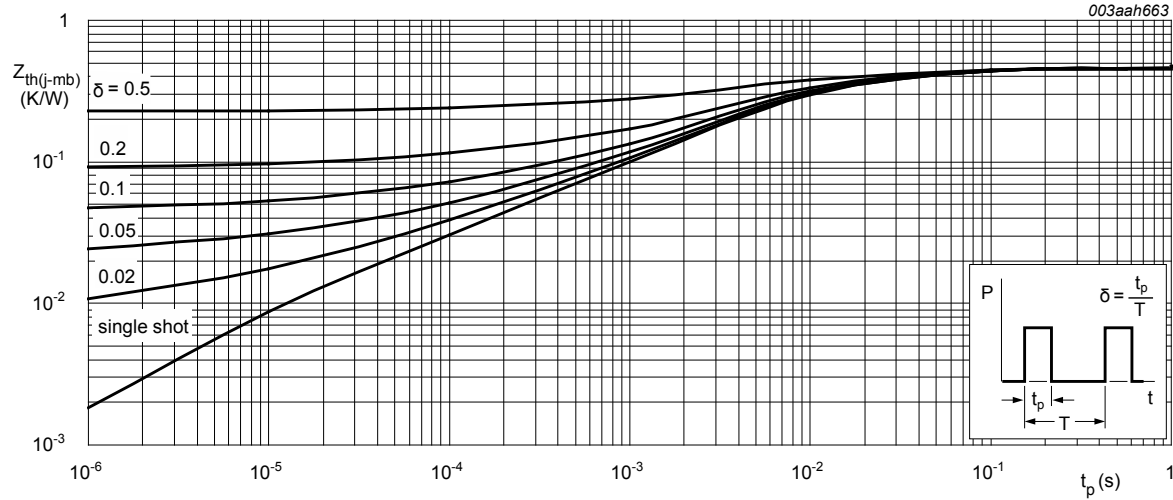


Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250\ \mu A$; $V_{GS} = 0\ V$; $T_J = 25\ ^\circ C$	60	-	-	V
		$I_D = 250\ \mu A$; $V_{GS} = 0\ V$; $T_J = -55\ ^\circ C$	54	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1\ mA$; $V_{DS} = V_{GS}$; $T_J = 25\ ^\circ C$; Fig. 9; Fig. 10	2.4	3	4	V
		$I_D = 1\ mA$; $V_{DS} = V_{GS}$; $T_J = 175\ ^\circ C$; Fig. 9	1	-	-	V
		$I_D = 1\ mA$; $V_{DS} = V_{GS}$; $T_J = -55\ ^\circ C$; Fig. 9	-	-	4.5	V
I_{DSS}	drain leakage current	$V_{DS} = 60\ V$; $V_{GS} = 0\ V$; $T_J = 175\ ^\circ C$	-	-	500	μA
		$V_{DS} = 60\ V$; $V_{GS} = 0\ V$; $T_J = 25\ ^\circ C$	-	0.09	1	μA
I_{GSS}	gate leakage current	$V_{GS} = 20\ V$; $V_{DS} = 0\ V$; $T_J = 25\ ^\circ C$	-	2	100	nA
		$V_{GS} = -20\ V$; $V_{DS} = 0\ V$; $T_J = 25\ ^\circ C$	-	2	100	nA
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10\ V$; $I_D = 25\ A$; $T_J = 25\ ^\circ C$; Fig. 11	-	1.97	2.6	mΩ
		$V_{GS} = 10\ V$; $I_D = 25\ A$; $T_J = 175\ ^\circ C$; Fig. 11; Fig. 12	-	-	5.6	mΩ
R_G	gate resistance	$f = 1\ MHz$	0.39	0.79	1.58	Ω
Dynamic characteristics						
$Q_{G(tot)}$	total gate charge	$I_D = 25\ A$; $V_{DS} = 48\ V$; $V_{GS} = 10\ V$; Fig. 13; Fig. 14	-	140	-	nC
Q_{GS}	gate-source charge		-	32.7	-	nC

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Q _{GD}	gate-drain charge			-	43.7	-	nC
C _{iss}	input capacitance	V _{GS} = 0 V; V _{DS} = 25 V; f = 1 MHz; T _j = 25 °C; Fig. 15		-	7629	-	pF
C _{oss}	output capacitance			-	968	-	pF
C _{rss}	reverse transfer capacitance			-	591	-	pF
t _{d(on)}	turn-on delay time			-	32	-	ns
t _r	rise time	V _{DS} = 45 V; R _L = 1.8 Ω; V _{GS} = 10 V; R _{G(ext)} = 5 Ω		-	50	-	ns
t _{d(off)}	turn-off delay time			-	87	-	ns
t _f	fall time			-	58	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	I _S = 25 A; V _{GS} = 0 V; T _j = 25 °C; Fig. 16		-	0.78	1.2	V
t _{rr}	reverse recovery time	I _S = 20 A; dI _S /dt = -100 A/μs; V _{GS} = 0 V; V _{DS} = 25 V		-	44	-	ns
Q _r	recovered charge			-	67	-	nC

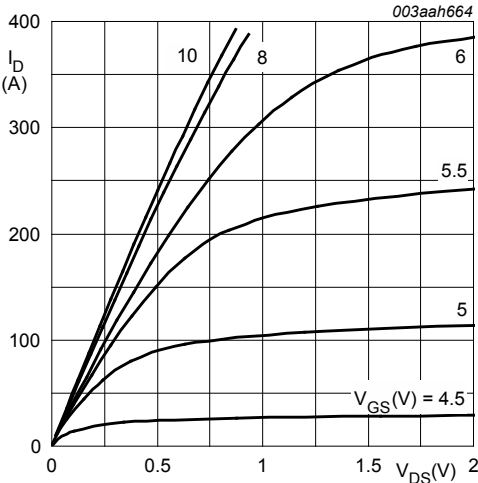


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

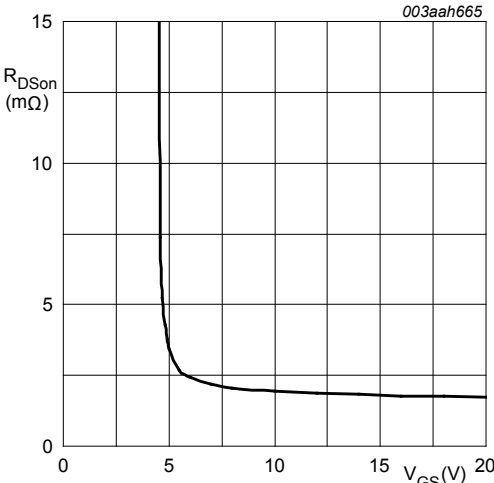


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

$T_j = 25\text{ }^{\circ}\text{C}; I_D = 25\text{ A}$

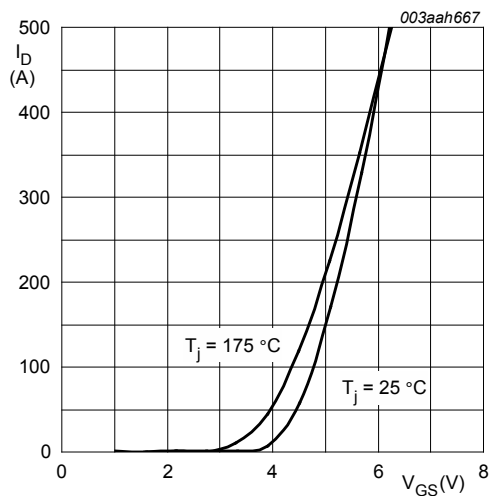


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

$V_{DS} = 10\text{ V}$

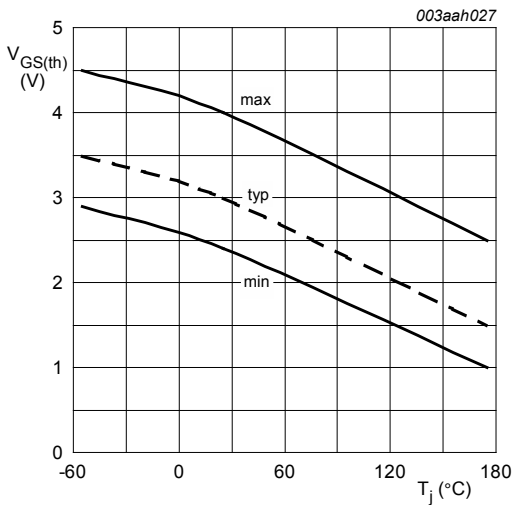


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$I_D = 1\text{ mA}; V_{DS} = V_{GS}$

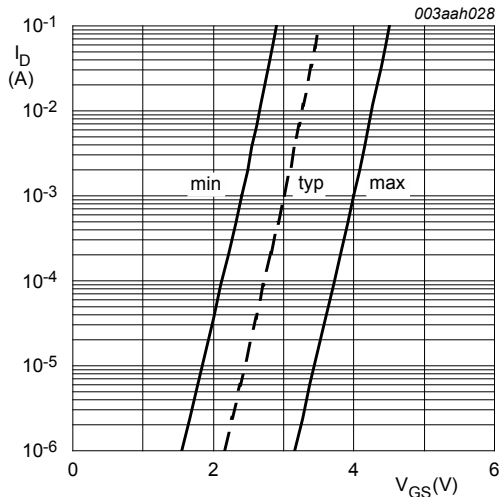


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$T_j = 25\text{ }^{\circ}\text{C}; V_{DS} = 5\text{ V}$

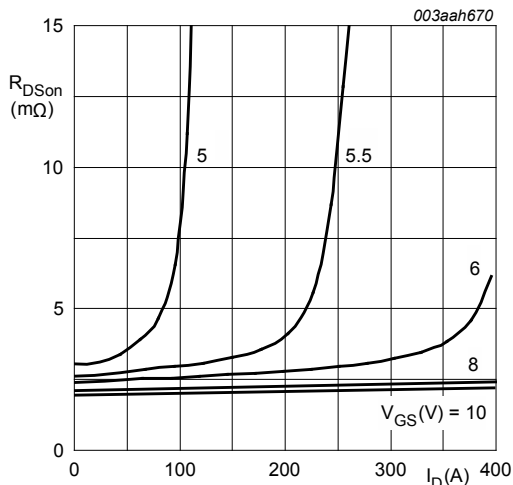


Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

$T_j = 25\text{ }^{\circ}\text{C}; t_p = 300\text{ }\mu\text{s}$

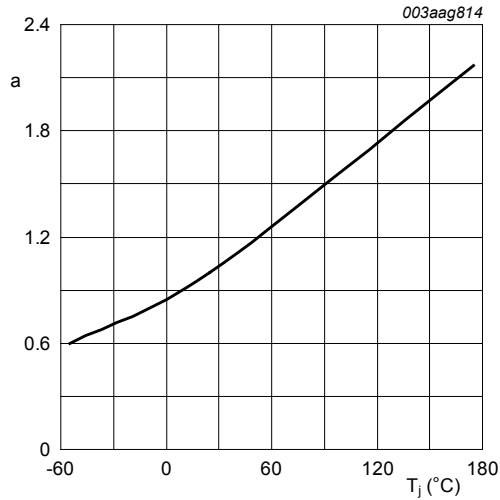


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DS(on)}}{R_{DS(on)(25^\circ\text{C})}}$$

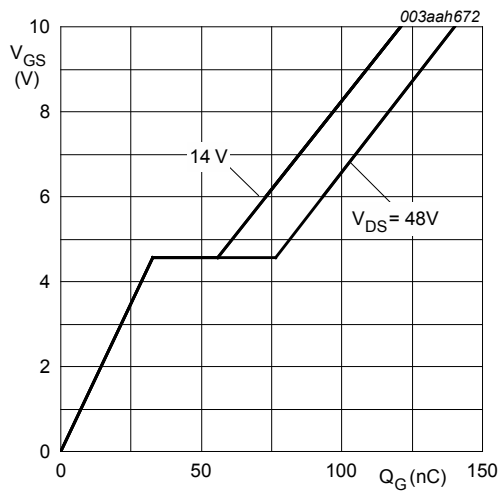


Fig. 14. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25^\circ\text{C}; I_D = 25\text{A}$$

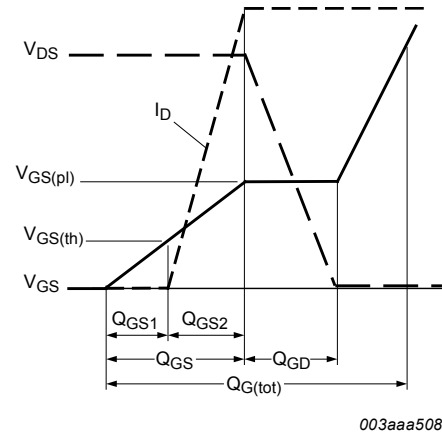


Fig. 13. Gate charge waveform definitions

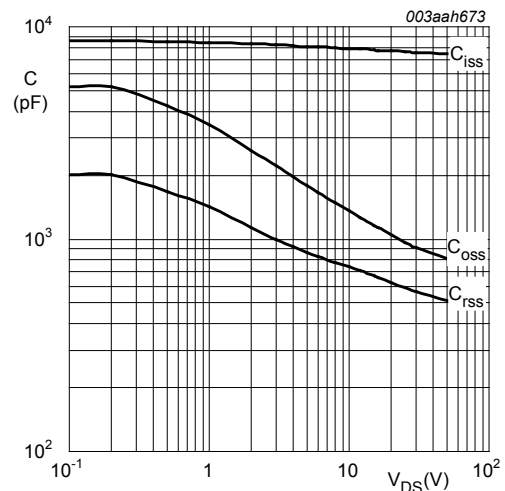


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = 0\text{V}; f = 1\text{MHz}$$

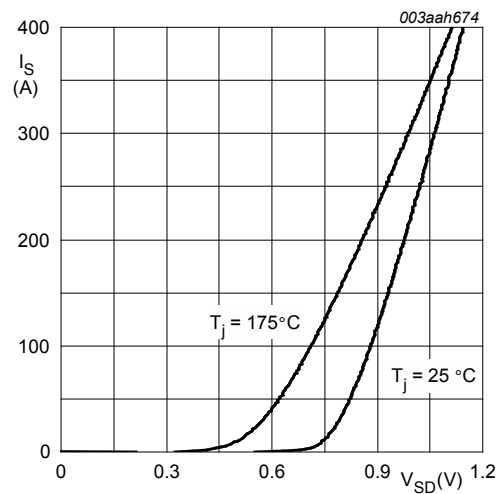


Fig. 16. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

$V_{GS} = 0V$

11. Package outline



Fig. 17. Package outline TO-220AB (SOT78)

12. Legal information

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Document status [1][2]	Product status [3]	Definition
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