



TS809/ 810/ 809R/ 810R

Microprocessor Reset Circuit

SOT-23



Pin assignment:

TS809/810

1. Gnd
2. RESET (RESET)
3. Vcc

Pin assignment:

TS809R/810R

1. RESET (RESET)
2. Gnd
3. Vcc

**Threshold Voltage Option
From 2.63V ~ 4.63V**

General Description

The TS809/810/809R/810R are microprocessor (μ P) supervisory circuit used to monitor the power supplies in μ P and digital systems. They provide excellent circuit reliability and low cost by eliminating external components and adjustments when used with +5V, +3.3V, +3.0V powered circuits. These circuits perform a single function: they assert a reset signal whenever the VCC supply voltage declines below a preset threshold, keeping it asserted for at least 140ms after VCC has risen above the reset threshold. Reset thresholds suitable for operation with a variety of supply voltages are available. The TS809/810/809R/810R have push pull outputs. The TS809/809R have an active low RESET output, while the TS810/810R has an active high RESET output. The reset comparator is designed to ignore fast transients on VCC, and the output guaranteed to be in the correct logic state for VCC down to 1V. Low supply current makes the TS809/810/809R/810R ideal for use in portable equipment.

The TS809/810/809R/810R is available in a 3-pin SOT-23 package.

Features

- ✧ Precision monitoring of +3V, +3.3V and +5V power supply voltage
- ✧ Fully specified over temperature
- ✧ Available in three output configurations
- ✧ Push-Pull RESET low output (TS809 & TS809R)
- ✧ Push-Pull RESET high output (TS810 & TS810R)
- ✧ 140mS min. power-on reset pulse width
- ✧ 12uA supply current
- ✧ Guaranteed reset valid to Vcc = +1V
- ✧ Power supply transient immunity
- ✧ No external components

Ordering Information

Part No.	Enable Function	Package
TS809CX _x	Active-Low	SOT-23
TS810CX _x	Active-High	
TS809RCX _x	Active-Low	
TS810RCX _x	Active-High	

Note: _x is the threshold voltage type, option as

A: 4.63V

B: 4.38V

C: 4.00V

D: 3.08V

E: 2.93V

F: 2.63V

Applications

- ✧ Computers
- ✧ Controllers
- ✧ Intelligent instruments
- ✧ Critical uP and uC power monitoring
- ✧ Portable / Battery powered equipment
- ✧ Automotive

Pin Descriptions

Name	Description
Gnd	Ground
<u>RESET</u> (RESET)	Reset output pin L: for TS809 & TS811 H: for TS809R & TS811R
Vcc	Operating voltage input



Absolute Maximum Rating						
Terminal Voltage (with respect to Gnd)	V _{CC}	- 0.3 ~ +6.0			V	
<u>RESET</u> & (RESET) push-pull	V _{RESET}	- 0.3 ~ (V _{CC} +0.3)			V	
Input Current, V _{cc}	I _{CC}	20			mA	
Output Current, <u>RESET</u> , (RESET)	I _O	20			mA	
Rate of Rise, V _{cc}	V _R	100			V/μS	
Continuous Power Dissipation (Ta=+70 °C) de-rate 4mW/ °C above +70 °C	P _D	320			mW	
Operating Junction Temperature Range	T _{OP}	-40 ~ +105			°C	
Storage Temperature Range	T _{STG}	-65 ~ +150			°C	
Lead Soldering Temperature (260 °C)	T _{LEAD}	10			S	
Electrical Characteristics						
Ta = 25 °C, unless otherwise specified.						
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Input Supply Voltage	Ta = 0 °C ~ +70 °C	V _{CC}	1.0		5.5	V
Supply Current	V _{CC} ≤ 5.5V	I _{CC}		19	60	μA
	V _{CC} ≤ 3.6V			17	50	
Reset Threshold	TS809/910/809R/810RCXA	V _{TH}	4.56	4.63	4.7	V
	TS809/910/809R/810RCXB		4.31	4.38	4.45	
	TS809/910/809R/810RCXC		3.94	4.00	4.06	
	TS809/910/809R/810RCXD		3.03	3.08	3.13	
	TS809/910/809R/810RCXE		2.89	2.93	2.97	
	TS809/910/809R/810RCXF		2.59	2.63	2.67	
Reset Threshold Temperature Coefficient		V _{TH}		30		ppm/ °C
V _{CC} to Reset Delay	V _{CC} = V _{TH} to (V _{TH} - 100mV)	T _{DELAY}		20		μS
Reset Active Timeout Period	Ta = 0 °C ~ +70 °C		100	240	380	mS
<u>RESET</u> Output Voltage Low	V _{CC} = V _{TH(MIN)} , I _{SINK} =1.2mA, TS809 & TS809R	V _{OL}			0.3	V
	V _{CC} = V _{TH(MIN)} , I _{SINK} =3.2mA,				0.4	
	V _{CC} > 1.0V, I _{SINK} =50μA				0.3	
<u>RESET</u> Output Voltage High	V _{CC} > V _{TH(MAX)} , I _{SOURCE} =500μA, TS809 & TS809R	V _{OH}	0.8 V _{CC}			V
	V _{CC} > V _{TH(MAX)} , I _{SOURCE} =800μA,		V _{CC} -1.5			
RESET Output Voltage Low	V _{CC} = V _{TH(MAX)} , I _{SINK} =1.2mA, TS810 & TS810R	V _{OL}			0.3	V
	V _{CC} = V _{TH(MAX)} , I _{SINK} =3.2mA,				0.4	
RESET Output Voltage High	1.8V < V _{CC} < V _{TH(MIN)} , I _{SOURCE} =150μA, TS810 & TS810R	V _{OH}	0.8 V _{CC}			V



Function Description

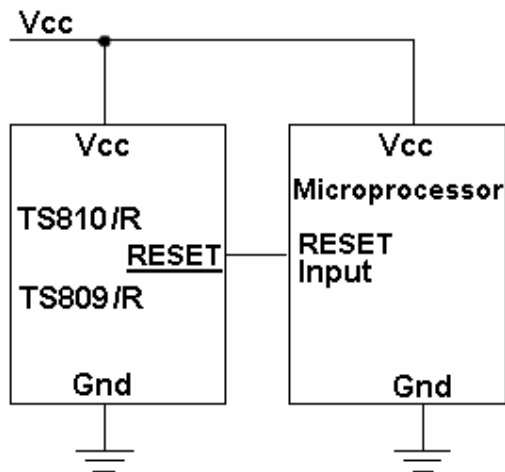
A microprocessor's (μP 's) reset input starts the μP in a known state. The TS809/810/809R/810R assert reset to prevent code-execution errors during power-up, power-down, or brownout conditions. They assert a reset signal whenever the V_{CC} supply voltage declines below a preset threshold, keeping it asserted for at least 140ms after V_{CC} has risen above the reset threshold. The TS809/810/809R/810R have a push-pull output stage.

Applications Information

Negative-Going V_{CC} transients in addition to issuing a reset to the μP during power-up, power-down, and brownout conditions, the TS809/810/809R/810R are relatively immune to short-duration negative-going V_{CC} transients (glitches).

The TS809/810/809R/810R do not generate a reset pulse. The graph was generated using a negative going pulse applied to V_{CC} , starting 0.5V above the actual reset threshold and ending below it by the magnitude indicated (reset comparator overdrive). The graph indicates the maximum pulse width a negative going V_{CC} transient can have without causing a reset pulse. As the magnitude of the transient increases (goes farther below the reset threshold), the maximum allowable pulse width decreases. Typically, a V_{CC} transient that goes 100mV below the reset threshold and lasts 20 μs or less will not cause a reset pulse. A 0.1 μF bypass capacitor mounted as close as possible to the V_{CC} pin provides additional transient immunity.

Applications Circuit



Ensuring a Valid Reset Output Down to $V_{CC}=0$

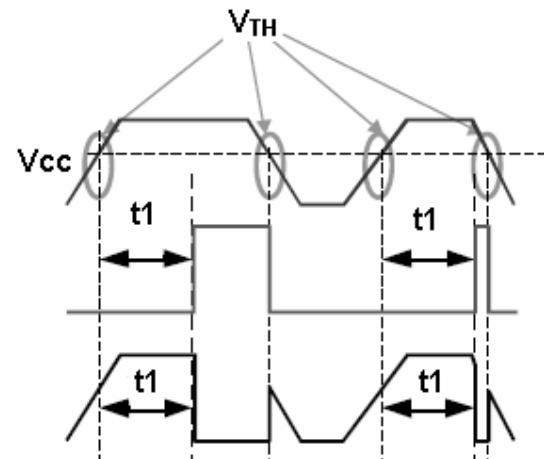
When V_{CC} falls below 1V, the TS809/810/809R/810R RESET output no longer sinks current - it becomes an open circuit. Therefore, high impedance CMOS logic input connected to RESET can drift to undetermined voltages.

This presents no problem in most applications since most μP and other circuitry is inoperative with V_{CC} below 1V. However, in applications where RESET must be valid down to 0V, adding a pull down resistor to RESET causes stray leakage currents to flow to ground, holding RESET low (Figure 2.) R1's value is not critical; 100K is large enough not to load RESET and small enough to pull RESET to ground. For the TS809/810/809R/810R if RESET is required to remain valid for $V_{CC} < 1V$.

Benefits of Highly Accurate Reset Threshold

Most μP supervisor ICs have reset threshold voltages between 5% and 10% below the value of nominal supply voltages. This ensures a reset will not occur within 5% of the nominal supply, but will occur when the supply is 10% below nominal. When using ICs rated at only the nominal supply $\pm 5\%$, this leaves a zone of uncertainty where the supply is between 5% and 10% low, and where the reset may or may not be asserted.

Timing Diagram



Electrical Characteristics Curve

Figure 1. Iq v.s. Temperature

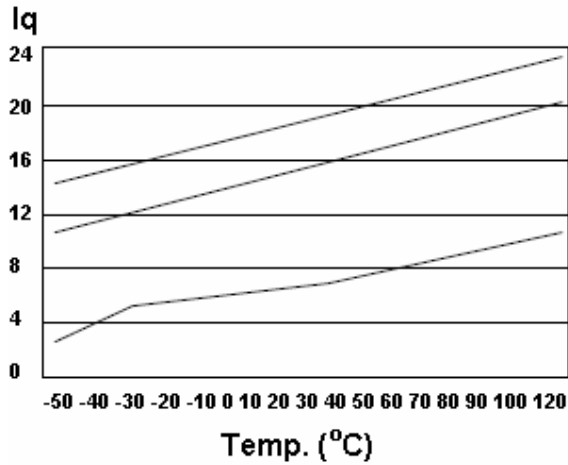


Figure 2. Threshold v.s. Temperature

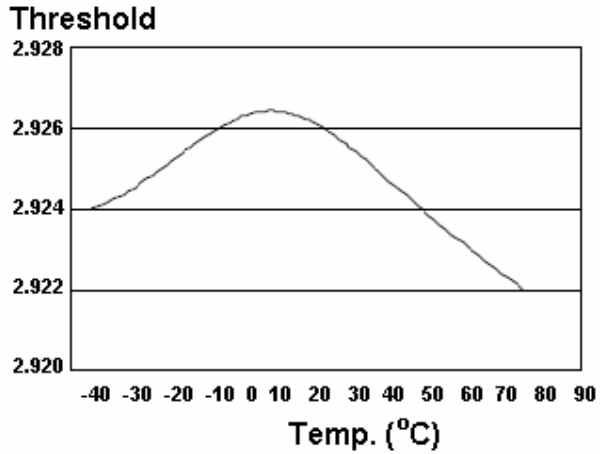
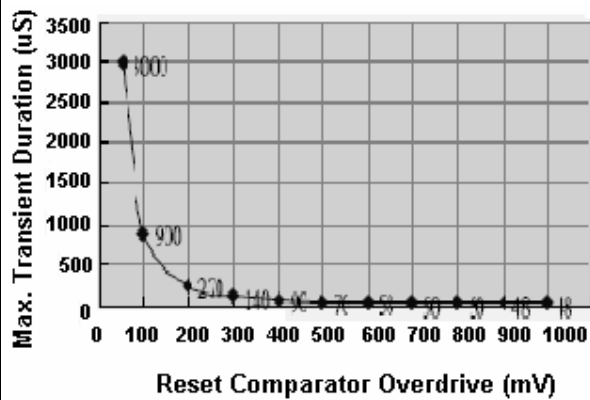
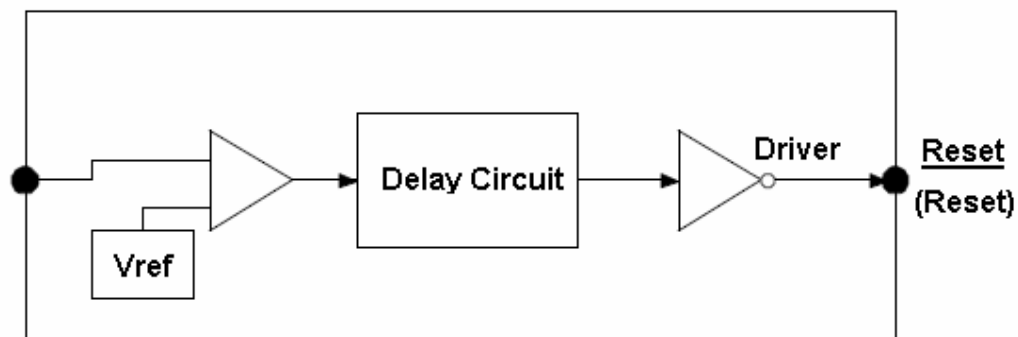


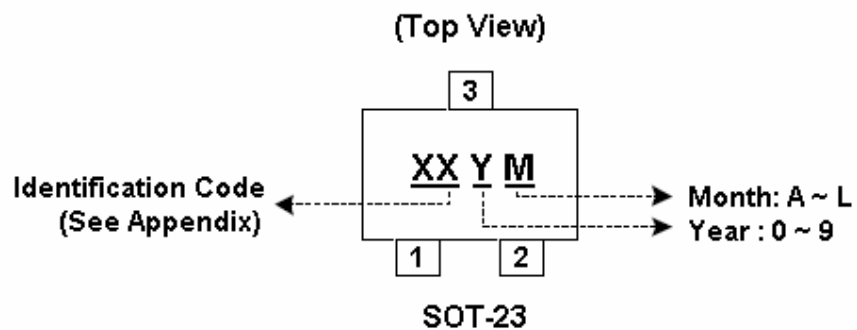
Figure 3. Immunity



Function Block

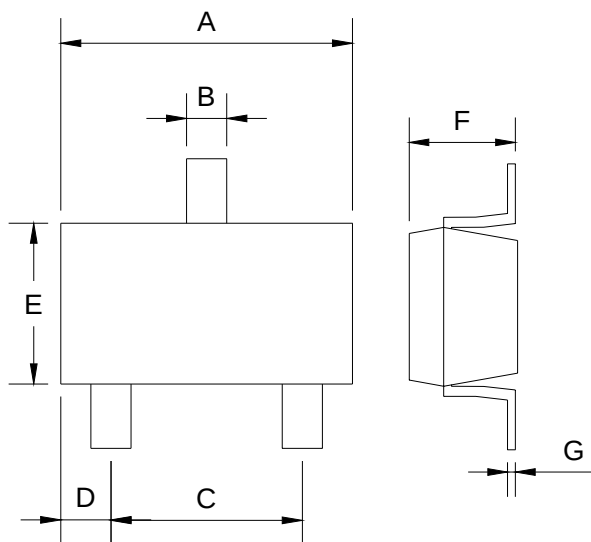


Marking Information



Part No.	Identification Code	Part No.	Identification Code	Part No.	Identification Code	Part No.	Identification Code
TS809CXA	EA	TS810CXA	E0	TS809RCXA	EG	TS810RCXA	E7
TS809CXB	EB	TS810CXB	E2	TS809RCXB	EH	TS810RCXB	E8
TS809CXC	EC	TS810CXC	E3	TS809RCXC	EI	TS810RCXC	E9
TS809CXD	ED	TS810CXD	E4	TS809RCXD	EJ	TS810RCXD	EM
TS809CXE	EE	TS810CXE	E5	TS809RCXE	EK	TS810RCXE	EN
TS809CXF	EF	TS810CXF	E6	TS809RCXF	EL	TS810RCXF	EP

SOT-23 Mechanical Drawing



SOT-23 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.88	2.91	0.113	0.115
B	0.39	0.42	0.015	0.017
C	1.78	2.03	0.070	0.080
D	0.51	0.61	0.020	0.024
E	1.59	1.66	0.063	0.065
F	1.04	1.08	0.041	0.043
G	0.07	0.09	0.003	0.004