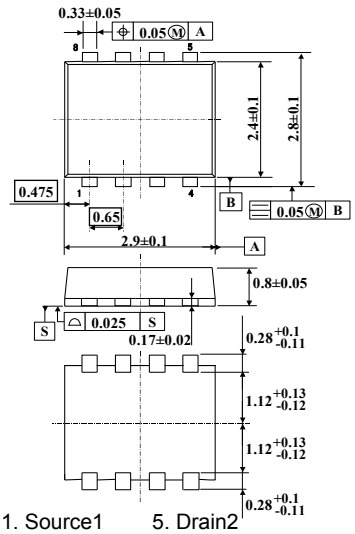
TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSⅣ)

TPCP8302

Lithium Ion Battery Applications
Notebook PC Applications
Portable Equipment Applications

- Lead (Pb)-free
- Small footprint due to small and thin package
- Low drain-source ON-resistance: $R_{DS(ON)}$ = 25 mΩ (typ.)
- High forward transfer admittance: $|Y_{fs}|$ = 14 S (typ.)
- Low leakage current: I_{DSS} = −10 μA (max) (V_{DS} = −20 V)
- Enhancement model: V_{th} = −0.4 to −1.0V (V_{DS} = −6 V, I_D = −1 mA)

Unit: mm



1. Source1 5. Drain2
2. Gate1 6. Drain2
3. Source2 7. Drain1
4. Gate2 8. Drain1

JEDEC	―
JEITA	―
TOSHIBA	2-3V1G

Weight: 0.017 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

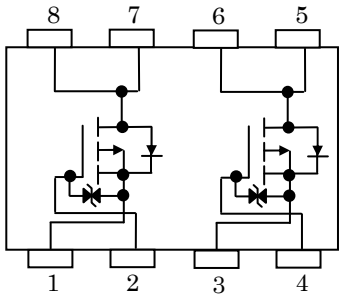
Characteristic		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	−20	V
Drain-gate voltage (R_{GS} = 20 kΩ)		V_{DGR}	−20	V
Gate-source voltage		V_{GSS}	±12	V
Drain current	DC (Note 1)	I_D	−5	A
	Pulse (Note 1)	I_{DP}	−20	
Drain power dissipation (t = 5 s) (Note 2a)	Single-device operation (Note 3a)	$P_{D\ (1)}$	1.48	W
	Single-device value at dual operation (Note 3b)	$P_{D\ (2)}$	1.23	
Drain power dissipation (t = 5 s) (Note 2b)	Single-device operation (Note 3a)	$P_{D\ (1)}$	0.58	
	Single-device value at dual operation (Note 3b)	$P_{D\ (2)}$	0.36	
Single-pulse avalanche energy (Note 4)		E_{AS}	6.5	mJ
Avalanche current		I_{AR}	-5	A
Repetitive avalanche energy Single-device value at dual operation (Note 2a, 3b, 5)		E_{AR}	0.12	mJ
Channel temperature		T_{ch}	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Note: For Notes 1 to 6, see the next page.

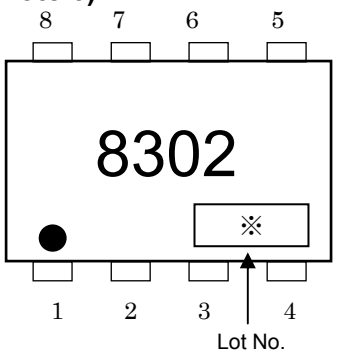
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Circuit Configuration



Marking (Note 6)

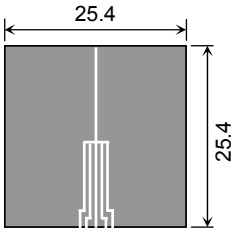


Thermal Characteristics

Characteristic		Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	Single-device operation (Note 3a)	$R_{th\ (ch\text{-}a)\ (1)}$	84.5	°C/W
	Single-device value at dual operation (Note 3b)	$R_{th\ (ch\text{-}a)\ (2)}$	101.6	
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	Single-device operation (Note 3a)	$R_{th\ (ch\text{-}a)\ (1)}$	215.5	°C/W
	Single-device value at dual operation (Note 3b)	$R_{th\ (ch\text{-}a)\ (2)}$	347.2	

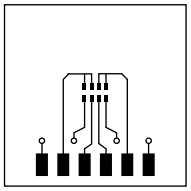
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

(a)



FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

(b)

Note 3: a) The power dissipation and thermal resistance values shown are for a single device. (During single-device operation, power is applied to one device only.)

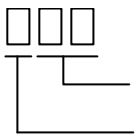
b) The power dissipation and thermal resistance values shown are for a single device. (During dual operation, power is applied to both devices evenly.).

Note 4: V_{DD} = −16 V, T_{ch} = 25°C (initial), L = 0.2 mH, R_G = 25 Ω, I_{AR} = −5 A

Note 5: Repetitive rating: Pulse width limited by Max. Channel temperature.

Note 6: ● on the lower left of the marking indicates Pin 1.

* Weekly code (3 digits):



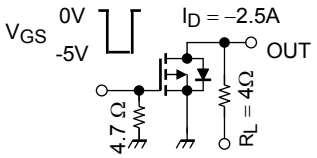
Week of manufacture

(01 for the first week of the year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)

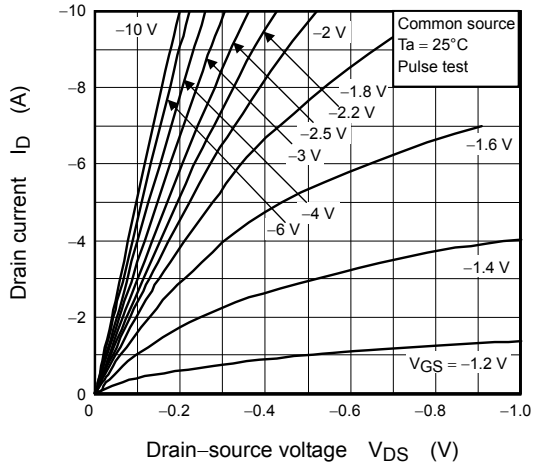
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±10 V, V_{DS} = 0 V	—	—	±10	μA
Drain cutoff current		I_{DSS}	V_{DS} = −20 V, V_{GS} = 0 V	—	—	−10	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = −10 mA, V_{GS} = 0 V	−20	—	—	V
		$V_{(BR)\ DSX}$	I_D = −10 mA, V_{GS} = 12 V	−8	—	—	
Gate threshold voltage		V_{th}	V_{DS} = −6 V, I_D = −1 mA	-0.4	—	-1.0	V
Drain-source ON-resistance		$R_{DS\ (ON)}$	V_{GS} = −1.8 V, I_D = −0.3 A	—	50	95	mΩ
		$R_{DS\ (ON)}$	V_{GS} = −2.5 V, I_D = −2.5A	—	33	45	
		$R_{DS\ (ON)}$	V_{GS} = −4 V, I_D = −2.5A	—	25	33	
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 2.5A	7	14	—	S
Input capacitance		C_{iss}	V_{DS} = −10 V, V_{GS} = 0 V, f = 1 MHz	—	1500	—	pF
Reverse transfer capacitance		C_{rss}		—	220	—	
Output capacitance		C_{oss}		—	240	—	
Switching time	Rise time	t_r	 Duty ≦ 1%, t_w = 10 μs	—	10	—	ns
	Turn-on time	t_{on}		—	20	—	
	Fall time	t_f		—	65	—	
	Turn-off time	t_{off}		—	200	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≃ −16 V, V_{GS} = −5 V, I_D = −5 A	—	20	—	nC
Gate-source charge1		Q_{gs1}		—	3.6	—	
Gate-drain (“Miller”) charge		Q_{gd}		—	5.1	—	

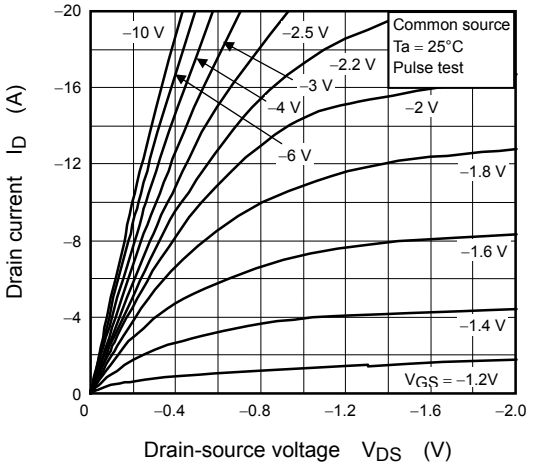
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	I_{DRP}	—	—	—	−20	A
Forward voltage (diode)		V_{DSF}	I_{DR} = −5 A, V_{GS} = 0 V	—	—	1.2	V

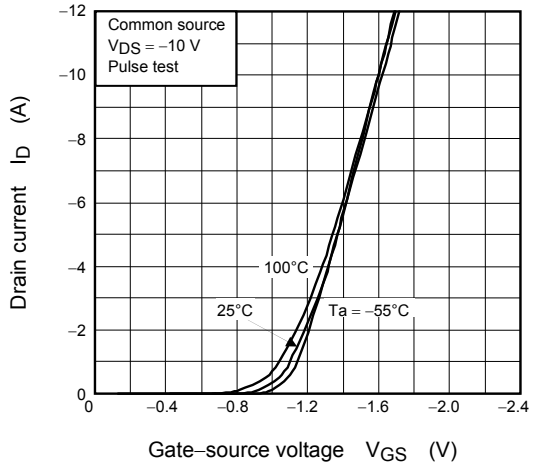
$I_D - V_{DS}$



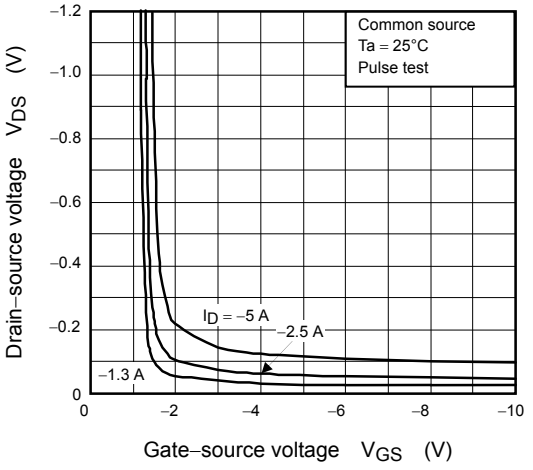
$I_D - V_{DS}$



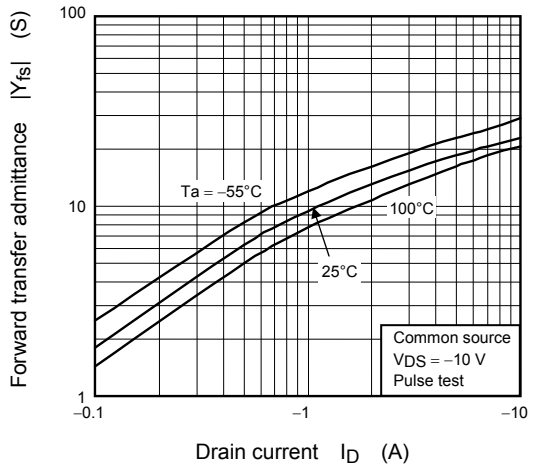
$I_D - V_{GS}$



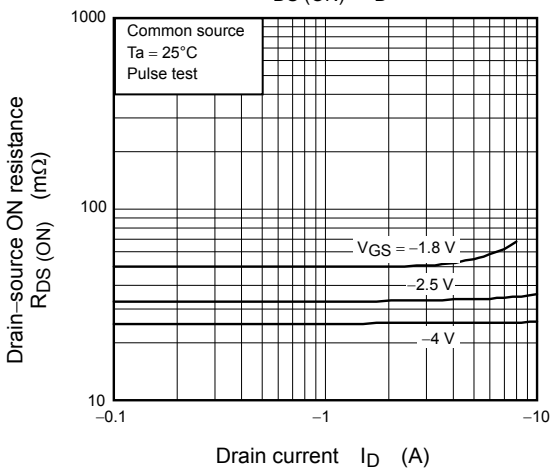
$V_{DS} - V_{GS}$



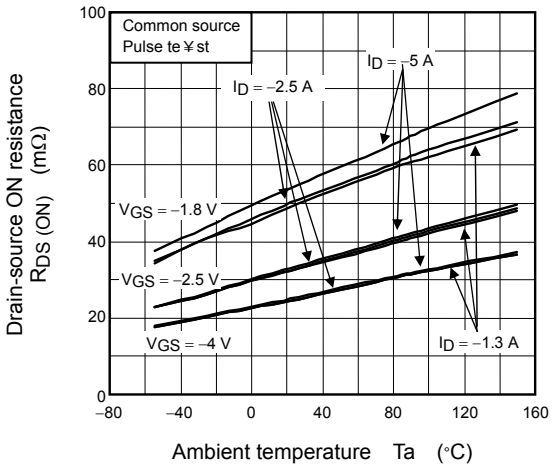
$|Y_{fs}| - I_D$



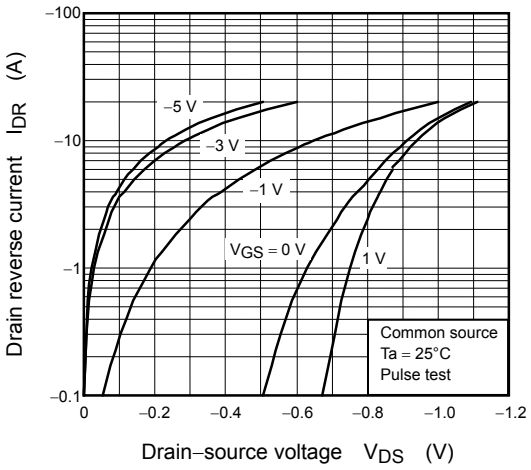
$R_{DS\ (ON)} - I_D$



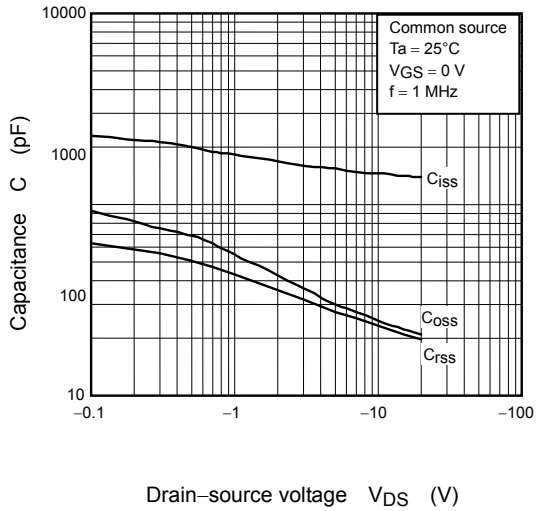
$R_{DS\ (ON)}$ – Ta



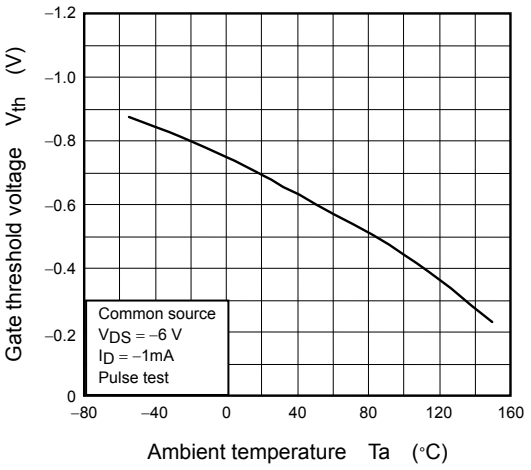
I_{DR} – V_{DS}



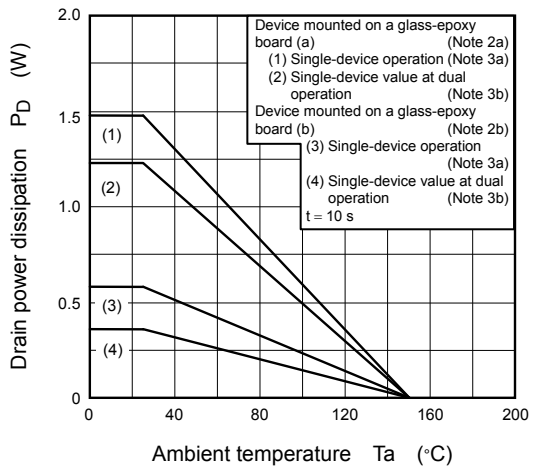
Capacitance – V_{DS}



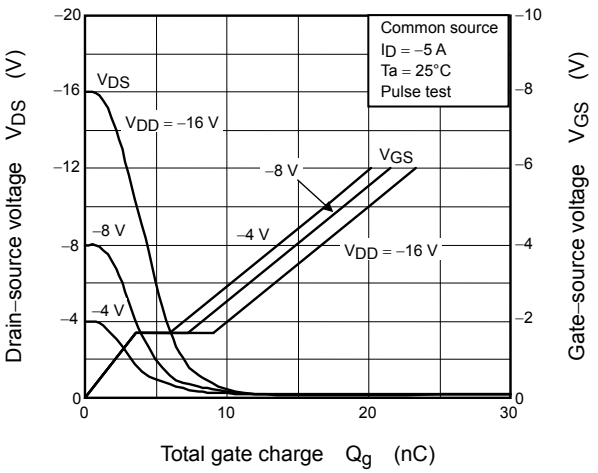
V_{th} – Ta



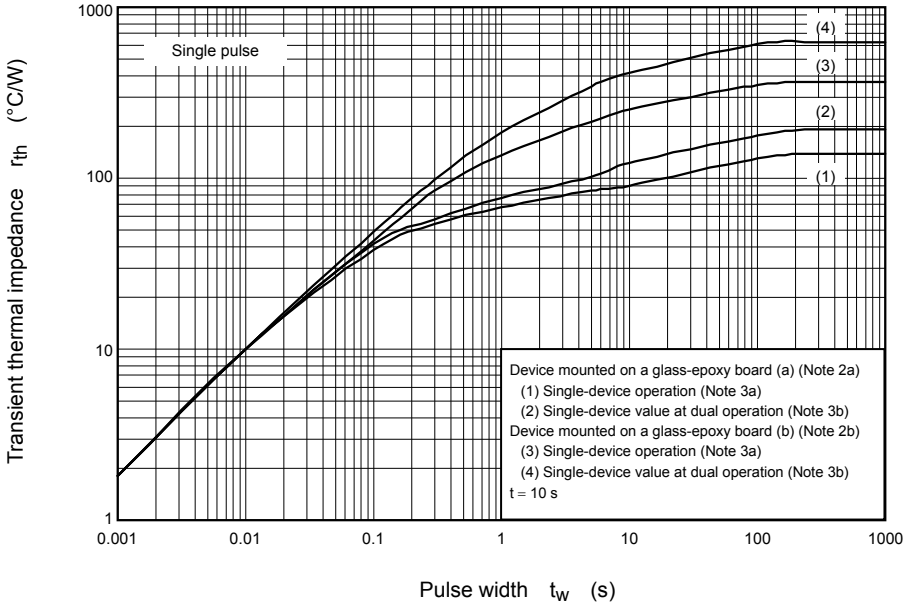
P_D – Ta



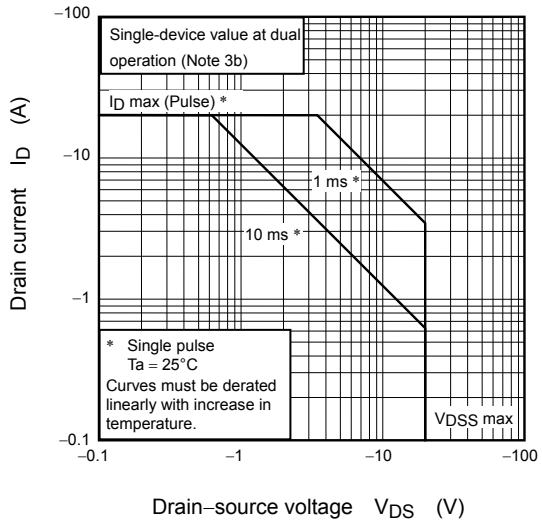
Dynamic input/output characteristics



$r_{th} - t_w$



Safe operating area



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