TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process)

HN3C61FU

Ultra High Speed Switching Application Computer, Counter Applications.

$$\begin{split} & \text{High Transition Frequency} & : f_T = 400 \text{MHz}(\text{Typ.}) \\ & \text{Low Saturation Voltage} & : \text{V}_{\text{CE}(\text{sat})} = 0.3 \text{V}(\text{Max.}) \\ & \text{High Speed Switching Time} & : t_{\text{stg}} = 15 \text{ns}(\text{Typ.}) \end{split}$$

Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	40	V
Collector-emitter voltage	V_{CEO}	15	٧
Emitter-base voltage	V _{EBO}	5	٧
Collector current	IC	200	mA
Base current	Ι _Β	40	mA
Collector power dissipation	P _C *	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	−55~150	°C

^{*}Total rating. Power dissipation per element should not exceed 130mW.

Unit: mm 1.25 ± 0.1 2.0 ± 0.2 1.COLLECTOR1 (C1) 2.EMITTER1 (E1) 3.COLLECTOR2 (C2) 4.EMITTER2 (E2) 5.BASE2 (B2) 6.BASE1 (B1) US₆ JEDEC JEITA **TOSHIBA** 2-2J1A

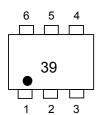
Weight: 6.8mg(typ.)

Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

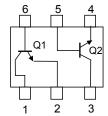
Characteristic		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	_	V _{CB} = 40V, I _E = 0	_	_	0.1	μΑ
Emitter cut-off current		I _{EBO}	_	V _{EB} = 5V, I _C = 0	_	_	0.1	μΑ
DC current gain		h _{FE(1)}	_	V _{CE} = 1V, I _C = 10mA		_	240	
		h _{FE(1)}	_	V _{CE} = 1V, I _C = 100mA	20	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	_	I _C = 20mA, I _B =1mA	_	_	0.3	V
Base-Emitter Saturation voltage		V _{BE (sat)}	_	I _C = 20mA, I _B =1mA	_	_	1.0	V
Transition frequency		f _T	_	V _{CE} = 10V, I _C = 10mA	200	400	_	MH_{Z}
Collector output capacitance		C _{ob}	_	V _{CB} = 10V, I _E = 0, f = 1MH _Z	_	4	_	pF
Switching Time	Turn-on Time	t _{on}	_		_	70	_	
	Storage Time	t _{stg}	_	(Note 1)	_	15	_	ns
	Turn-off Time	t _{off}	_		_	30	_	

Note 1 : Total rating

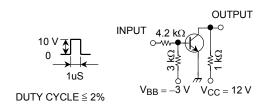
Marking



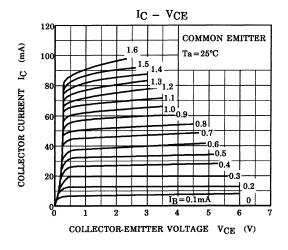
Equivalent Circuit (Top View)

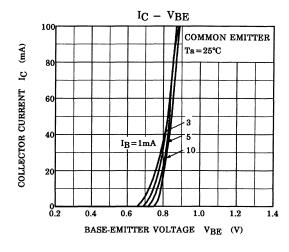


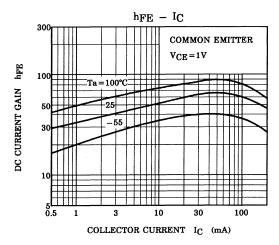
Note 1 : Switching Time Test Circuit

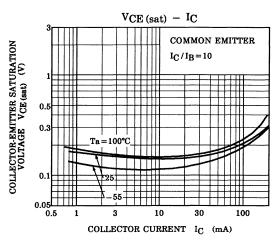


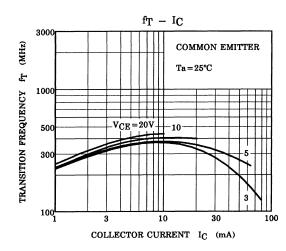
(Q1, Q2 Common)

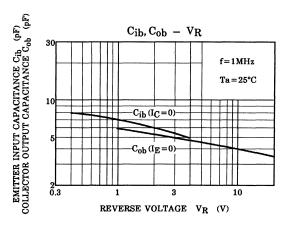




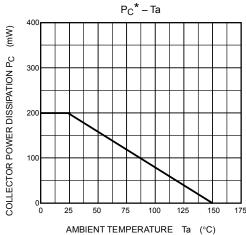








(Q1,Q2 Common)



4

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