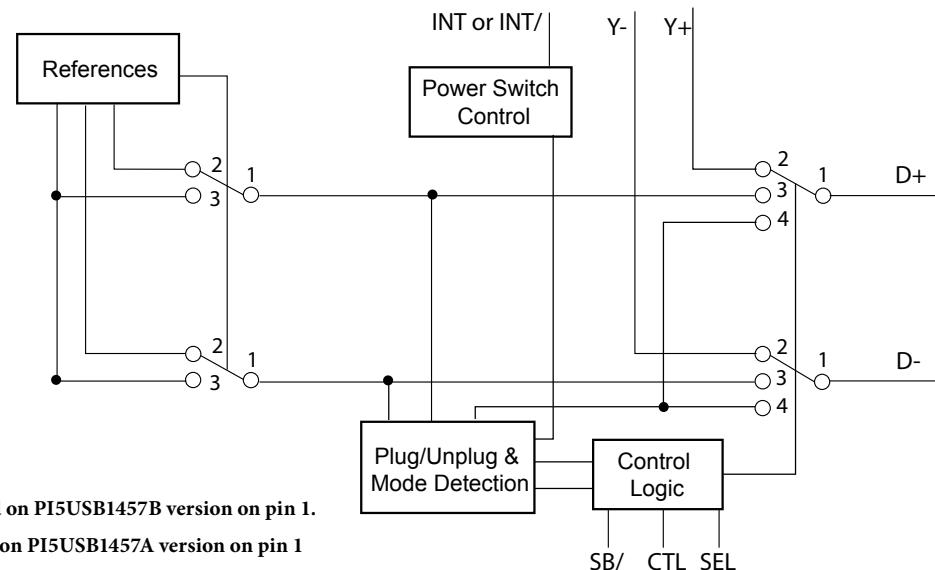
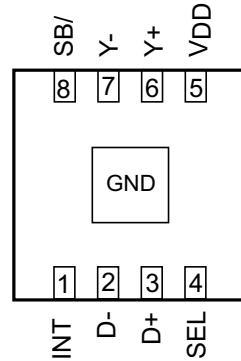
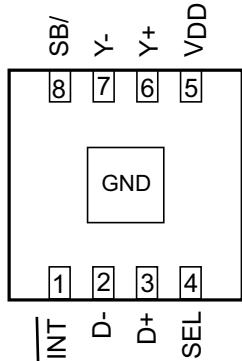
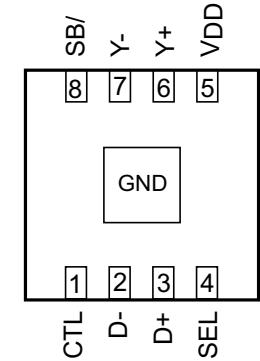


**Features**

- USB charger controller that supports SDP, DCP and CDP modes
  - DCP (Dedicated charger port) is used when enumeration is not available
  - CDP (Charging downstream port) is used when enumeration is available, but FAST charging is requested
  - SDP (Standard downstream port) is used for systems that can only support 500mA across Vbus
- Single Power Supply, 5V ±10%
- Integrated ESD protection up to +/- 4kV contact on pins 2 and 3 per IEC61000-4-2 spec
- Detects and differentiates between mouse/keyboard (USB Low/Full Speed device) when in Sleep and Charge mode (USB1457 and USB1457A versions only)
- Provides multiple modes of charging to ensure all of the following spec's can be met:
  - USB Battery Charger 1.2 spec
  - USB Battery Charger 1.1 spec
  - USB Battery Charger 1.0 spec
  - YD/T-1591 charger spec
  - Certain modes available can also support devices using non-standard approaches to charging, such as Apple products and RIM/Blackberry products
- Packaging (Pb-free & Green):
  - 8-Contact TDFN, ZAE (2.0mm x 2.0mm x 0.75mm)

**Block Diagram**

**Pin Configuration 8-Pin TDFN (ZAE)**


PI5USB1457B pinout

PI5USB1457 pinout

PI5USB1457A pinout

**Pinout Table**

PI5USB1457	PI5USB1457A	PI5USB1457B	Pin Name	I/O Type	Description
1	*	*	INT/	O	p-mosfet open source output. When SB/ observes a logic change (from H to L or from L to H). INT/ is high for ~ 2.1 seconds, otherwise INT/ is hi-z. External pull-down is required.
*	1	*	INT	O	n-mosfet open drain output. When SB/ observes a logic change (from H to L or from L to H). INT is LOW for ~ 2.1 seconds, otherwise, INT is hi-z. External pull-up is required.
*		1	CTL	I	Input control logic (See truth table)
2	2	2	D-	I/O	USB Connector, D- connection
3	3	3	D+	I/O	USB Connector, D+ connection
4	4	4	SEL	I	Input control logic (See truth table)
5	5	5	V <sub>DD</sub>	Power	5V power supply
6	6	6	Y+	I/O	Host USB transceiver (if existent) D+ connection
7	7	7	Y-	I/O	Host USB transceiver (if existent) D- connection
8	8	8	SB/	I	Control input to switch between S0 charging or sleep and charge
9	9	9	EP	Ground	Exposed Pad. Connect EP to ground.

## Truth Tables

### PI5USB1457 and PI5USB1457A

SB/ (pin 8)	SEL(pin 4)	Feature	pin 1 role (INT or INT/)
0	0	Auto S & C without mouse/keyboard pass through	INT or INT/
0	1	Auto S & C with mouse/keyboard pass through	INT or INT/
1	0	S0 charging with SDP only	INT or INT/
1	1	S0 charging with CDP or SDP only (depending on external device)	INT or INT/
0	M= (1/2)*V <sub>DD</sub>	Test Mode, M = V <sub>DD/2</sub> = (1/2)*V <sub>DD</sub>	

### PI5USB1457B

SB/ (pin 8)	CTL (pin 1)	Feature	pin 4 role (SEL)
0	0	Auto S & C	0: without mouse/keyboard pass through
0	0	Auto S & C	1: Not Allowed
0	1	S & C with manual mode	0: BC1.1 only
0	1	S & C with manual mode	1: Apple only
1	0	S0 charging enabled	0: SDP only
1	0	S0 charging enabled	1: SDP/CDP depending on external device
1	1	S0 charging enabled	0: SDP only
1	1	power down	1: IC power off with all I/Os hi-z
0	x	test mode	M = (1/2)*V <sub>DD</sub>

## Detailed Feature Description

### Sleep and Charge Mode enabled

When the PI5USB1457 (\*A/B) is configured into auto mode with sleep and charge enabled (see truth table to understand how to configure into auto mode), the charger controller will manage all communication with external devices to enable charging to occur. First, Pericom's IC will use load sensing techniques to detect when a device has been plugged in.

Once a device is properly detected, the Pericom IC will automatically switch to the appropriate charging mode required for the specific external USB device that was detected.

### Mouse/Keyboard pass through mode Explanation

Since USB1.1 devices do not integrate charging protocols, there is no special charging mode required to support charging the device. Therefore, if a USB Low-Speed external device is detected, then Pericom's solution will simply connect D+/- from the USB connector directly to the USB host controller.

## System wake-up via external mouse or keyboard

Today's systems are enabled to wake up from S3 mode if a pre-enumerated mouse/keyboard has activity (movement or key-pressed). In order for the system to continue supporting this feature, the USB host controller must be able to communicate with the external device in S3 mode. Therefore, if a mouse/keyboard is detected (via the USB low speed pass through feature), Pericom's PI5USB1457 family will enable the communication between device and controller. Our device will automatically pass the signals through with minimal degradation.

## CDP mode description

When the PI5USB1457 family is configured into CDP mode (see truth table to understand how to configure into CDP mode), D+/- is connected to Y+/-, allowing communication/enumeration to occur between USB host controller and USB device. The Pericom device will continue to monitor D+ for contact detection and to start CDP charging.

Once contact detection has been observed, then CDP enumeration will occur. The external device must initiate the charge-set up, and once the Pericom device correctly observes the required command, the PI5USB1457 family will respond to complete the charge-set up.

## Electrical Parameters/Characteristics Recommended Operation Conditions

Parameter	Min.	Typ.	Max.	Unit
Ambient Operating Temperature	-40		+85	°C
V <sub>DD</sub> Power Supply Voltage (measured in respect to GND)	+4.5		+5.5	V

## DC Electrical Characteristics (V<sub>DD</sub> = 5.0V +/- 10%, Ambient Temperature -40 to +85°C)

Symbol	Parameter	Conditions		Min.	Typ.	Max.	Unit
V <sub>DD</sub>	Operating Voltage			4.5		5.5	V
I <sub>DD</sub>	Supply Current	V <sub>DD</sub> = 5.25V	SB/ = Low, SEL = Low CTL (*B only) = don't care		250	300	μA
			SB/ = Low, SEL = High CTL (*B only) = don't care		250	300	
			SB/ = High, SEL = Low CTL (*B only) = don't care		50	75	
			SB/ = High, SEL = High CTL (*B only) = don't care		115	165	
I <sub>DDQ</sub>	Quiescent Supply Current	Power Down Mode enabled (only available on *B version SEL = SB/ = CTL = HIGH, V <sub>DD</sub> = 5.25V)			16	25	
V <sub>IH</sub> for SB+CTL	Input High Voltage	SB/, CTL		1.4			V
V <sub>IL</sub> for SB+CTL	Input Low Voltage	SB/, CTL				0.4	
V <sub>IH</sub> for SEL	Input High Voltage	For SEL pin	V <sub>DD</sub> = 5V	3.2			
V <sub>IL</sub> for SEL	Input Low Voltage	For SEL pin				0.8	
V <sub>OH</sub> for INT/	Output high voltage	For INT/ pin	PI5USB1457ZAE only	2.4			V
V <sub>OL</sub> for INT	Output high voltage	For INT pin	PI5USB1457AZAE only			0.55	V

**DC Electrical Characteristics** ( $V_{DD} = 5.0V \pm 10\%$ , Ambient Temperature -40 to +85°C) Cont..

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{ON}$	Switch On-Resistance for USB 2.0 HS signal (From $D_{\pm}$ to $Y_{\pm}$ )	$V_{DD} = 4.5V, -0.4V < V_{INPUT} < +0.4V, I_{INPUT} = +30mA, SB/ = High$		3	5	$\Omega$
$R_{ON}$	Switch On-Resistance for USB 2.0 FS signal (From $D_{\pm}$ to $Y_{\pm}$ )	$V_{DD} = 5.0V, 0V < V_{INPUT} < 3.3V, I_{INPUT} = -15mA, SB/ = High$			7	$\Omega$
$I_{OZ}$	I/O leakage on signal pins ( $D_{\pm}, Y_{\pm}$ ) current when port is off	$V_{DD} = 5.5V, V_{INPUT}$ from 0V to 3.6V			$\pm 10$	$\mu A$
$I_{OFF}$	Signal leakage on signal pins ( $D_{\pm}, Y_{\pm}$ ) when chip is off	$V_{DD} = 0V, V_{INPUT}$ from 0V to 5.5V			$\pm 10$	$\mu A$

**Capacitance** (For typical spec  $T_A = 25^{\circ}C$ , for max spec  $T_A = -40$  to  $+85^{\circ}C, f = 1MHz$ )

Parameter	Description	Test Condition	Typ.	Max.	Unit
$C_{IN}$	Control Logic Capacitance		2.7	3.5	$pF$
$C_{OFF}$ - USB path only	Switch Capacitance at $D_{\pm}$ or $Y_{\pm}$ when switch is OFF	SB/ = low	3.1	3.5	
$C_{ON}$ - USB path only	Switch Capacitance between $D_{+/-}$ to $Y_{+/-}$ when switch is ON	SB/ = high	6.0	6.5	

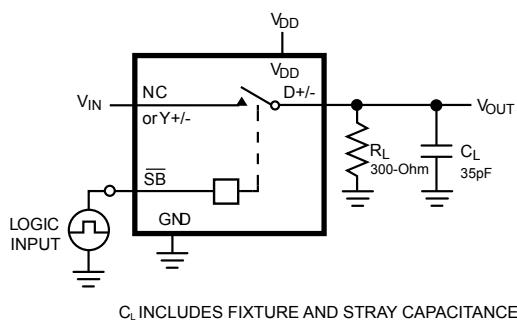
**Dynamic Electrical Characteristics Over the Operating Range**

Parameter	Description	Test Condition	Min.	Typ.	Max.	Units
XTALK	Crosstalk	$R_L = 50\Omega, freq = 240MHz$		-30		dB
OIRR	Off-Isolation	$R_L = 50\Omega, freq = 240MHz$		-30		dB
-3dB BW	-3dB Bandwidth (along $D_{+/-}$ path)	$R_L = 50\Omega$		1,350		MHz
-0.5dB BW	-0.5dB Bandwidth (along $D_{+/-}$ path)	$R_L = 50\Omega$		350		MHz
INLOSS	Insertion loss (along $D_{+/-}$ path)	freq = 240MHz		0.57		dB

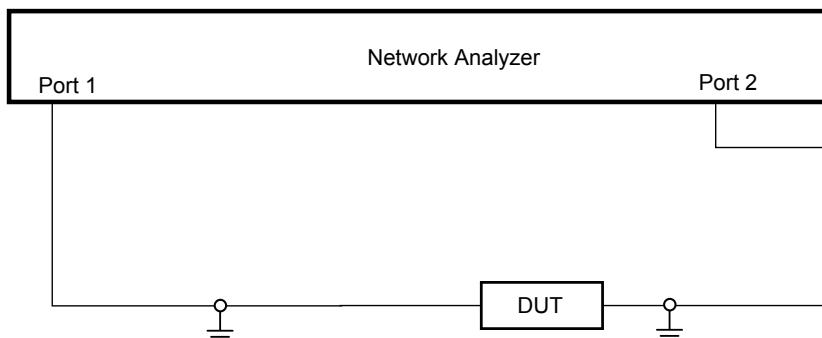
**CDP Mode**

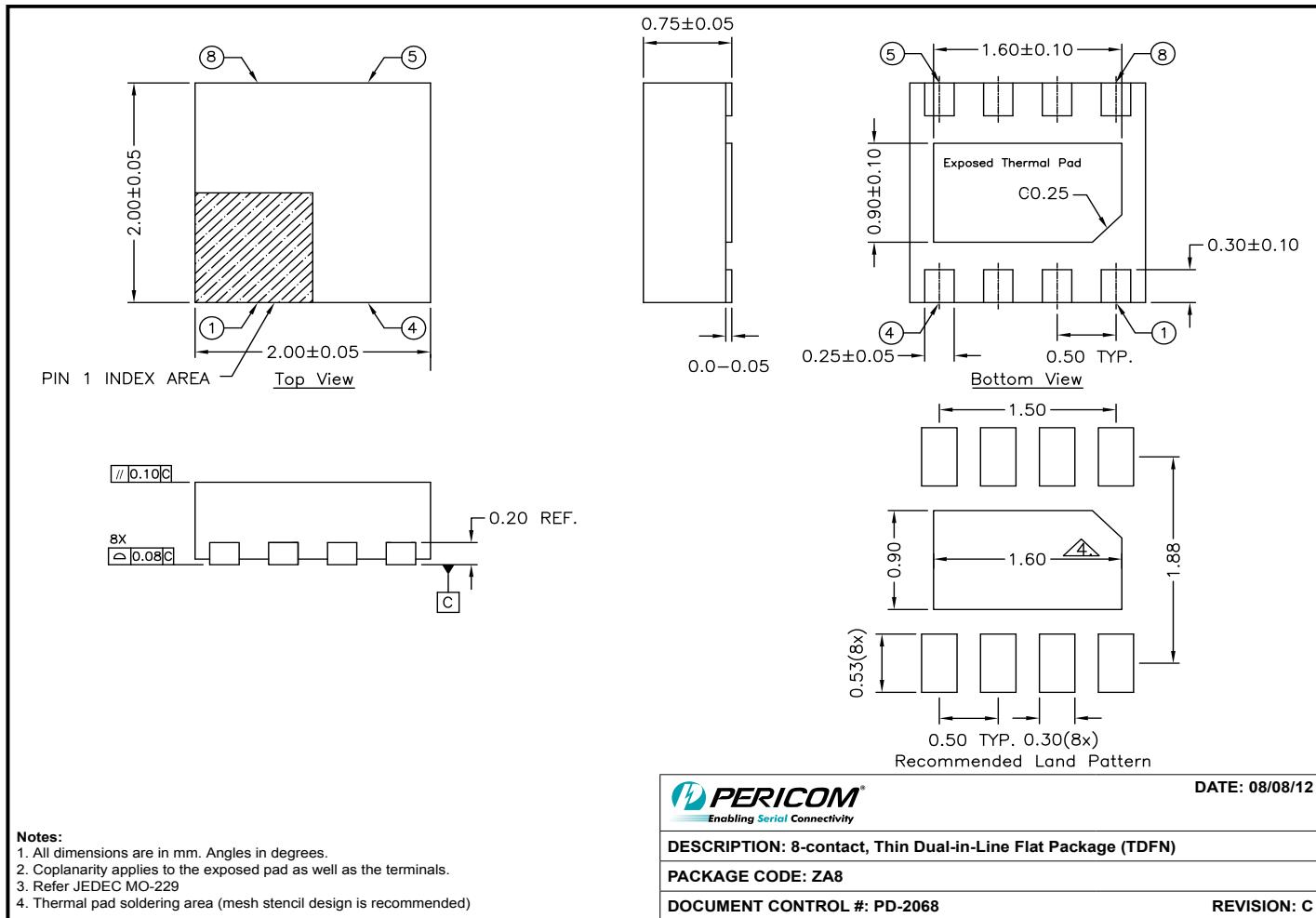
Symbol	Parameter	Test Condition	Min	Max	Units
V_D-_SRC	Voltage source on D- for CDP detect	$V_{D-} = 0.6V$	0.5	0.7	V
V_D+_SINK	D+_IN sink current	$0.4V \leq V_{D-} \leq 0.8V$	50	150	uA

Parameter	Description	Test Condition(1)	Min	Typ	Max	Units
Tpd	Propagation delay			0.3		ns
Intra-Pair Skew	Output skew, bit to bit (Between + and - of same pair)	See Test Circuit for Electrical Characteristics		10		ps
TON	Turn on time for USB path	$C_L = 35pF, R_L = 300\Omega$			0.5	\mu s
TOFF	Turn off time for USB path	$C_L = 35pF, R_L = 300\Omega$			0.25	

**Figure 1: Test Circuit for Electrical Characteristics**

**Notes:**

1.  $C_L$  = Load capacitance: includes jig and probe capacitance.
2.  $R_T$  = Termination resistance: should be equal to  $Z_{OUT}$  of the Pulse Generator
3. All input impulses are supplied by generators having the following characteristics: PRR  $\leq$  MHz,  $Z_O = 50\Omega$ ,  $t_r \leq 2.5ns$ ,  $t_f \leq 2.5ns$ .

**Test Circuit for Dynamic Electrical Characteristics**


**Packaging Mechanical: 8-Pin TDFN (ZAE)**


13-0126

**Ordering Information**

Ordering Code	Package Code	Package Description	Top Mark
PI5USB1457ZAE	ZAE	Pb-free & Green, 8-pin TDFN	UX
PI5USB1457AZAE	ZAE	Pb-free & Green, 8-pin TDFN	UB
PI5USB1457BZAE	ZAE	Pb-free & Green, 8-pin TDFN	

**Notes:**

- Thermal characteristics can be found on the company web site at [www.pericom.com/packaging/](http://www.pericom.com/packaging/)
- E = Pb-free and Green
- X suffix = Tape/Reel