
CHT-OPA DATASHEET

*Revision: 2.5
Dec. 30, 2014*

High-Temperature General-Purpose Quad Operational Amplifier

General Description

The CHT-OPA is a general-purpose quad operational amplifier for applications over the temperature range from -55 to 225°C.

The CHT-OPA can operate with both single and symmetrical power supplies. The supply voltages range goes from 4.5 to 20V.

The CHT-OPA uses internal metal lines presenting extremely high immunity to electromigration, improving product lifetime.

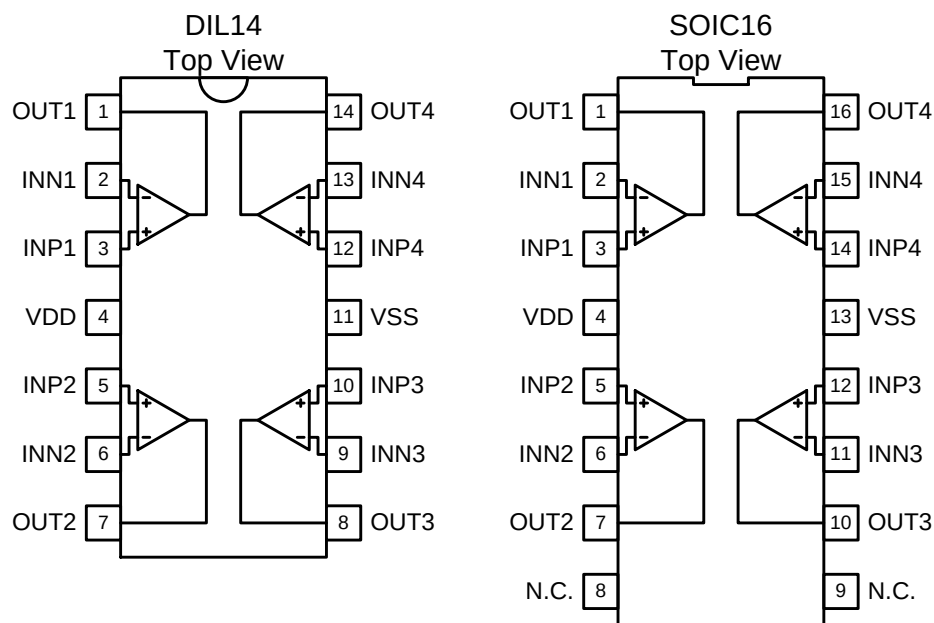
Features

- Qualified from -55 to +225°C (Tj)
- 4.5 to 20V supply voltages
- Single or symmetrical supply operation
- Latchup-free at any supply and temperature condition
- Validated at 225°C for 30000 hours (and still on-going)
- Available in DIL14 and SOIC16 hermetic ceramic standard packages

Applications

- Well logging, Automotive, Aeronautics & Aerospace
 - Harsh Environments
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Package Configurations¹



Pin #	Pin Name	Pin Description
1	OUT1	OPA1 output
2	INN1	OPA1 negative input
3	INP1	OPA1 positive input
4	VDD	Positive power supply
5	INP2	OPA2 positive input
6	INN2	OPA2 negative input
7	OUT2	OPA2 output
8	OUT3	OPA3 output
9	INN3	OPA3 negative input
10	INP3	OPA3 positive input
11	VSS	Negative power supply
12	INP4	OPA4 positive input
13	INN4	OPA4 negative input
14	OUT4	OPA4 output

Pin #	Pin Name	Pin Description
1	OUT1	OPA1 output
2	INN1	OPA1 negative input
3	INP1	OPA1 positive input
4	VDD	Positive power supply
5	INP2	OPA2 positive input
6	INN2	OPA2 negative input
7	OUT2	OPA2 output
8	N.C.	Not connected pin
9	N.C.	Not connected pin
10	OUT3	OPA3 output
11	INN3	OPA3 negative input
12	INP3	OPA3 positive input
13	VSS	Negative power supply
14	INP4	OPA4 positive input
15	INN4	OPA4 negative input
16	OUT4	OPA4 output

¹ Other packages available upon request.

Absolute Maximum Ratings

Supply Voltage VDD to VSS -0.5 to 25V
Voltage on any Pin to VSS -0.5 to V_{DD}+0.5V

Operating Conditions

Supply Voltage VDD to VSS 4.5V to 20V
Junction temperature -55°C to +225°C

ESD Rating (expected)

Human Body Model 1kV

DC Electrical Characteristics

Unless otherwise stated: VDD=10V, VSS=0V, T_J=25°C. **Bold underlined** values indicate values over the whole temperature range (-55°C < T_J < +225°C).

Parameter	Condition	Min	Typ	Max	Units
Supply voltage VDD-VSS		4.5		20	V
Supply current (full package) I_{DD}	T _J =25°C			1.7	mA
	T _J =-55 to 225°C			<u>2.2</u>	
Output voltage swing V_O	R _L =2kΩ, THD ¹ =1%	0.15		VDD-0.18	V
	R _L =∞, THD=0.1%	0.03		VDD-0.02	
Output current ^{2,3} I_O	T _J =-55 to 225°C			<u>±15</u>	mA
Common mode input range V_{CM}	T _J =225°C	1.5		VDD-0.1	V
	T _J =-55°C	2.0		VDD-0.2	
Input offset voltage ⁴ V_{IOFF}	T _J =25°C		<±2.5	±8	mV
Input offset drift ³ TC_{VIOFF}	T _J =25°C		<±5	±15	μV/°C
Input bias current ⁵ I_B	T _J =-55 to 225°C			<u>±10</u>	nA
Input offset current ⁴ I_{OFF}	T _J =25°C			±0.01	nA
	T _J =225°C			±10	

¹ Total Harmonic Distortion.

² Source or sink.

³ Output current is not internally limited. Value given indicate the maximum recommended conditions.

⁴ The absolute value of the input offset voltage, |V_{IOFF}|, decreases as temperature increases. TC_{VIOFF} must be used so that |V_{IOFF}| decreases with temperature, i.e. TC_{VIOFF} has opposite sign than V_{IOFF}.

⁵ Due to ESD structures. Under full characterization.



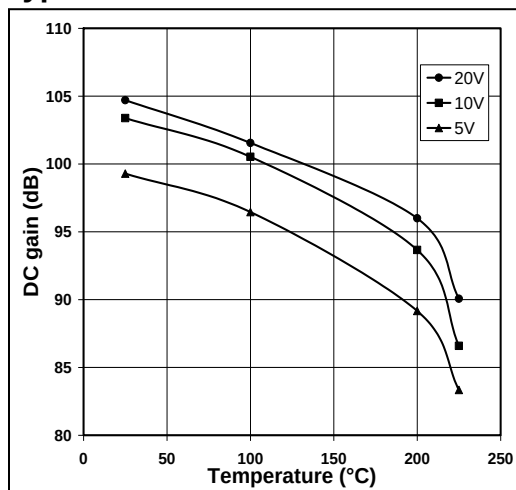
AC Electrical Characteristics

Unless otherwise stated: VDD=10V, VSS=0V, $T_j=25^{\circ}\text{C}$. **Bold underlined** values indicate values over the whole temperature range ($-55^{\circ}\text{C} < T_j < +225^{\circ}\text{C}$).

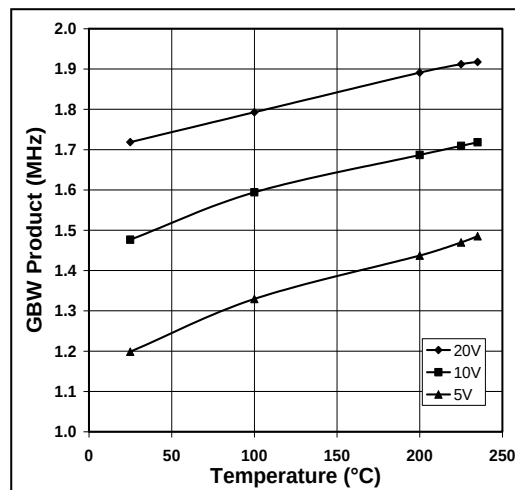
Parameter	Condition	Min	Typ	Max	Units
DC gain A_o	RL=2k Ω , $T_j=25^{\circ}\text{C}$	90	100		dB
	RL=2k Ω , $T_j=225^{\circ}\text{C}$	80	87		
Gain-bandwidth product GBW	RL=2k Ω , CL=30pF	<u>1.3</u>	<u>1.5</u>		MHz
Common mode rejection ratio CMRR	DC to 1kHz	<u>86</u>			dB
Power supply rejection ratio PSRR	Positive or negative. DC to 100Hz	<u>78</u>			dB
Slew rate SR	RL=2k Ω , CL=30pF $T_j=25^{\circ}\text{C}$	1.0	1.2		V/ μsec
	RL=2k Ω , CL=30pF $T_j=225^{\circ}\text{C}$	1.6	1.7		
Phase margin Φ_M	RL=2k Ω , CL=30pF	<u>50</u>	<u>>60</u>		Degree
Input noise spectral density	F=1Hz		11.0		$\mu\text{V}/\sqrt{\text{Hz}}$
	F=100Hz		1.2		
	F=1kHz		0.43		
	F=10kHz		0.19		
Integrated input noise e_n	DC to 10Hz, $T_j=-55$ to 225°C		25		μV_{RMS}



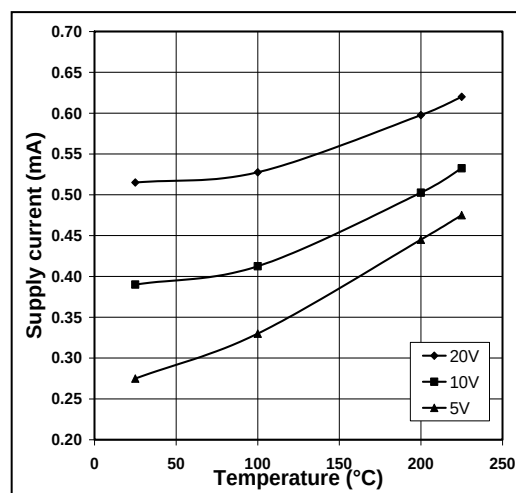
Typical Performance Characteristics



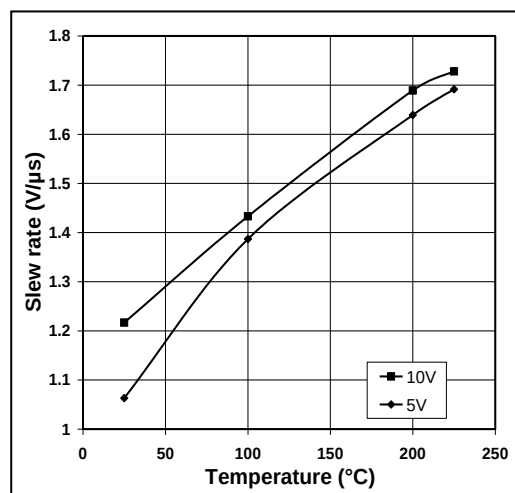
DC Gain vs. Temperature for V_{DD} = 5/10/20V



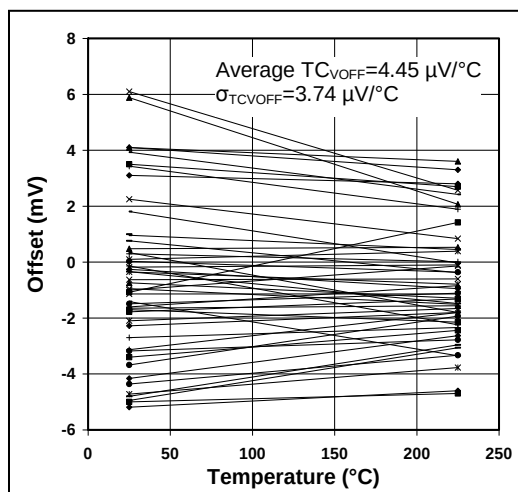
GBW vs. Temperature for V_{DD} = 5/10/20V



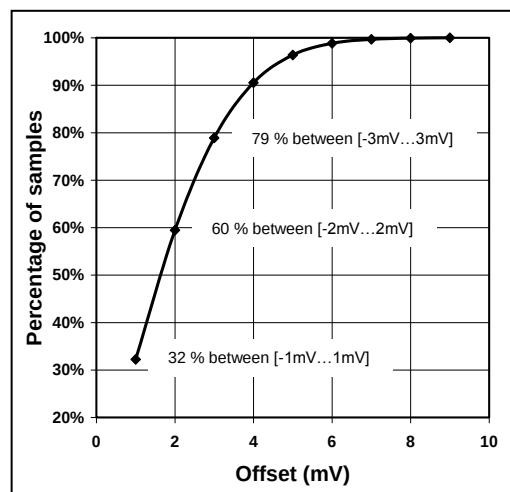
Current consumption per amplifier vs. Temperature for V_{DD} = 5/10/20V



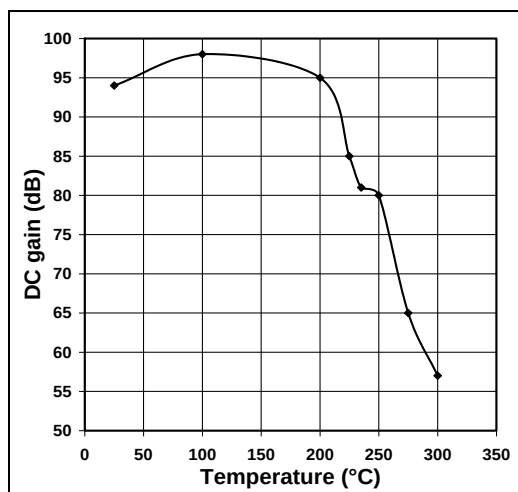
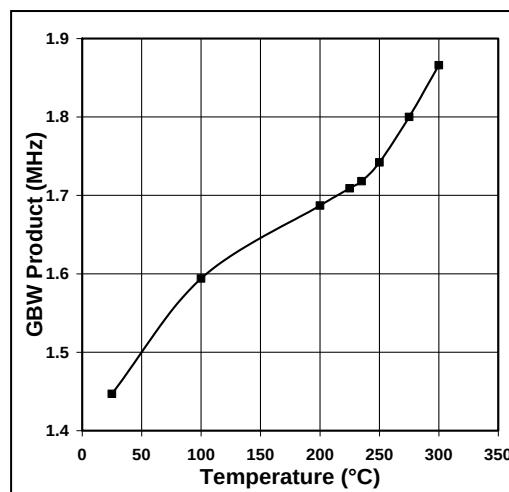
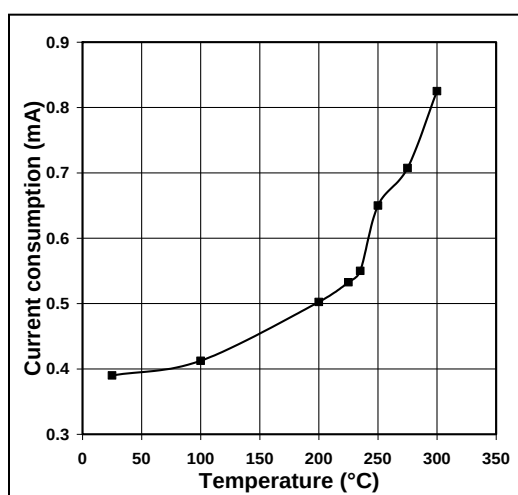
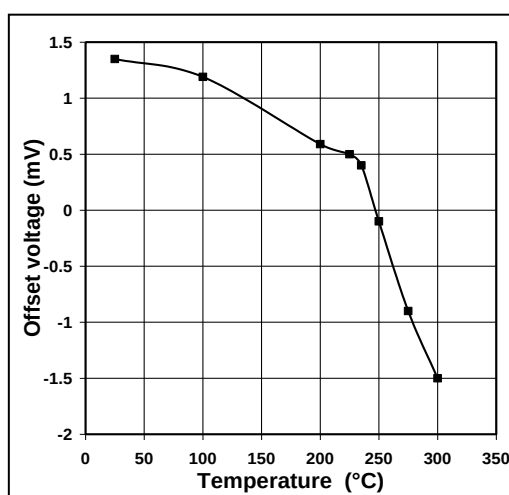
Slew Rate vs. Temperature for V_{DD} = 5/10V



Offset voltage vs. Temperature



Sample size vs. Offset voltage

DC Gain vs. Temperature for $V_{DD} = 10V$.
TypicalGBW vs. Temperature for $V_{DD} = 10V$.
TypicalCurrent consumption per aplifier vs. Tempera-
ture for $V_{DD} = 10V$. TypicalOffset voltage vs. Temperature.
Typical



Circuit Functionality

Operating conditions

The CHT-OPA has been qualified to operate with supply voltages ranging from 4.5V to 20V and temperatures from -55°C to 225°C. Device characteristics vary smoothly outside the qualification temperature range.

With supply voltage above 5.5V, the CHT-OPA must be used in closed loop configuration under linear regime. If the application fails to be compliant to this requirement, this will lead to violation of the "Safe Operating Area" conditions inside the CHT-OPA device.

For supply voltages below 5.5V, no limitation on the operation regime exists and the part can even be used as comparator. When CHT-OPA is used in non-linear regime (eg used as comparator), if the voltage difference between the 2 input pins exceeds 2V typ., current will flow between

the 2 input pins according to following formula:

$$I = \frac{V(INP) - V(INN) - 2V}{20K\Omega}$$

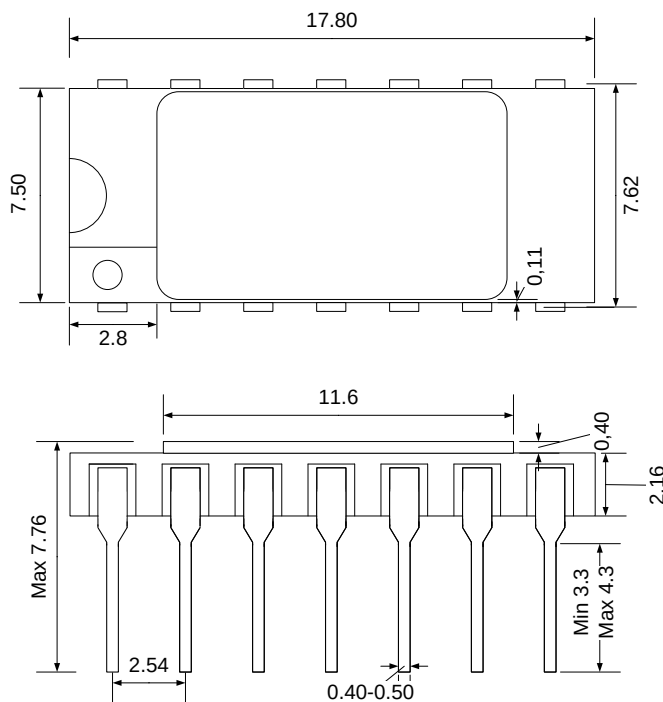
Specific Operating Conditions

The CHT-OPA presents slightly different positive and negative slewing values. This makes that when a square wave is used as input signal, the output presents an additional DC offset due to the slight change of the output duty cycle.

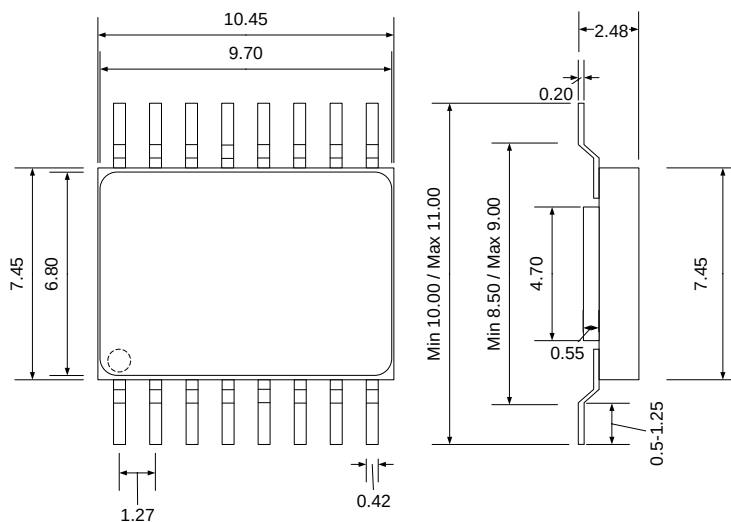
Additionally, for square input signals with frequencies above 10kHz, the circuit presents an output DC offset which increases with the input frequency. At 30kHz, the input referred offset increases by about 20mV.



Package Dimensions



Drawing CDIL14 (mm +/- 10%)



Drawing CSOIC16 (mm +/- 10%)

Ordering Information

Ordering Reference	Package	Temperature Range	Marking
CHT-OPA-CDIL14-T	Ceramic DIL14	-55°C to +225°C	CHT-OPA
CHT-OPA-CSOIC16-T	Ceramic SOIC16	-55°C to +225°C	CHT-OPA

Contact & Ordering

CISOID S.A.

Headquarters and contact EMEA:	CISOID S.A. – Rue Francqui, 3 – 1435 Mont Saint Guibert - Belgium T : +32 10 48 92 10 - F: +32 10 88 98 75 Email: sales@cissoid.com
Representatives	Visit our website: www.cissoid.com

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