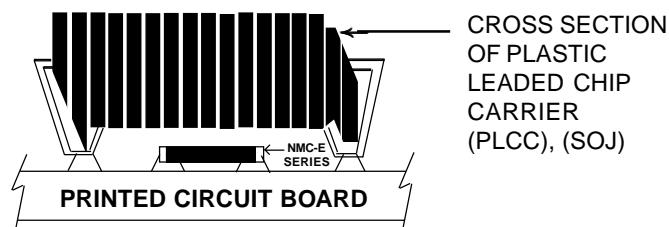


Low Profile Ceramic Chip Capacitors

NMC-E Series

FEATURES

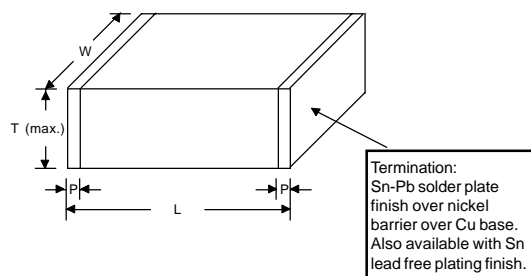
- Z5U AND Y5V DIELECTRIC DECOUPLING CAPACITORS FOR SURFACE MOUNT MEMORY AND LOGIC CIRCUITS
- LOW PROFILE IN STANDARD 0805, 1206 AND 1210 FOOTPRINT
- DESIGNED TO BE MOUNTED BENEATH A PLASTIC SURFACE LEADED CHIP CARRIER (PLCC)



SURFACE MOUNT

SPECIFICATIONS Z5U

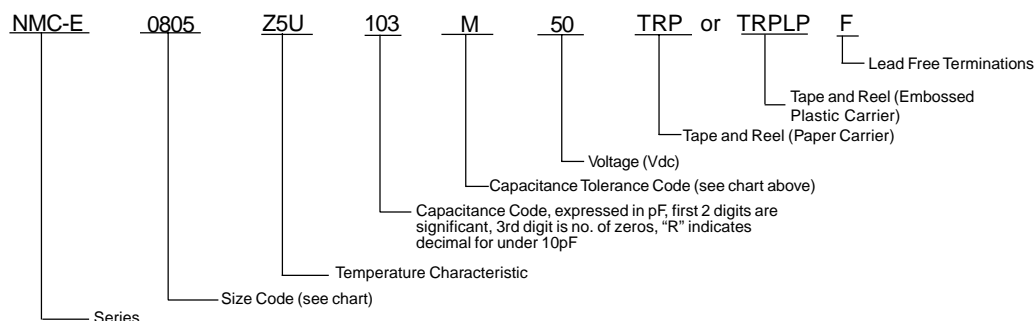
SPECIFICATIONS		Z5U	Y5V
Operating Temperature Range		+10°C to +85°C	-30°C to +85°C
Temperature Characteristics (ΔC max.)		+22% to -56%	+22% to -82%
Dissipation Factor		4% max.	5% max.
Insulation Resistance		1,000 Megohm Min. @ 25°C	
Capacitance Range	0805	.1 μ F	.1 μ F
	1206	.1 μ F, .22 μ F	--
	1210	.1 μ F, .22 μ F, .33 μ F	--
Capacitance Tolerance		$\pm 20\%$ (M)	+80%-20%(Z)
Rated Voltage		25 Vdc & 50 vdc	50 Vdc
Dielectric Withstanding Voltage		150% of Rated Voltage for 5 + 1.0 Seconds, 50 Milliamps Maximum	
Test Conditions		1Khz, 0.5V +0.1Vrms	



DIMENSIONS (mm)

Dimension	0805	1206	1210
Length (L)	2.0 \pm 0.2	3.2 \pm 0.2	3.2 \pm 0.2
Width (W)	1.25 \pm 0.2	1.6 \pm 0.2	2.5 \pm 0.2
Thickness (T)	Z5U=0.79 max. Y5V=0.66 max.	0.66 max.	0.66 max.
Overlap (P)	0.5 \pm 0.25	0.5 \pm 0.25	0.5 \pm 0.25

PART NUMBERING SYSTEM



NIC COMPONENTS CORPORATION

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