

MATERIAL (INSULATOR/CONTACT)
N = NYLON 46 / PHOSPHOR BRONZE
OPERATING TEMPERATURE: -10°C TO +85°C
PROCESSING TEMPERATURE: WAVE ONLY

G = NYLON 9T / PHOSPHOR BRONZE
OPERATING TEMPERATURE: -10°C TO +85°C
*PROCESSING TEMPERATURE: 260°C FOR 5 SEC MAX

PLATING
(ALL PLATINGS ARE LEAD FREE AND HAVE 50µ" NICKEL UNDERPLATE)
CONTACT SURFACE TERMINATION
W = GOLD FLASH .000100" PURE TIN, MATTE
B = .000010" GOLD .000100" PURE TIN, MATTE
C = .000030" GOLD .000100" PURE TIN, MATTE

PART NUMBER CODING

E DHRN-T94

POSITIONS

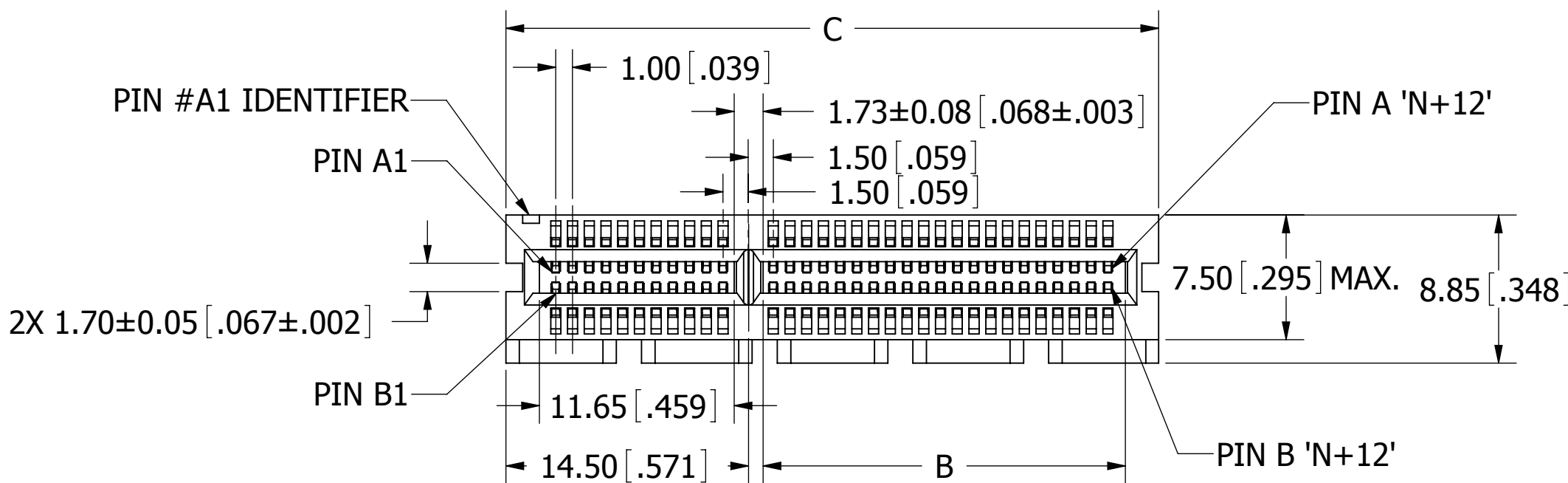
18
32
49
82

COLOR

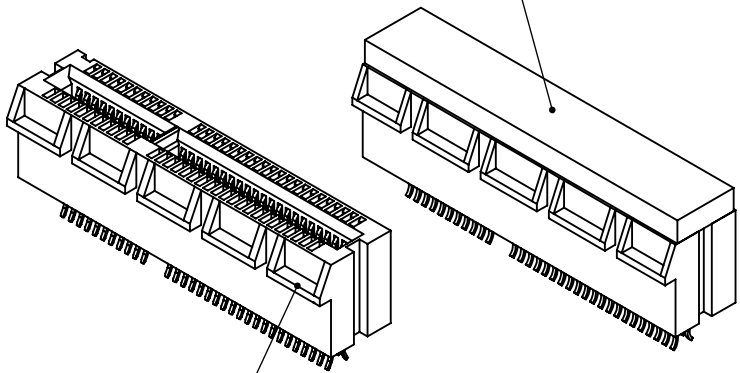
1= BLACK
6= WHITE

REVISIONS

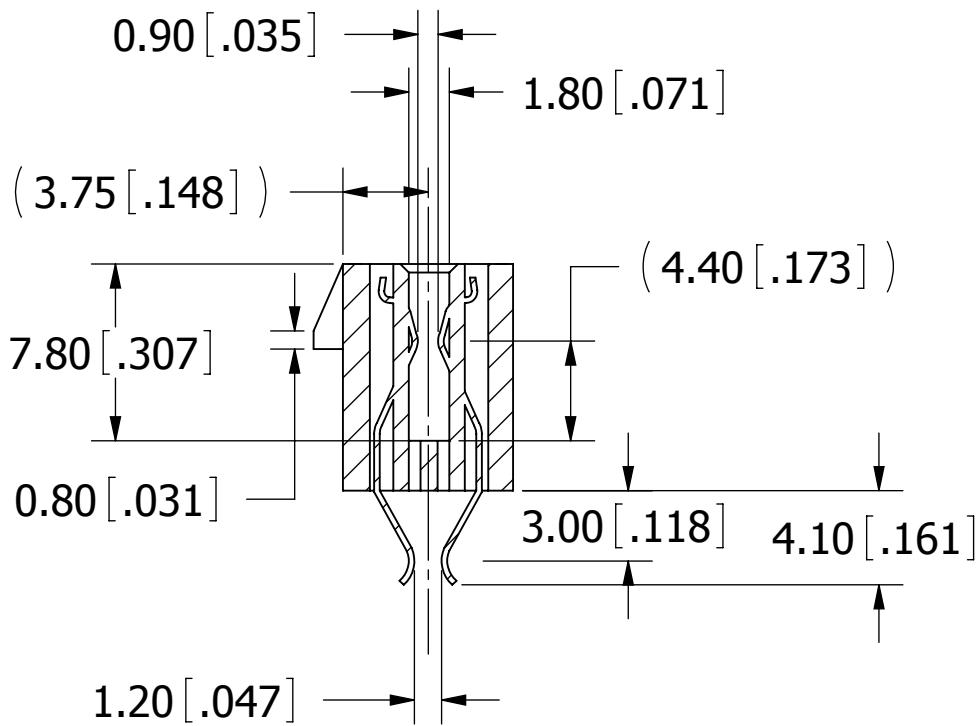
REV.	ECO. NO	DESCRIPTION	DATE	BY
A	1595	INITIAL RELEASE	04/01/08	VJ
B	1707	UPDATE P/N CODING CHART, PLATING	05/30/08	ASK
C	1946	UPDATE NOTES, TOLERANCE BLOCK, COLOR CODES	07/08/09	JH
D	2252	ADD INSULATOR MATERIAL NYLON 9T & ISO VIEW WITH CAP, UPDATE NOTES, TOLERANCE TABLE & TEMP SPEC.	03/10/2011	LH
E	2536	UPDATE PROCESSING TEMP FOR "N" MTL	3/30/2012	JH



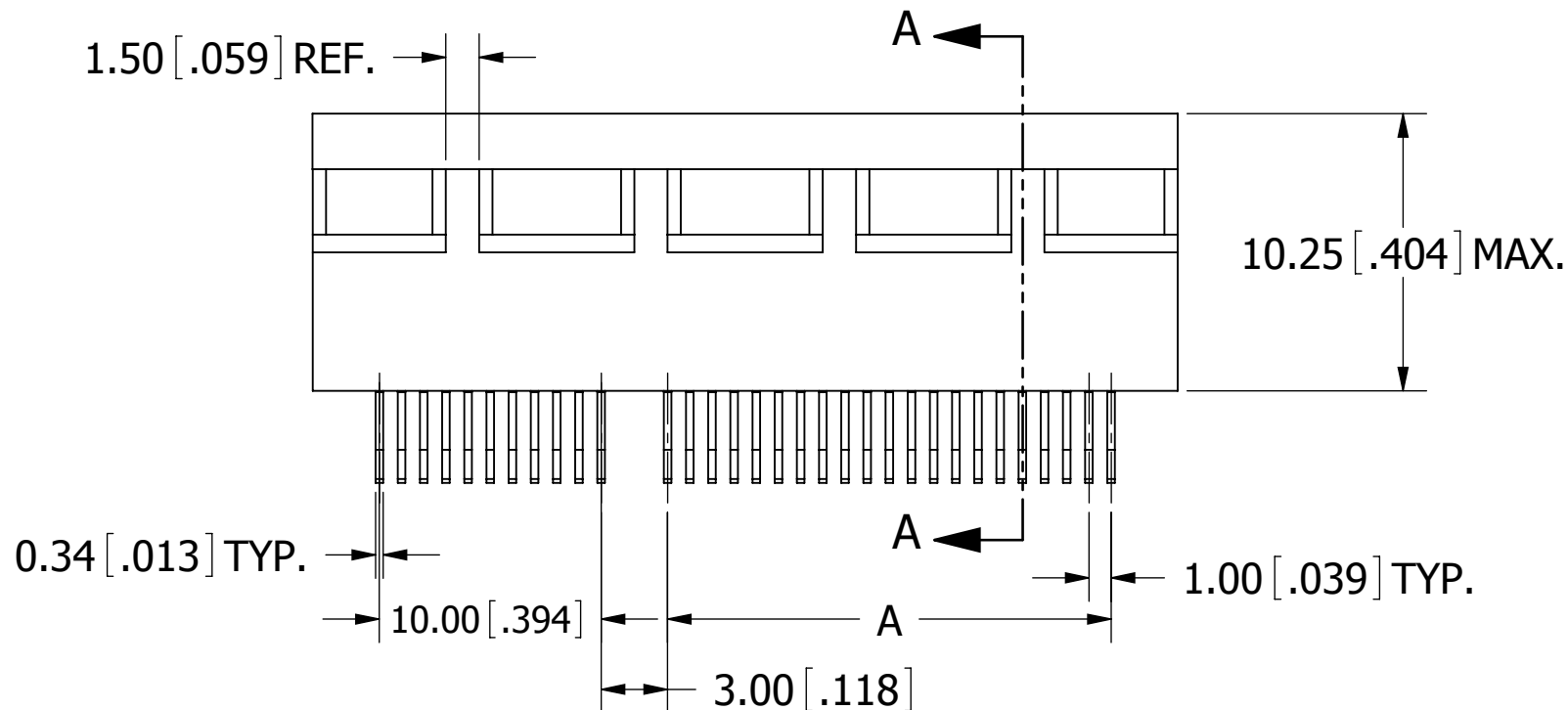
G_E_ _DHRN-T94_ WITH CAP
(SEE NOTE 14)



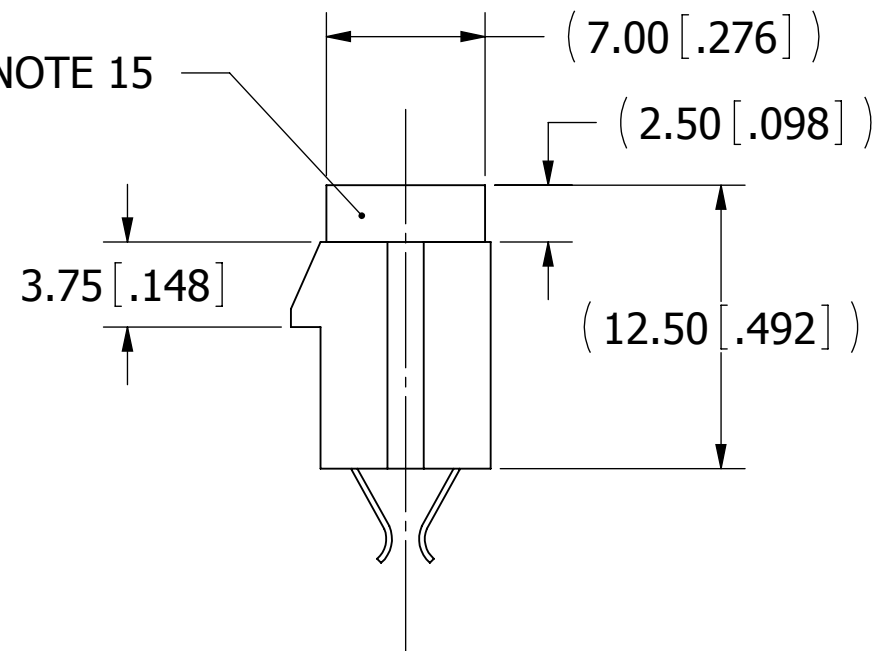
N_E_ _DHRN-T94_



SECTION A-A



SEE NOTE 15

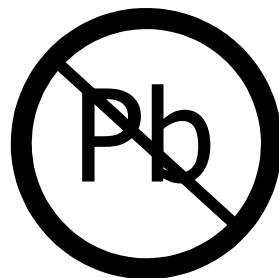


NOTES:

1. INSULATOR MATERIAL: SEE PART NUMBER CODING, UL 94V-0.
2. CONTACT MATERIAL: SEE PART NUMBER CODING.
3. CONTACT PLATING: SEE PART NUMBER CODING.
4. CURRENT RATING: 1.1 AMP PER CONTACT.
5. VOLTAGE RATING: 50 V AC/DC.
6. INSULATOR RESISTANCE: 1000 MEGOHMS MIN.
7. CONTACT RESISTANCE: 30 MILLI OHMS MAX.
8. DIELECTRIC WITHSTANDING: 300V AC.
9. OPERATING TEMP.: SEE PART NUMBER CODING.
10. *PROCESSING TEMP.: SEE PART NUMBER CODING.
11. BOARD THICKNESS ACCOMMODATED: 1.57mm±0.20[.062±.008]
12. INSERTION FORCE: 117.3g(1.173N) MAX PER CONTACT PAIR WHEN USING A 1.70mm[.067"] TEST BLADE.
13. WITHDRAWAL FORCE: 15.31g(0.153N) MIN PER CONTACT PAIR USING A 1.44mm[.057"] PCB.
14. PARTS WITH 'G' MATERIAL CODE HAVE CAPS, SUITABLE FOR IR REFLOW PROCESS.
15. CAP DIMENSIONS APPLICABLE FOR PARTS WITH 'G' MATERIAL CODE ONLY.

*(INDICATED TEMPERATURE AND TIME IS FOR COMPONENT INSULATOR. HIGHER PROCESSING TEMPERATURE MAY BE USED, PROVIDED HEAT IS APPLIED FROM BACK SIDE OF PCB, AND INSULATOR DOES NOT EXCEED INDICATED TEMPERATURE AND TIME.)

PART NUMBER	POSITIONS/ CONTACTS	A		B		C		D		E		G		H		SPACES 'N'	PIN "K"
		MM	INCH	MM	INCH	MM	INCH	MM	INCH	MM	INCH	MM	INCH	MM	INCH		
E18DHRN-T94	18/36	6.00	0.236	7.65	0.301	25.00	0.984	23.00	0.906	9.50	0.374	8.15	0.321	6.00	0.236	6	B17
E32DHRN-T94	32/64	20.00	0.787	21.7	0.852	39.00	1.535	37.00	1.457	23.50	0.925	22.15	0.872	20.00	0.787	20	B31
E49DHRN-T94	49/98	37.00	1.457	38.7	1.522	56.00	2.205	54.00	2.126	40.50	1.594	39.15	1.541	37.00	1.457	37	B48
E82DHRN-T94	82/164	70.00	2.756	71.7	2.821	89.00	3.504	87.00	3.425	73.50	2.894	72.15	2.841	70.00	2.756	70	B81



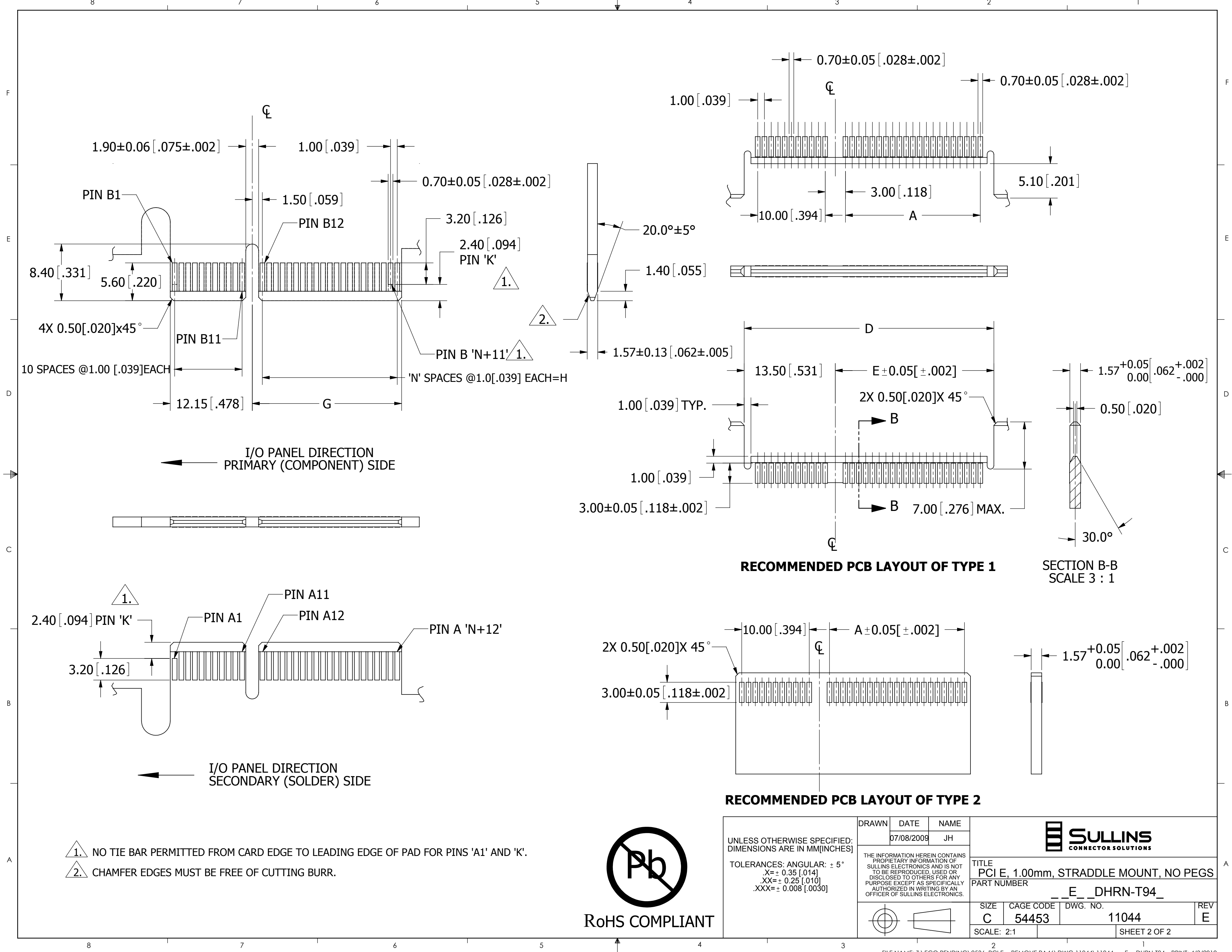
RoHS COMPLIANT

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN MM[INCHES]

TOLERANCES: ANGULAR: ± 3°
.X=± .35 [.014]
.XX=± .25 [.010]
.XXX=± .080 [.0030]

DRAWN	DATE	NAME
	07/08/2009	JH
THE INFORMATION HEREIN CONTAINS PROPRIETARY INFORMATION OF SULLINS ELECTRONICS AND IS NOT TO BE REPRODUCED, USED OR DISCLOSED TO OTHERS FOR ANY PURPOSE EXCEPT AS SPECIFICALLY AUTHORIZED IN WRITING BY AN OFFICER OF SULLINS ELECTRONICS.		

TITLE PCI E, 1.00mm, STRADDLE MOUNT, NO PEGS		
PART NUMBER _E_ _DHRN-T94_		
SIZE C	CAGE CODE 54453	REV E
SCALE: 3:1	DWG. NO. 11044	SHEET 1 OF 2



1. NO TIE BAR PERMITTED FROM CARD EDGE TO LEADING EDGE OF PAD FOR PINS 'A1' AND 'K'.
2. CHAMFER EDGES MUST BE FREE OF CUTTING BURR.



UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MM[INCHES]	DRAWN	DATE	NAME		
		07/08/2009	JH		
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				PART NUMBER _E_DHRN-T94_	
		SIZE C	CAGE CODE 54453	DWG. NO. 11044	REV E
		SCALE: 2:1		SHEET 2 OF 2	