

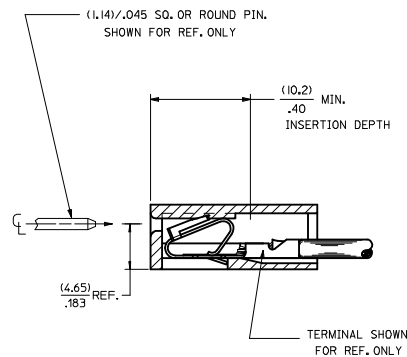


Diagram illustrating the layout of a 64Kbit memory array. The array is organized into 8 columns and 8 rows. The columns are labeled 6442, 6441, 6440, 6439, 6438, 6437, 6436, and MOLEX from right to left. The rows are labeled 17, 16, 15, 14, 13, 12, 11, and 10 from top to bottom. A dimension line on the left indicates a height of (17,0) / .67.

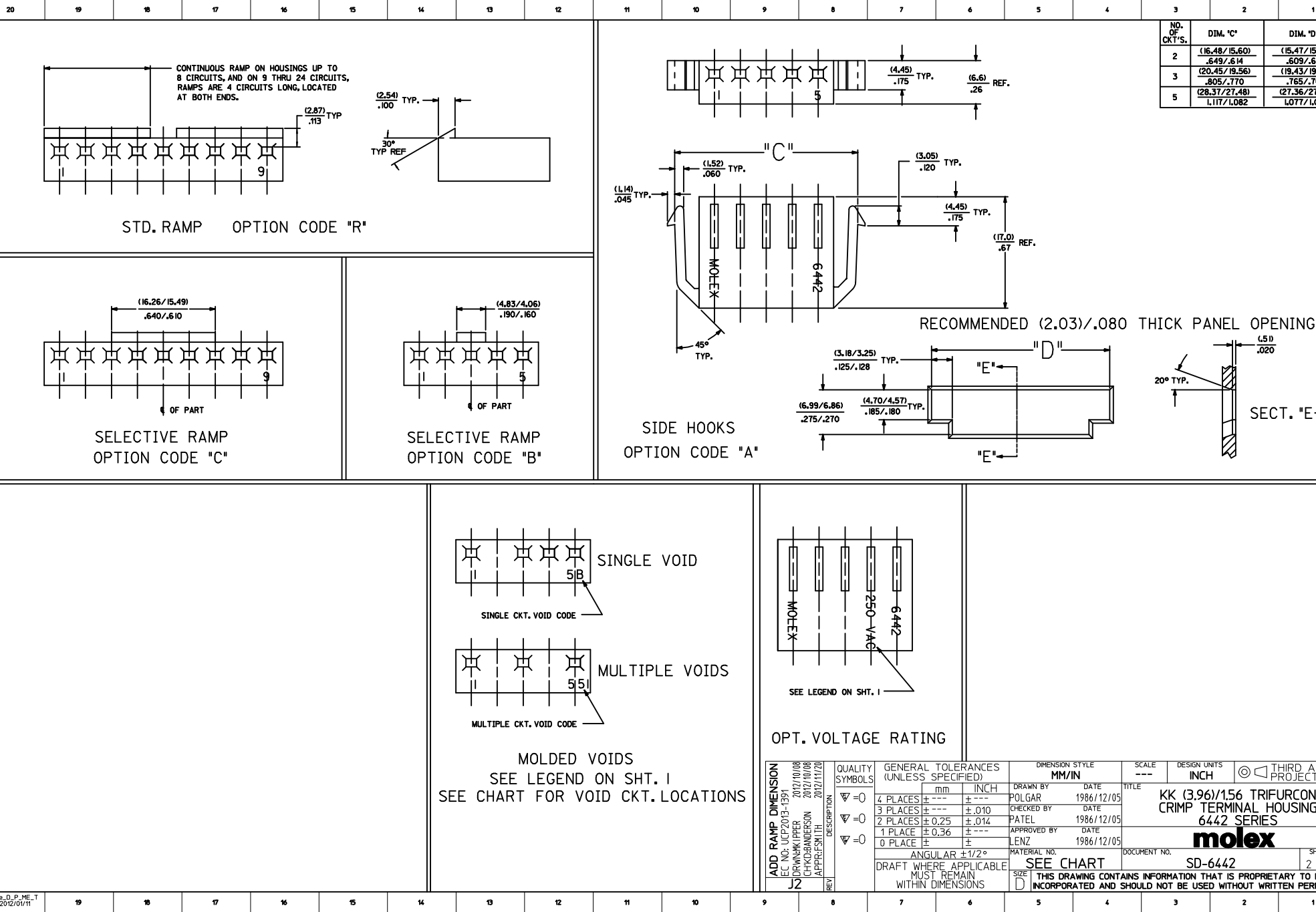
CODE	HOOKS	SIDE HOOKS	RAMP
BLANK	NONE	NONE	NONE
A	NONE	YES	NONE
R	NONE	NONE	FRICTION
B	NONE	NONE	TYPE B
C	NONE	NONE	TYPE C

6442- * N - * - * - *

— MOLDED VOID CODE
Z=NO VOIDS
A=CKT. 1 VOID,
B=CKT. 2 VOID, ETC.
51=1ST MULTIPLE VOID,
52=2ND MULTIPLE VOID, ETC.

NO. OF CKT'S.

4	J
3	J
2	J2
1	J3
SH	REV



lb_frame_D_P_ME_T
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