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ADC1410S125F1 Obsolete

ADC1410S125F1 Demo Board; CMOS version

IDTs ADC1410S125F1 demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance

Features

- Single power supply (on-board regulators)
- SMA connector for clock and analog input signals
- CMOS (buffered outputs)

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ADC1410S125F1-DB	Obsolete	BD0	BOARD	0	I	No	Availability

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Demo Board Software ADC_1_r04	ZIP	2.33 MB	Jul 18, 2012

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ADC1410S125F1 - Evaluation Board

