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NCP5425SOEVB:NCP5425SO Buck Controller Evaluation Board

Evaluation Board Description

The NCP5425 is a highly flexible dual buck controller with internal gate drivers that can be used with two input power supplies and one or two outputs in multiple configurations. The part contains all the circuitry required for two independent synchronous dual NFET buck regulators utilizing a feed forward voltage mode control method. The NCP5425 can run from a single supply ranging from 4.6 to 12 volts and support a single two phase or dual single phase outputs. When used as a dual output controller, the second output tracks voltage transients from the first. Power blanking for low noise applications is supported as well as independent cycle -by-cycle current limiting. The part is available in a 20 pin TSSOP package allowing the designer to minimize PCB area.



Features and Applications

Features

- ⌵ Operation Over 4.6 to 13.2 Volts
- ⌵ Dual Synchronous Buck Design
- ⌵ Programmable Power Sharing and Budgeting from Two Independent Supplies
- ⌵ 0.8 Volt +/- 1% Reference for Low Voltage Outputs
- ⌵ 1.5A Peak Power Drive
- ⌵ Switch Blanking for Noise Sensitive Applications through use of ROSC Pin
- ⌵ Programmable Frequency, 150 kHz to 750 kHz Operation
- ⌵ Programmable Soft -Start
- ⌵ Cycle -to -Cycle Overcurrent Protection
- ⌵ Independent Programmable Current Limits
- ⌵ 100% Duty Cycle for Fast Transient Response
- ⌵ Internal Slope Compensation
- ⌵ Out -of -Phase Synchronization between the Controllers
- ⌵ Configurable as a Single Two Phase Output or Two Single Phase Outputs
- ⌵ Input Undervoltage Lockout
- ⌵ On/Off Enable through use of the COMP Pins
- ⌵ Power Supply Sequencing

Evaluation Board Information

Evaluation Board	Short Description	Status	Parts Used	Action
NCP5425SOEVB	NCP5425SO Buck Controller Evaluation Board	Active	NCP5425DBG	Contact Local Sales Office

Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	NCP5425SOEVB BILL OF MATERIALS	NCP5425SOEVB_BOM - 84.0 KB	0
Eval Board: Gerber	NCP5425SOEVB GERBER LAYOUT FILES (ZIP FORMAT)	NCP5425SOEVB_GERBER - 85.0 KB	0
Eval Board: Schematic	NCP5425SOEVB SCHEMATIC	NCP5425SOEVB_SCHEMATIC - 42.0 KB	0
Eval Board: Test Procedure	NCP5425SOEVB TEST PROCEDURE	NCP5425SOEVB_TEST_PROCEDURE - 538.0 KB	0

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