

EMK23H2H-62.208M TR

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REGULATORY COMPLIANCE (Data Sheet downloaded on Sep 26, 2016)


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ITEM DESCRIPTION

MEMS Clock Oscillators LVCMOS (CMOS) 3.3Vdc 4 Pad 3.2mm x 5.0mm Plastic Surface Mount (SMD) 62.208MHz ± 50 ppm over -40°C to +85°C

ELECTRICAL SPECIFICATIONS

Nominal Frequency	62.208MHz
Frequency Tolerance/Stability	± 50 ppm Maximum over -40°C to +85°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, 260°C Reflow, Shock, and Vibration)
Aging at 25°C	± 1 ppm Maximum First Year
Supply Voltage	3.3Vdc $\pm 10\%$
Input Current	25mA Maximum
Output Voltage Logic High (Voh)	90% of Vdd Minimum (IOH=-8mA)
Output Voltage Logic Low (Vol)	10% of Vdd Maximum (IOL=+8mA)
Rise/Fall Time	2nSec Maximum (Measured from 20% to 80% of waveform)
Duty Cycle	50 $\pm 5\%$ (Measured at 50% of waveform)
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Output Control Function	Tri-State (Disabled Output: High Impedance)
Output Control Input Voltage	+0.7Vdd Minimum or No Connect to Enable Output, +0.3Vdd Maximum to Disable Output
Peak to Peak Jitter (tPK)	250pSec Maximum, 100pSec Typical
Start Up Time	50mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 2, HBM 2000V
Flammability	UL94-V0
Mechanical Shock	MIL-STD-883, Method 2002, Condition G, 30,000G
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity Level	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003 (Pads on bottom of package only)
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Thermal Shock	MIL-STD-883, Method 1011, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A, 20G

Technical drawing of a rectangular component showing three views: front, side, and top.

Front View: A rectangle with a width of 3.20 ± 0.15 and a height of 5.00 ± 0.15 . A small circular feature is located in the bottom right corner.

Side View: A narrow rectangle with a thickness of 0.85 ± 0.15 and a maximum width of 0.08 MAX .

Top View: A rectangle divided into four quadrants by a vertical and a horizontal centerline. The quadrants are numbered 1, 2, 3, and 4. The dimensions are as follows:

- Overall width: $1.20 \pm 0.10 \text{ (x4)}$
- Overall height: 2.39 ± 0.10
- Distance from left edge to centerline: 0.80 ± 0.15
- Distance from top edge to centerline: $1.15 \pm 0.10 \text{ (x4)}$
- Radius of circular features: $R0.58 \pm 0.10$
- Corner radius: $C0.35 \pm 0.10$

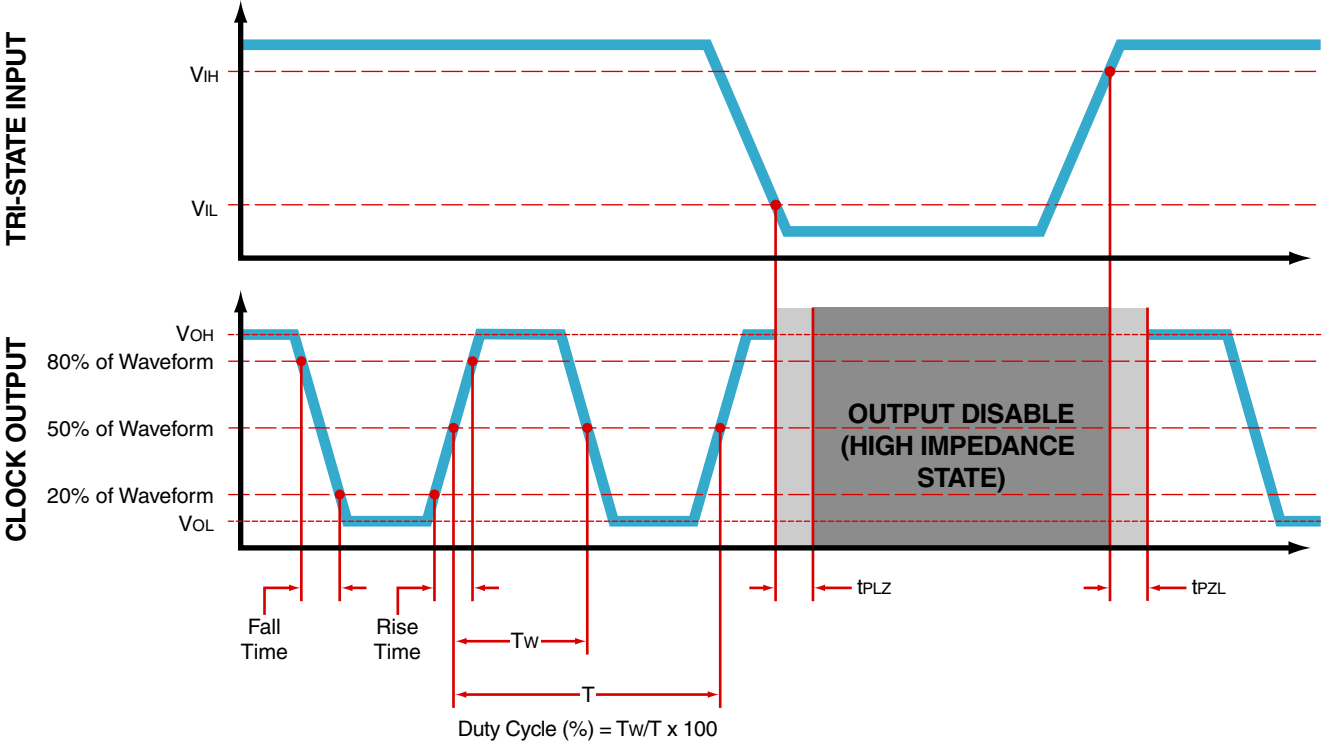
LINE	MARKING
1	XXXX or XXXXX <i>XXXX or XXXXX=Ecliptek Manufacturing Lot Code</i>

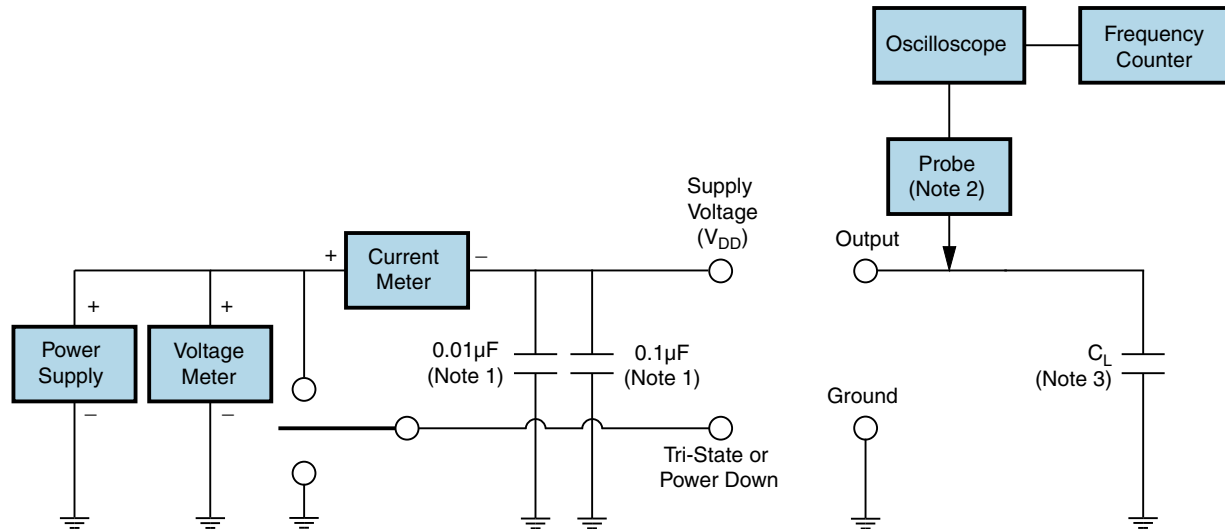
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OUTPUT WAVEFORM & TIMING DIAGRAM



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Test Circuit for CMOS Output

Note 1: An external $0.01\mu\text{F}$ ceramic bypass capacitor in parallel with a $0.1\mu\text{F}$ high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low input capacitance ($<12\text{pF}$), 10X Attenuation Factor, High Impedance ($>10\text{Mohms}$), and High bandwidth ($>300\text{MHz}$) passive probe is recommended.

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance. See applicable specification sheet for 'Load Drive Capability'.

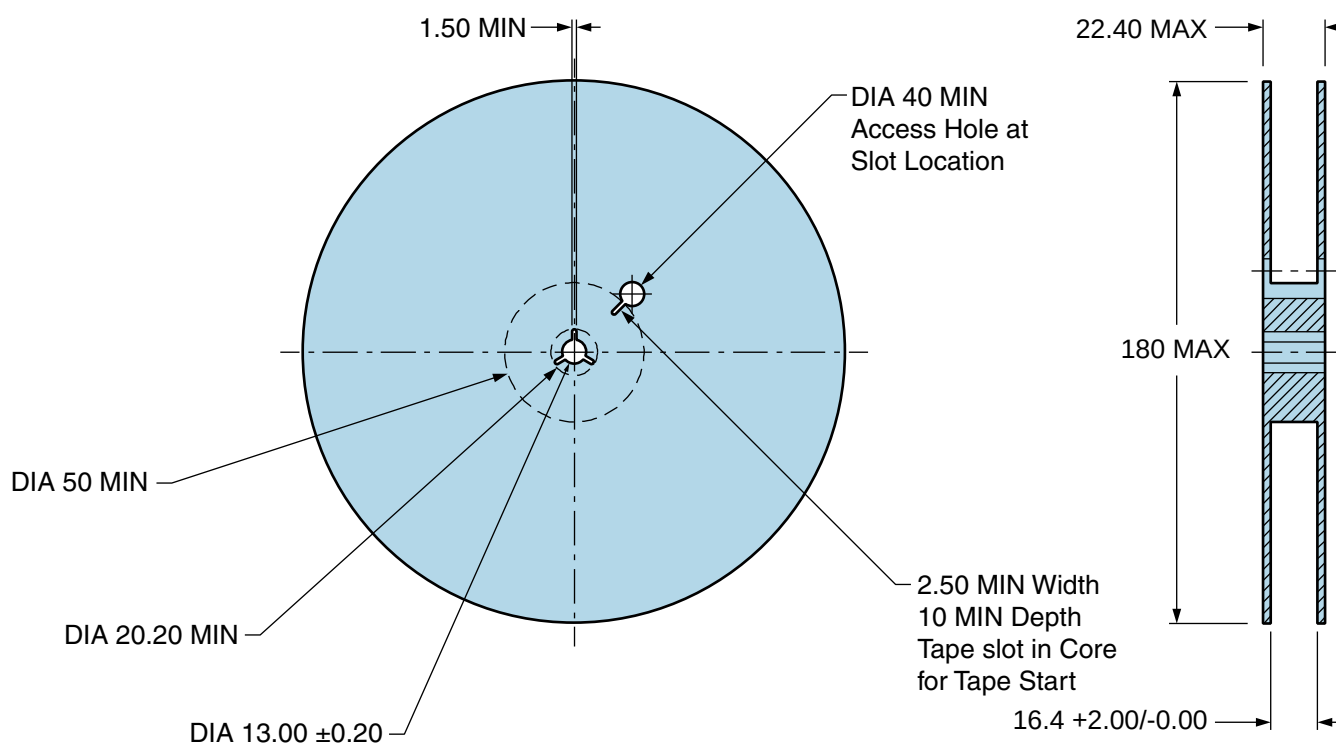
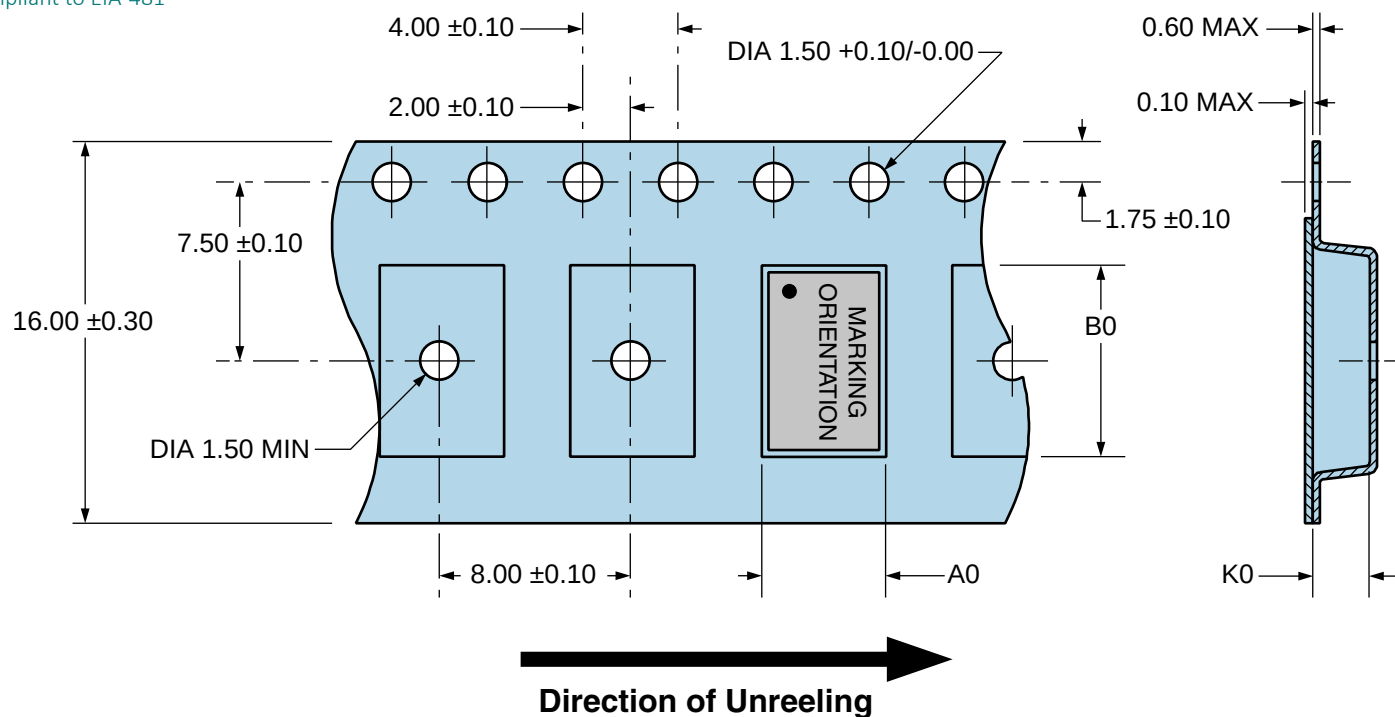
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Tape & Reel Dimensions

Quantity Per Reel: 1,000 units

All Dimensions in Millimeters

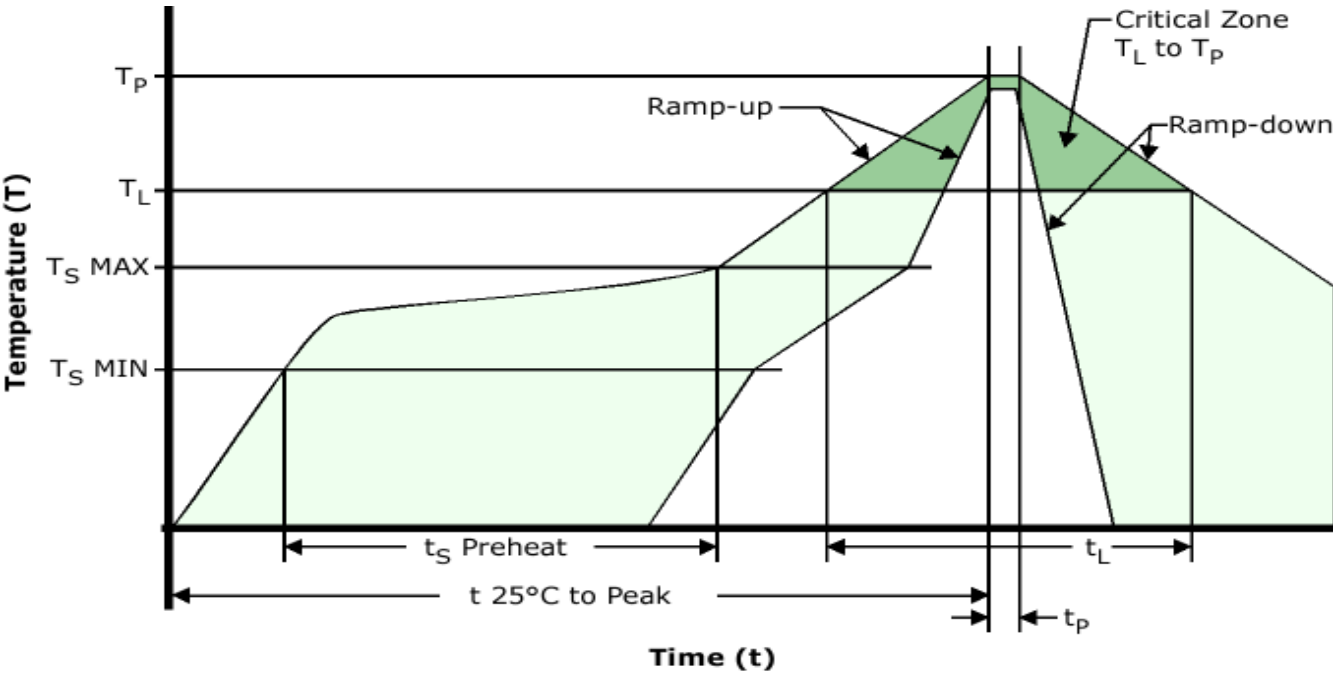
Compliant to EIA-481



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Recommended Solder Reflow Methods



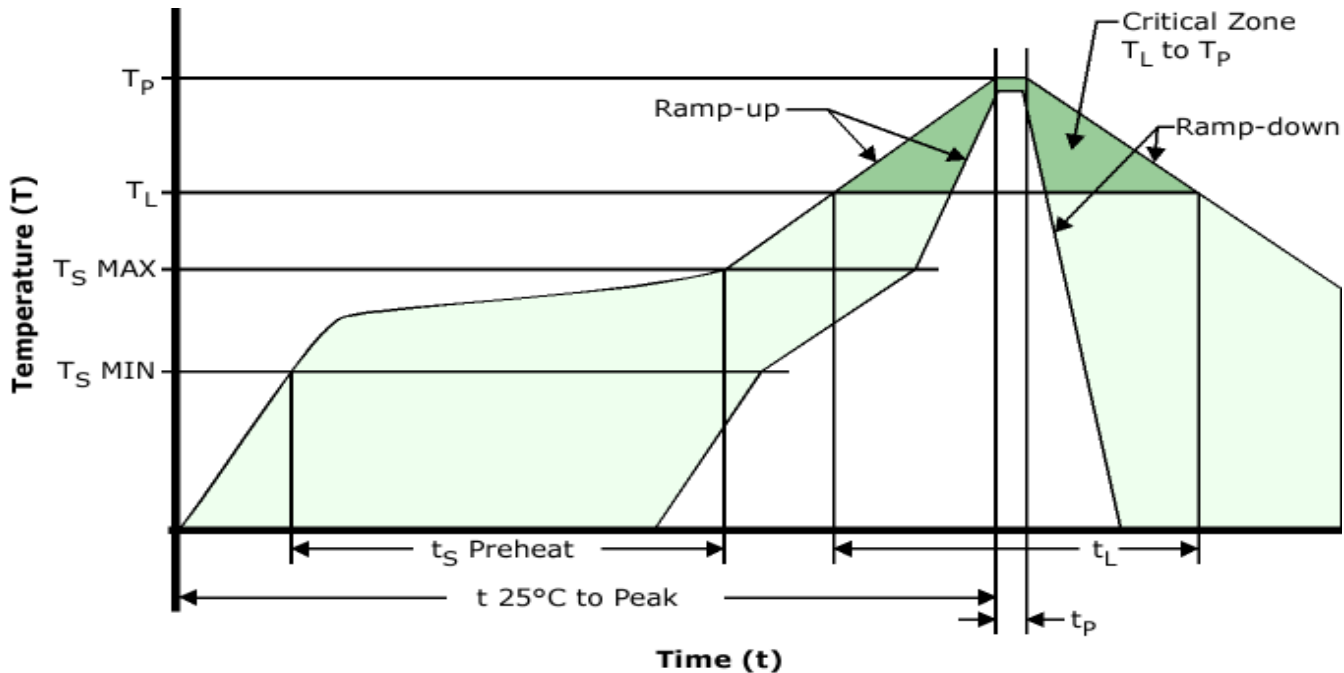
High Temperature Infrared/Convection

Ts MAX to Tl (Ramp-up Rate)	3°C/Second Maximum
Preheat	
- Temperature Minimum (Ts MIN)	150°C
- Temperature Typical (Ts TYP)	175°C
- Temperature Maximum (Ts MAX)	200°C
- Time (ts MIN)	60 - 180 Seconds
Ramp-up Rate (Tl to Tp)	3°C/Second Maximum
Time Maintained Above:	
- Temperature (Tl)	217°C
- Time (tL)	60 - 150 Seconds
Peak Temperature (Tp)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (Tp Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 Seconds
Ramp-down Rate	6°C/Second Maximum
Time 25°C to Peak Temperature (t)	8 Minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

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Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

TS MAX to TL (Ramp-up Rate)	5°C/Second Maximum
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Preheat

- Temperature Minimum (TS MIN)	N/A
- Temperature Typical (TS TYP)	150°C
- Temperature Maximum (TS MAX)	N/A
- Time (ts MIN)	60 - 120 Seconds

Ramp-up Rate (TL to TP)	5°C/Second Maximum
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Time Maintained Above:

- Temperature (TL)	150°C
- Time (tL)	200 Seconds Maximum

Peak Temperature (TP)	240°C Maximum
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Target Peak Temperature (TP Target)	240°C Maximum 2 Times / 230°C Maximum 1 Time
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Time within 5°C of actual peak (tp)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
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Ramp-down Rate	5°C/Second Maximum
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Time 25°C to Peak Temperature (t)	N/A
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Moisture Sensitivity Level	Level 1
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Additional Notes	Temperatures shown are applied to body of device.
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Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)