

# **Sound Processors for Home Theater System**

# 7.1ch Sound Processor



**BD3452KS** No.10081EAT01

### Description

BD3452KS is a sound processor where the functions including Input Selector, 8ch Volume and Gain Amp required for applications such as AV receivers, home theater systems and mini-component systems are integrated into a single chip. Adopting the BiCMOS process achieves low distortion, low noise and a wide dynamic range.

### Features

- 1) Dynamic range: 132dB (VOL=MUTE, IHF-A)
- 2) Independent 8 channels for Master Volume (0 to -99 dB, MUTE 1dB/Step)
- 3) Supporting 2nd room entertainment
- 4) Low current consumption design achieved by adopting the BiCMOS process
- 5) Built-in Output Gain Amp useful for adjusting output signal voltages (0 to 15dB, 1dB/Step)
- 6) BD3841FS (9-input selector), BD3843FS (6-input selector) and BUS are common to be controlled simultaneously.
- 7) Built-in 2ch output port
- 8) 2-wire serial control (For both 3.3V and 5V)

# Applications

AV receivers, home theater systems and mini-component systems

# ● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Power Supply Voltage	VCC VEE	7.5 <sup>*1</sup> -7.5	V
Input Signal Voltage	VIN	VCC+0.3 to VEE-0.3	V
Power Dissipation	Pd	1300 <sup>*2</sup>	mW
Operating Temperature Range	Topr	-20 to +75	°C
Storage Temperature Range	Tastg	-55 to +125	°C

<sup>\*1</sup> Even in the specified range of Power Supply Voltage, applying voltage only to the VCC side may cause an excessive current to give a permanent damage to the IC.

# Operating conditions

It must function normally at Ta=25°C

it must runction normally at 1a=25 0.											
Parameter	Symbol		Unit								
i alametei	Symbol	Min.	Тур.	Max.	Offic						
Operating Supply Voltage	VCC	6.5	7	7.3	W						
Operating Supply Voltage	VEE	-7.3	-7	-6.5	V						

When starting up power supplies, VEE and VCC should be powered on simultaneously or VEE first; then followed by VCC.

<sup>\*2</sup> Over Ta=25°C, reduce at the rate of 13mW/°C. When installed on the standard board (size: 70×70×1.6mm).

# Electrical characteristics

 $Ta=25^{\circ}C,\ VCC=7V,\ VEE=-7V,\ f=1kHz,\ Vin=1Vrms,\ RL=10k\Omega,\ Rg=600\Omega,\ Input\ Gain=0dB,$ 

Master volume=0dB, Output gain=0dB, unless otherwise noted.

Madio	er volume=0dB, Output gain=0		ub, unies	5 Olliei Wi	Limits			
	Paramete	r	Symbol	Min.	Typ.	Max.	Unit	Conditions
		VCC		-	20	40	_	
	Circuit Current VEE		IQ	-40	-20	-	mA	No signal
	Output Voltage Ga	iin	Gv	-2	0	2	dB	Measure : Pin87,88
	Total Harmonic Distortion Ratio		THD	-	0.0006	0.03	%	Measure : Pin87,88 BW=400 to 30kHz
<b>-</b>	Maximum Output '	Voltage	Vomax	3.6	4.2	-	Vrms	Measure : Pin87,88 THD=1%
Total Output	Output Noise Volta	age	Vno	-	1.4	12	μVrms	Measure : Pin87,88, Rg=0 $\Omega$ , BW=IHF-A
Tota	Residual Noise Vo	ltage	Vnor	-	1	8	μVrms	Measure : Pin87,88, Rg=0Ω, BW=IHF-A, Volume=MUTE
	Cross-talk between Channels	3	СТС	-	-95	-80	dB	Measure : Pin88(OUTFL),87 (OUTFR) Rg=0 $\Omega$ , BW=IHF-A Reference : Pin87(OUTFR), 88(OUTFL)=1Vrms
	Cross-talk between Selectors	3	CTS	-	-95	-80	dB	Measure : Pin87,88 Rg=0Ω, BW=IHF-A
	Input Impedance	Rin	32	47	62	kΩ		
	V Output Voltage Gain		GVV	-2	0	2	dB	Measure : Pin 81,82,83,84,85,86,87,88
tput	V Total Harmonic Distortion Ratio		THDV	-	0.0006	0.03	%	Measure : Pin 81,82,83,84,85,86,87,88 BW=400 to 30kHz
Volume Output	V Residual Noise	Voltage	VnorV	-	1	8	μVrms	Measure : Pin 81,82,83,84,85,86,87,88 BW=IHF-A, Rg=0Ω,Volume=MUTE
Volu	Volume Setting Er	ror	VOLE1	-0.5	0	0.5	dB	Measure : Pin 81,82,83,84,85,86,87,88 Volume=0dB, Vin=3Vrms
	Maximum Attenua	tion	VOLmin	-	-115	-105	dB	Measure : Pin 81,82,83,84,85,86,87,88 Vin=3Vrms, BW=IHF-A
Input Gain	Input Gain Control	Range	GIG	10	12	14	dB	Measure : Pin 81,82,83,84,85,86,87,88 Input Gain=12dB, Vin=0.3Vrms
Output Gain	Output Gain Contr	ol Range	GOG	13	15	17	dB	Measure : Pin 81,82,83,84,85,86,87,88 Output Gain=15dB, Vin=0.3Vrms
Outpu	Output Gain Settir	g Error	GOE	-0.5	0	0.5	dB	Measure : Pin 81,82,83,84,85,86,87,88 Output Gain=0dB, Vin=0.3Vrms
+	R Output Impedan	ce	RoutR	-	20	100	Ω	Measure : Pin 44,45,46,47
REC Out	R Voltage Gain  R Total Harmonic Distortion Ratio		GVR	-2	0	2	dB	Measure : Pin 44,45,46,47 (*)RL=10kΩ
			THDR	-	0.005	0.09	%	Measure : Pin 44,45,46,47 BW=400 to 30kHz, (*)RL=10kΩ
Port	Port H Output		PH	4.0	4.9	5.4	V	Measure : Pin 62,63 RL=10kΩ
Pc	Port Output Curre	nt	PI	-	-	1.0	mA	Measure : Pin 62,63

<sup>(\*)</sup> If two RECOUTs are ON, total load resistances of these two (RL) should be 10 k $\Omega$ .

This product is not of "anti radiation design".

# ●Timing chart

- 1) Signal Timing Conditions
  - Data is read on the rising edge of the clock.
  - · Latch is read on the falling edge of the clock.
  - · Latch signal must terminate with the LOW state.
  - \*To avoid malfunctions, clock and data signals must terminate with the LOW state.

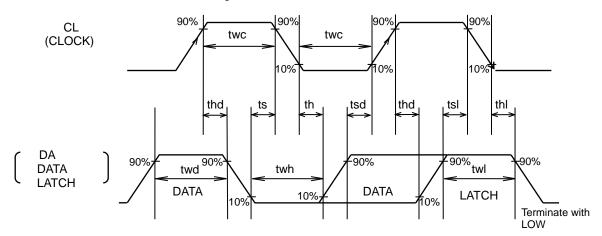


Fig.1

Parameter	Symbol			Unit	
Parameter	Symbol	Min.	Тур.	Max.	Unit
Minimum Clock Width	twc	1.0	-	-	μs
Minimum Data Width	twd	1.0	-	-	μs
Minimum Latch Width	twl	1.0	-	-	μs
LOW Hold Width	twh	1.0	-	-	μs
Data Set-up Time (DATA→CLK)	tsd	0.5	-	-	μs
Data Hold Time (CLK→DATA)	thd	0.5	-	-	μs
Latch Set-up Time (CLK→LATCH)	tsl	0.5	-	-	μs
Latch Hold Time (DATA→LATCH)	thl	0.5	-	-	μs
Latch Low Set-up Time	ts	0.5	-	-	μs
Latch Low Hold Time	th	0.5	-	-	μs

2) Voltage Conditions for Control Signals

Doromotor	_	Limits		Unit	Conditions		
Parameter	Min.	Тур.	Max.	Unit	Conditions		
"H" Input Voltage	2.2	-	5.5	V	Vcc = 6.5∼7.3V		
"L" Input Voltage	0	_	1.0	V	VEE=-6.5~-7.3V		

3) Basic Configuration of Control Data Formats

$\overline{}$	Inpu	t direct	ion														
	MSB																LSB
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Dala							Da	ata							Sele	ct Addı	ress

• Con		ol Data Formats Input direction												Select Address			
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(1)	I	nput se	lector	1	Input selector 2				Input ATT			Input gain		*	0	0	0
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(2)		LR Inpo Selecto			BLR Inp Selecto		Multi Sele		REC A	REC B	Port A	Port B	*	0	0	0	1
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(3)				:h	Output gain SWch			*	*	*	*	*	1	0	0	1	
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(4)	ı	Master	volume	е	FLch			Master volume				FRch			0	1	0
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(5)	ı	Master	volume	е		Cch			Master volume				SWch		0	1	1
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(6)	···			SLch			Master	volume	Э	SRch			1	1	0		
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(7)						SBLch			Master volume				SBRch			1	1

By changing the setting of Select Address, seven different control formats are selectable.

For Select Address, the values except those shown above must not be specified.

Each time of power-on, all of the address data must be initialized.

# (Example)

← Input direction

MSB	LS	SB	MSB I	LSB	MSB	LSB	MSB L	SB	MSB L	SB	MSB I	LSB	MSB L	SB
Da	ta(1)	L	Data(2)	L	Data(3	3) L	Data(4)	L	Data(5)	L	Data(6)	L	Data(7)	L

<sup>&</sup>quot;L" means latch.

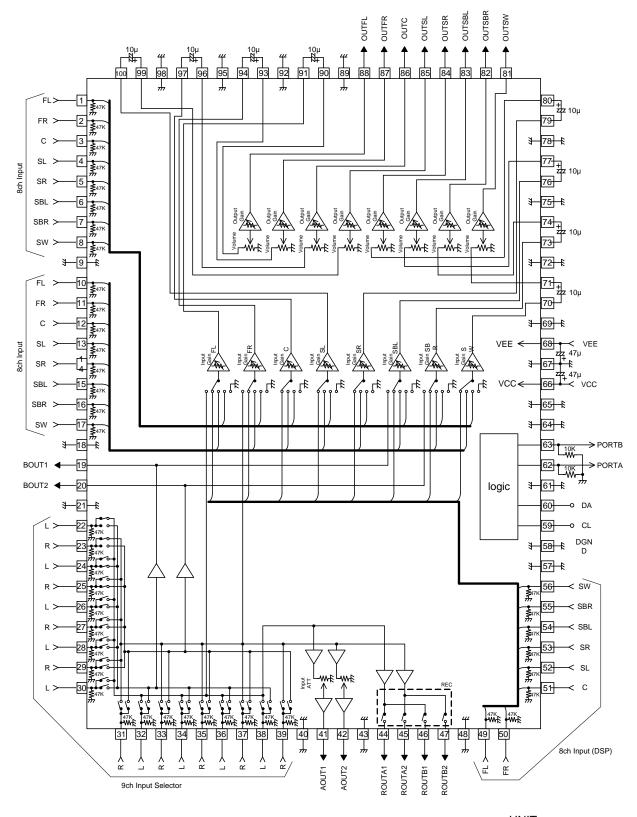
After power-on, for the second and subsequent times, only the desired data can be selected for setting.

(Example) When changing Output Gain SWch,Input direction



<sup>\*</sup> indicates 0 or 1.

# Application circuit



UNIT RESISTOR :  $\Omega$  CAPACITOR : F

Fig.2

### Reference data

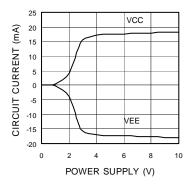


Fig.3 Circuit Current - Power Supply

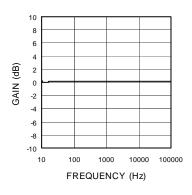


Fig.4 Voltage Gain - Frequency

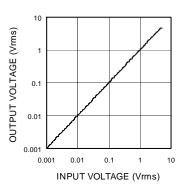


Fig.5 Output Voltage - Input Voltage

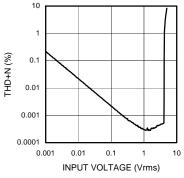


Fig.6 THD+N - Input Voltage

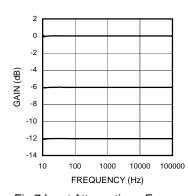


Fig.7 Input Attenuation - Frequency

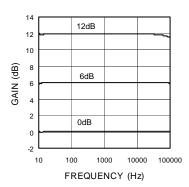


Fig.8 Input Gain - Frequency

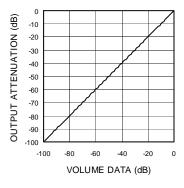


Fig.9 Volume Attenuation - Volume Settin

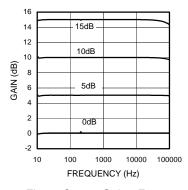


Fig.10 Output Gain - Frequency

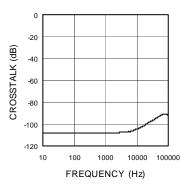


Fig.11 Cross-talk between Channels - Frequency

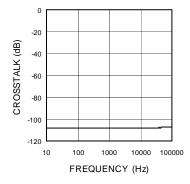


Fig.12 Cross-talk between Selectors - Frequency

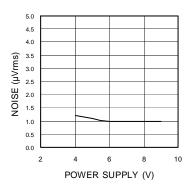


Fig.13 Output Noise Voltage -Power Supply Voltage

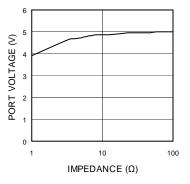


Fig.14 Port H Voltage – Load Resistance

### Notes for use

- (1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- (2) Although we are confident in recommending the sample application circuits, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that they have sufficient margins by taking into account variations in externally attached components and the Rohm LSI, not only for static characteristics but also including transient characteristics.
- (3) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

(4) VEE potential

Make the VEE pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the VEE pin, including transient phenomena.

- (5) Thermal design
  - Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.
- (6) Shorts between pins and misinstallation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

(7) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

(8) About Operating Voltage Range and Operating Temperature Range

The circuit functional operations are guaranteed within the Operating Voltage Range and Operating Temperature Range. The standard values of electrical characteristics, however, are guaranteed under the specific conditions. Accordingly, careful consideration of the IC characteristic variations is required to design a set of circuit.

- (9) About power ON/OFF
  - (a) At power ON/OFF, a shock sound will be generated and, therefore, use MUTE on the set.
  - (b) When turning on power supplies, VEE and VCC should be powered on simultaneously or VEE first; then followed by VCC. If the VCC side is started up first, an excessive current may pass VCC through VEE.
- (10) About serial control

For the CL and DA terminals, the patterned and other wirings should be routed not to cause interference with the analog-signal-related lines.

(11) About function switching

When switching Input Selector or Input Gain, use MUTE on Master Volume.

# ●Thermal derating characteristic

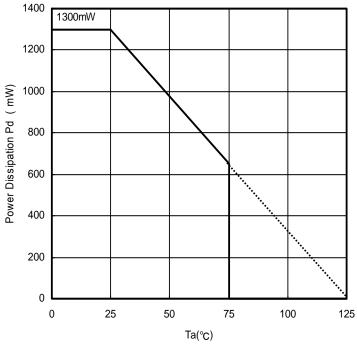
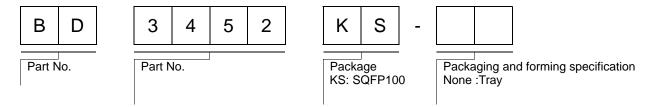


Fig.15

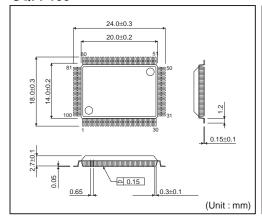
### BD3452KS

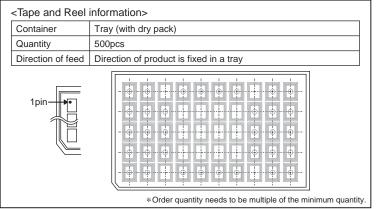
ROHM standard board packaging time value Board size: 70 x 70 x 1.6mm Raw material : FR4 glass epoxy board (copper area 3% or below)

# Ordering part number



# SQFP100





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Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

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JÁPAN	USA	EU	CHINA
CLASSI	СГУССШ	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSIII	— CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

# Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

# **Precautions Regarding Application Examples and External Circuits**

- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

# **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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QR code printed on ROHM Products label is for ROHM's internal use only.

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