

MH Ceramic Capacitor



Lead Free Ceramic Capacitor in Molded SM Leadframe



The MH components use a X7R high capacitance value ceramic capacitor in a surface mount precision made moulded case.

The MH capacitor combine the ceramic attributes of very low ESR, non-polar construction, excellent high frequency behaviour and voltage stress capabilities and wide temperature range; with the enhanced mechanical protection of a moulded case. The moulded case is UL94 V-0 flame retardant and the MH is RoHS and also AEC-Q200 compliant.

The MH range provides a lead frame solution to customers who have previously been unable to use large case ceramic capacitors because of mechanical stressing concerns.

For those applications where a tin termination is not acceptable, a Tin/Lead termination is available.

FEATURES

- Capacitance: 2.2u F – 22 μ F
- MHs are AEC-Q200 compliant
- Voltage Range DC: 25V – 100V
- Enhanced thermo mechanical stress resistance.

HOW TO ORDER

MH	V	1	1	C	475	M	A	T	2	A
MH Series	Case Size see table below	MLCC Count	Voltage 3 = 25V 5 = 50V 1 = 100V	Dielectric C = X7R	Capacitance Code (In pF) 2 Sig. Digits + Number of Zeros	Capacitance Tolerance K = $\pm 10\%$ M = $\pm 20\%$	Failure Rate A = Not Applicable	Terminations T = Tin Plated B = Tin/Lead Plated	Packaging 2 = 7" Reel 4 = 13" Reel 6 = Waffle Pack	Special Code A = Std. Product

MH X7R RANGE

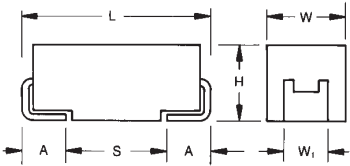
	Cap μ F	25V	50V	100V
225	2.2			
335	3.3			
475	4.7			
685	6.8			
106	10			
156	15			
226	22			

PACKAGING QUANTITY

7" Reels	400
13" Reels	1500
Waffle Pack	108

“V” CASE DIMENSIONS: millimeters (inches)

L	7.3 \pm 0.2 0 (0.287 \pm 0.008)
W	6.1 + 0.20 - 0.10 (0.24 + 0.008 - 0.004)
H	3.45 \pm 0.30 (0.136 \pm 0.012)
W₁	3.1 \pm 0.20 (0.120 \pm 0.008)
A	1.4 + 0.30 - 0.20 (0.055 + 0.012 - 0.008)
S Min	4.40 (0.173)



LEAD-FREE
LEAD-FREE COMPATIBLE
COMPONENT



RoHS
COMPLIANT

TS 16949, ISO 14001
Certified Manufacture



Performance of SMPS capacitors can be simulated by downloading SpiCalci software program -
<http://www.avx.com/SpiApps/default.asp#spicalci>

