TLC363C Series (5V5, 7V0, 20V8)

TRIPLE LOW CAPACITANCE MOUNT ZENER DIODE ARRAY

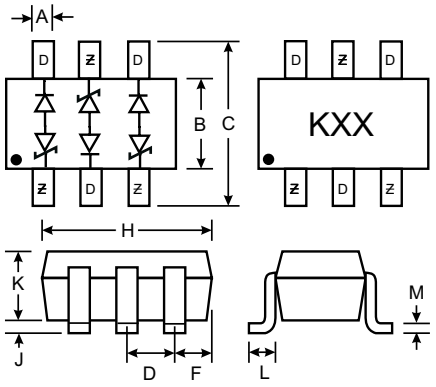
NEW PRODUCT

Features

- Planar Die Construction
- 200mW Power Dissipation
- Nominal Zener Voltages: 5.5V, 6.4V, 7.0V, 20.8V
- Ultra-Small Surface Mount Package

Mechanical Data

- Case: SOT-363, Molded Plastic
- Terminals: Solderable per MIL-STD-202, Method 208
- Orientation: See Diagram
- Marking: See Table Below
- Weight: 0.006 grams (approx.)



SOT-363		
Dim	**Min**	**Max**
A	0.10	0.30
B	1.15	1.35
C	2.00	2.20
D	0.65 Nominal	
F	0.30	0.40
H	1.80	2.20
J	—	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.25
All Dimensions in mm		

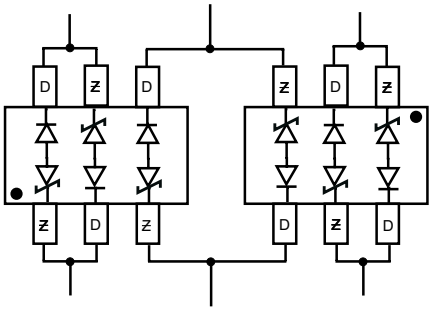
Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Zener Current (See Table Below)	—	—	—
Power Dissipation (Note 1)	P_d	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	625	K/W
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +150	°C

Type Number	Marking Code	Zener Voltage Range (Note 2)			Maximum Zener Impedance				Maximum Reverse Current		Maximum Junction Capacitance
		V_Z @ I_{ZT}			Z_{ZT} @ I_{ZT}		Z_{ZK} @ I_{ZK}		I_R @ V_R		C_J
		Nom (V)	Min (V)	Max (V)	(Ω)	(mA)	(Ω)	(mA)	(μA)	(V)	pF
TLC363C5V5	K01	5.5	5.22	5.78	80	5.0	500	1.0	1.0	2.0	4.0
TLC363C6V4	K04	6.4	6.08	6.72	50	5.0	400	1.0	2.0	3.0	4.0
TLC363C7V0	K05	7.0	6.65	7.35	18	5.0	200	1.0	2.0	4.0	4.0
TLC363C20V8	KN7	20.8	26.74	21.84	58	5.0	225	1.0	0.05	14	4.0

Notes: 1. Valid provided that device terminals are kept at ambient temperature.
2. V_Z measured @ I_{ZT} using a pulse test. I_{ZT} pulse width = 5.0ms. Standard voltage tolerance is 5%.

APPLICATION



Two packages connected to yield three low capacitance bi-directional zener circuits.