

FGA90N30D

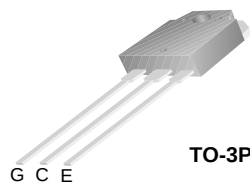
300V PDP IGBT

Features

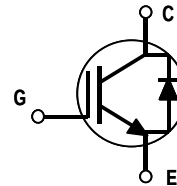
- High Current Capability
- Low saturation voltage: $V_{CE(sat)}$, Typ = 1.1V@ $I_C = 20A$
- High Input Impedance

Description

Employing Unified IGBT Technology, FGA90N30D provides low conduction and switching loss. FGA90N30D offers the optimum solution for PDP applications where low conduction loss is essential.



TO-3P



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Description		FGA90N30D	Units
V_{CES}	Collector-Emitter Voltage		300	V
V_{GES}	Gate-Emitter Voltage		± 30	V
I_C	Collector Current	@ $T_C = 25^\circ\text{C}$	90	A
I_{CM}	Pulsed Collector Current (Note 1)	@ $T_C = 25^\circ\text{C}$	220	A
I_F	Diode Continuous Forward Current	@ $T_C = 100^\circ\text{C}$	10	A
I_{FM}	Diode Maximum Forward Current		40	A
P_D	Maximum Power Dissipation	@ $T_C = 25^\circ\text{C}$	219	W
	Maximum Power Dissipation	@ $T_C = 100^\circ\text{C}$	87	W
T_J	Operating Junction Temperature		-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range		-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	$^\circ\text{C}$

Notes:

(1) Repetitive test , pulse width = 100usec , Duty = 0.5

* I_{C_pulse} limited by max T_J

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction-to-Case for IGBT	--	0.57	$^\circ\text{C/W}$
$R_{\theta JC}(DIODE)$	Thermal Resistance, Junction-to-Case for Diode	--	1.56	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	40	$^\circ\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGA90N30D	FGA90N30D	TO-3P	--	--	30

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250μA	300	--	--	V
ΔB _{VCES} / ΔT _J	Temperature Coefficient of Breakdown Voltage	V _{GE} = 0V, I _C = 250μA	--	0.6	--	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0V	--	--	100	μA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	--	--	± 250	nA
On Characteristics						
V _{GE(th)}	G-E Threshold Voltage	I _C = 250uA, V _{CE} = V _{GE}	2.5	4.0	5.0	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 20A, V _{GE} = 15V	--	1.1	1.4	V
		I _C = 90A, V _{GE} = 15V	--	1.9	--	V
		I _C = 90A, V _{GE} = 15V, T _C = 125°C	--	2.0	--	V
Dynamic Characteristics						
C _{ies}	Input Capacitance	V _{CE} = 30V, V _{GE} = 0V, f = 1MHZ	--	1700	-	pF
C _{oes}	Output Capacitance		--	290	-	pF
C _{res}	Reverse Transfer Capacitance		--	80	-	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{CC} = 200V, I _C = 20A, R _G = 10Ω, V _{GE} = 15V, Resistive Load, T _C = 25°C	--	30	--	ns
t _r	Rise Time		--	200	--	ns
t _{d(off)}	Turn-Off Delay Time		--	110	--	ns
t _f	Fall Time		--	140	300	ns
E _{on}	Turn-On Switching Loss		--	0.15	--	mJ
E _{off}	Turn-Off Switching Loss		--	0.45	--	mJ
E _{ts}	Total Switching Loss	V _{CC} = 200V, I _C = 20A, R _G = 10Ω, V _{GE} = 15V, Resistive Load, T _C = 125°C	--	0.6	--	mJ
t _{d(on)}	Turn-On Delay Time		--	30	--	ns
t _r	Rise Time		--	210	--	ns
t _{d(off)}	Turn-Off Delay Time		--	110	--	ns
t _f	Fall Time		--	200	--	ns
E _{on}	Turn-On Switching Loss		--	0.16	--	mJ
E _{off}	Turn-Off Switching Loss		--	0.72	--	mJ
E _{ts}	Total Switching Loss		--	0.88	--	mJ
Q _g	Total Gate Charge	V _{CE} = 200V, I _C = 20A, V _{GE} = 15V	--	87	130	nC
Q _{ge}	Gate-Emitter Charge		--	12	18	nC
Q _{gc}	Gate-Collector Charge		--	38	57	nC

Electrical Characteristics of DIODE $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
V _{FM}	Diode Forward Voltage	I _F = 10A	T _C = 25°C	--	1.1	1.4	V
			T _C = 125°C	--	0.9	--	
t _{rr}	Diode Reverse Recovery Time	I _F = 10A dI/dt = 200A/μs	T _C = 25°C	--	21	--	ns
			T _C = 125°C	--	35	--	
I _{rr}	Diode Peak Reverse Recovery Cur- rent		T _C = 25°C	--	2.8	--	A
			T _C = 125°C	--	5.6	--	
Q _{rr}	Diode Reverse Recovery Charge		T _C = 25°C	--	29.4	--	nC
			T _C = 125°C	--	98	--	

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

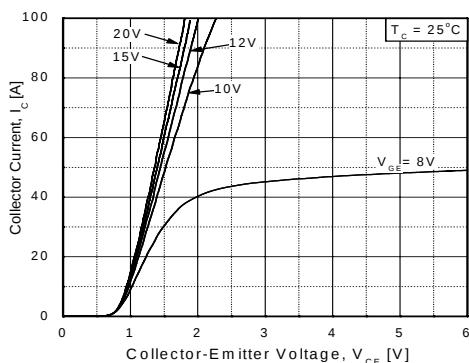


Figure 2. Typical Output Characteristics

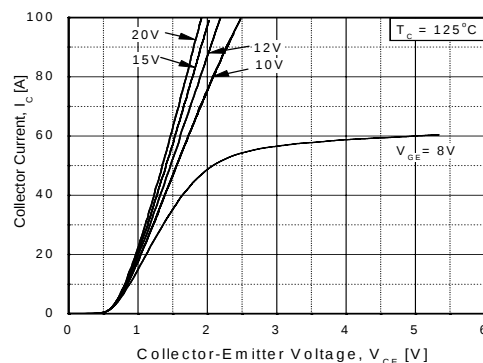


Figure 3. Typical Saturation Voltage Characteristics

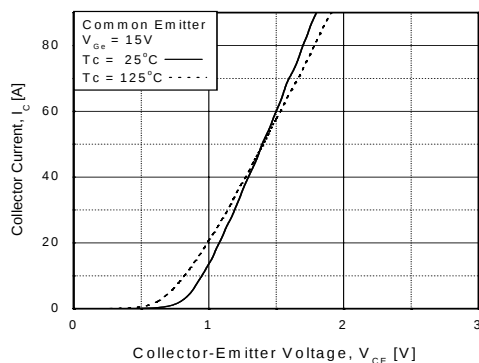


Figure 4. Transfer characteristics

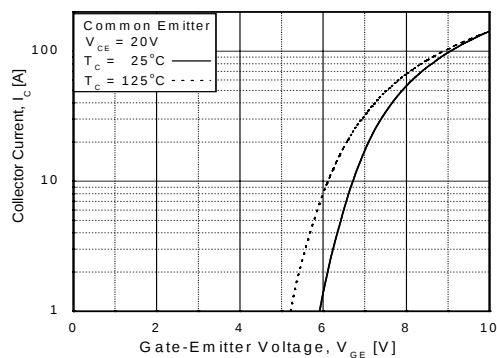


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

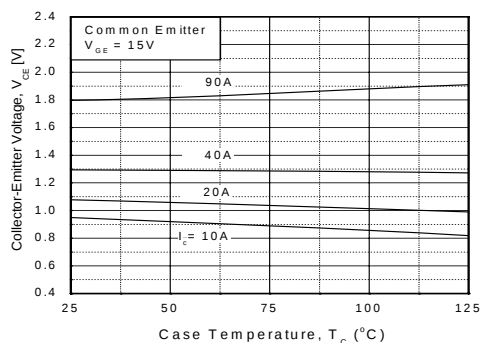
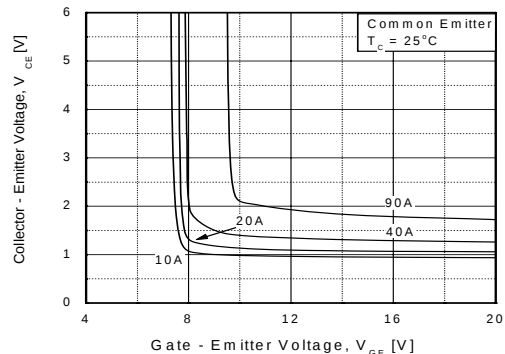


Figure 6. Saturation Voltage vs. Vge



Typical Performance Characteristics (Continued)

Figure 7. Saturation Voltage vs. V_{GE}

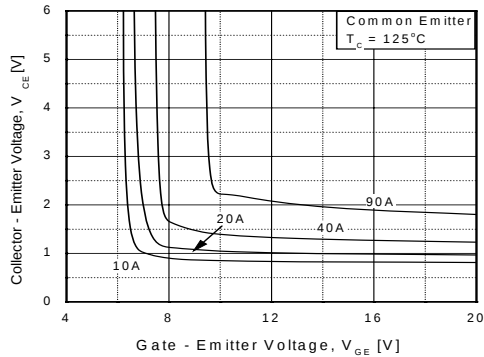


Figure 8. Capacitance Characteristics

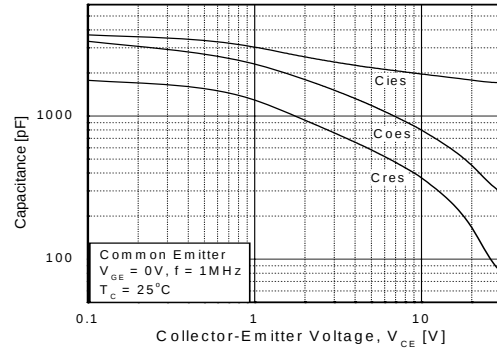


Figure 9. Gate Charge Characteristics

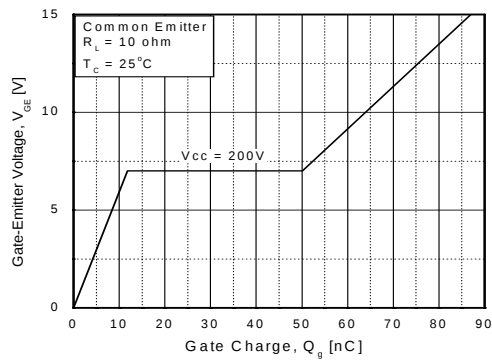


Figure 10. SOA Characteristics

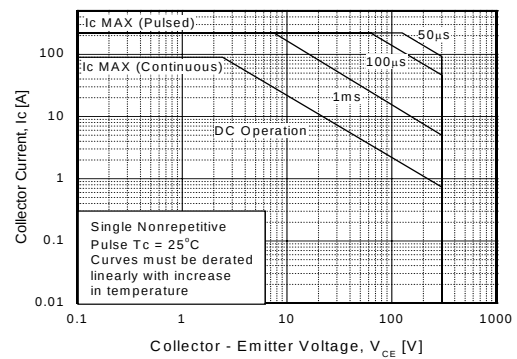


Figure 11. Turn-On Characteristics vs. Gate Resistance

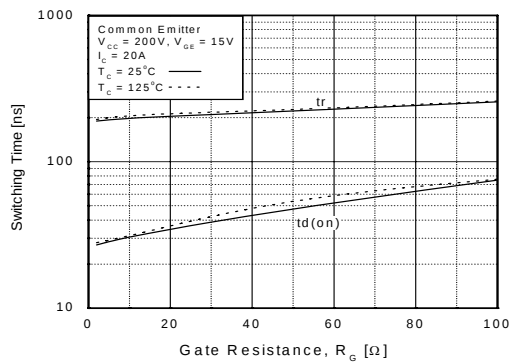
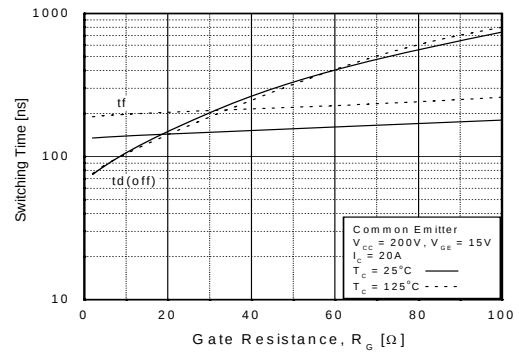


Figure 12. Turn-Off Characteristics vs. Gate Resistance



Typical Performance Characteristics (Continued)

Figure 13. Turn-On Characteristics vs. Collector Current

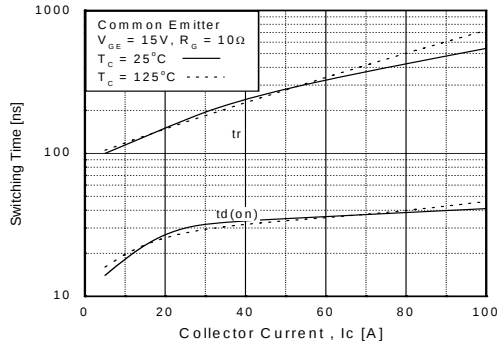


Figure 14. Turn-Off Characteristics vs. Collector Current

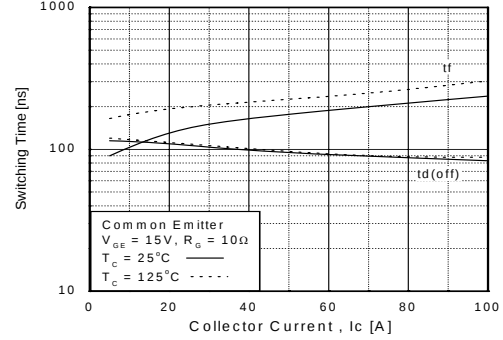


Figure 15. Switching Loss vs. Gate Resistance

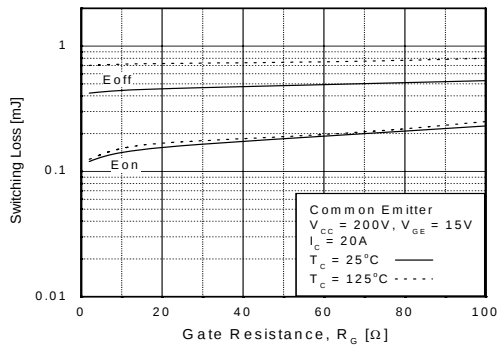


Figure 16. Switching Loss vs. Collector Current

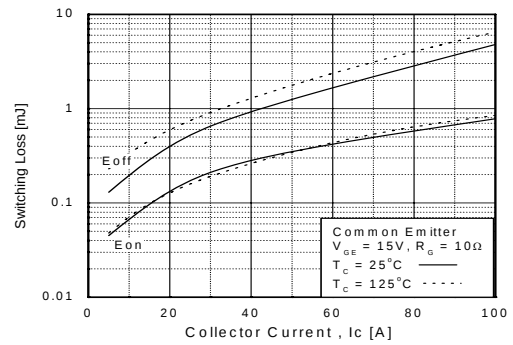
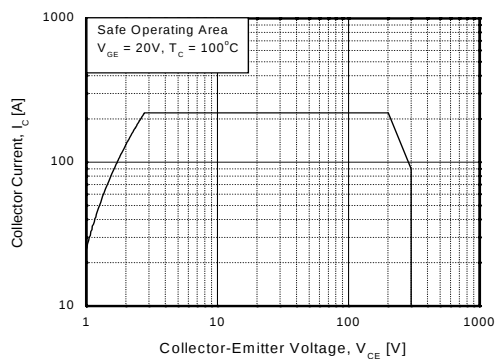


Figure 17. Turn-Off SOA Figure



Typical Performance Characteristics (Continued)

Figure 18. Transient Thermal Impedance of IGBT

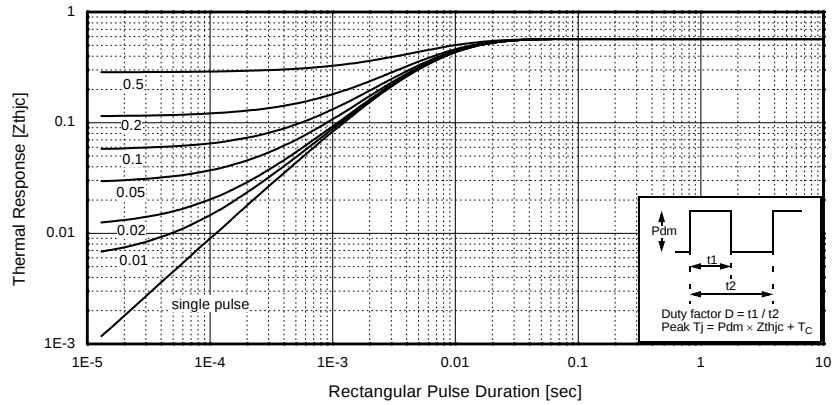


Figure 19. Forward Characteristics

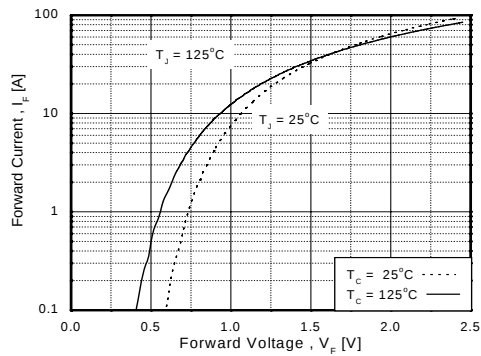


Figure 20. Typical Reverse Recovery Current

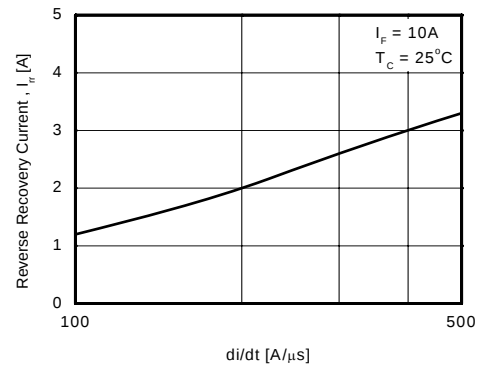
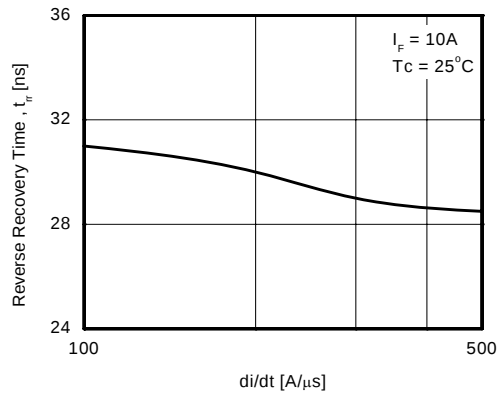
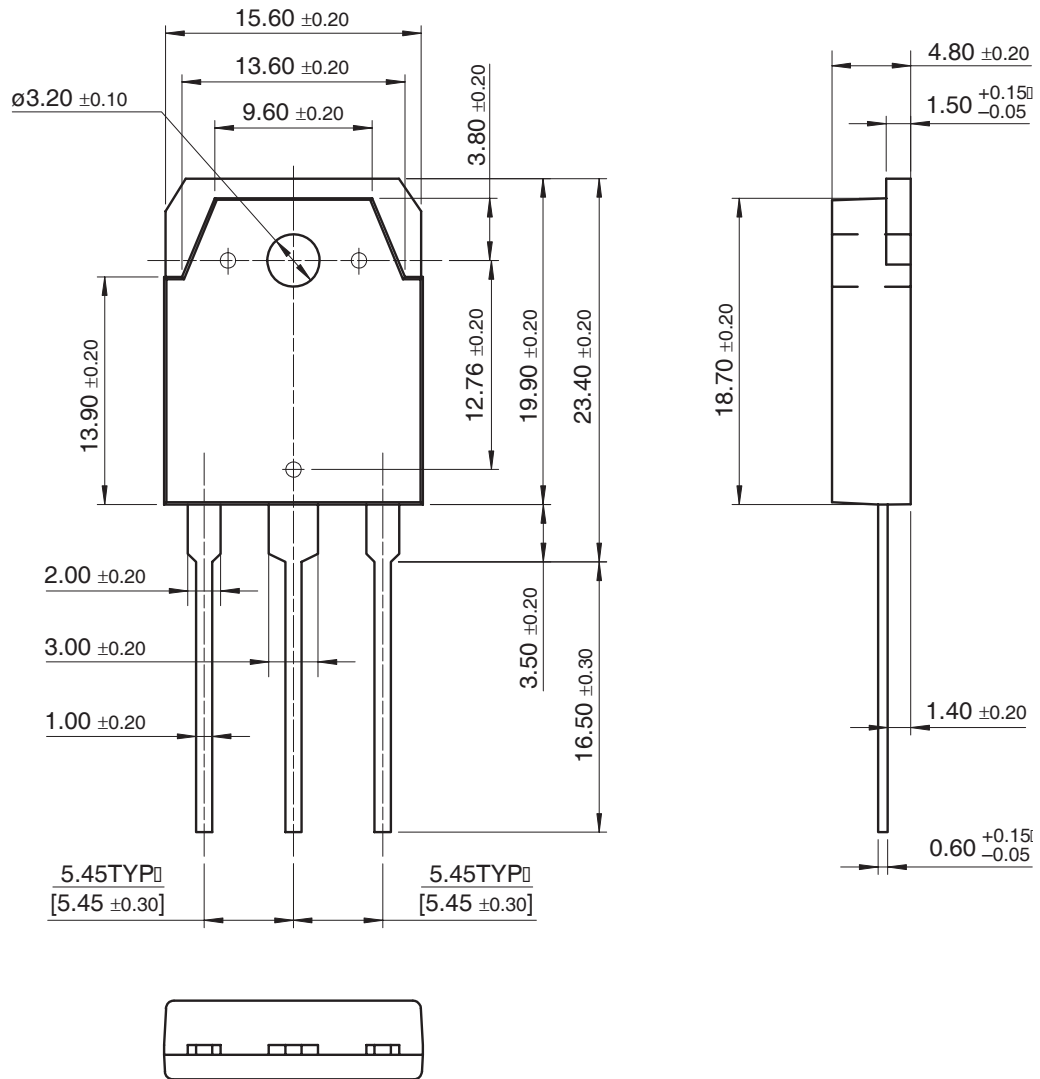


Figure 21. Typical Reverse Recovery Time



Mechanical Dimensions

TO-3P



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FAST®	MicroFET™	QST™	TinyBuck™	
FASTr™	MicroPak™	QT Optoelectronics™	TinyPWM™	
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