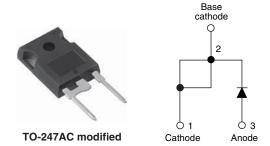


VS-40EPS...PbF Series, VS-40EPS...-M3 Series

Vishay Semiconductors

High Voltage, Input Rectifier Diode, 40 A



PRODUCT SUMMARY				
Package	TO-247AC modified (2 pins)			
I _{F(AV)}	40 A			
V _R	800 V, 1200 V			
V _F at I _F	1.1 V			
I _{FSM}	475 A			
T _J max.	150 °C			
Diode variation	Single die			

FEATURES

- · Very low forward voltage drop
- 150 °C max. operating junction temperature
- Designed and qualified according to JEDEC-JESD47
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition (-M3 only)





ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- · Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Sinusoidal waveform	40	Α			
V _{RRM}	Range	800/1200	V			
I _{FSM}		475	Α			
V _F	40 A, T _J = 25 °C	1.1	V			
T _J		- 40 to 150	°C			

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
VS-40EPS08PbF, VS-40EPS08-M3	800	900	1		
VS-40EPS12PbF, VS-40EPS12-M3	1200	1300	1		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T _C = 105 °C, 180° conduction half sine wave	40			
Maximum peak one cycle	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	400	А		
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	475			
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	800	A ² s		
Maximum i-t for fusing	I-r	10 ms sine pulse, no voltage reapplied	1131	— A ^z s		
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied	11 310	A²√s		



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum forward voltage drap	V	20 A, T _J = 25 °C	20 A, T _J = 25 °C		V
Maximum forward voltage drop	V_{FM}	40 A, T _J = 25 °C		1.1	
Forward slope resistance	r _t	T 450 00		7.16	mΩ
Threshold voltage	V _{F(TO)}	T _J = 150 °C		0.74	V
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _{RRM}	0.1	mA
Maximum reverse leakage current	I _{RM}	T _J = 150 °C	v _R = nateu v _{RRM}	1.0	IIIA

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storrage temperature range	1	T _J , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	0.6	
Maximum thermal resistance, junction to ambient		R _{thJA}		40	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, flat, smooth and greased	0.2	
Approximate weight				6	g
Approximate weight				0.21	oz.
Mounting torque -	minimum			6 (5)	kgf ⋅ cm
wounting torque –	maximum			12 (10)	(lbf ⋅ in)
Marking device			Coop atula TO 247AC modified (JEDEC)	40EPS08	
			Case style TO-247AC modified (JEDEC)	40EPS12	

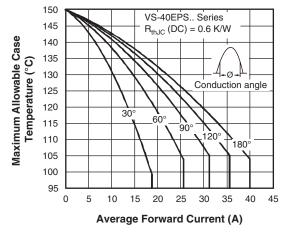


Fig. 1 - Current Rating Characteristics

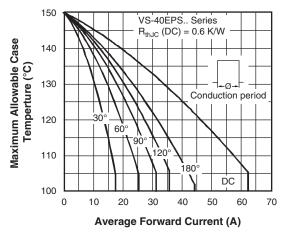


Fig. 2 - Current Rating Characteristics

www.vishay.com

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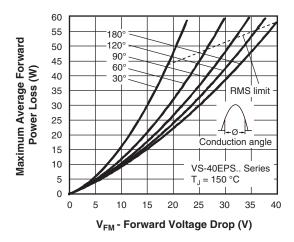


Fig. 3 - Forward Power Loss Characteristics

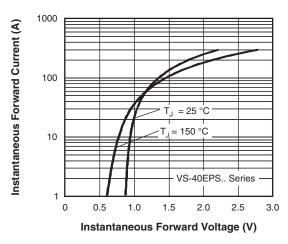


Fig. 5 - Forward Voltage Drop Chacteristics

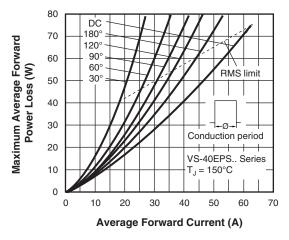


Fig. 4 - Forward Power Loss Characteristics

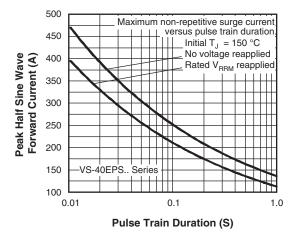


Fig. 6 - Maximum Non-Repetitive Surge Current

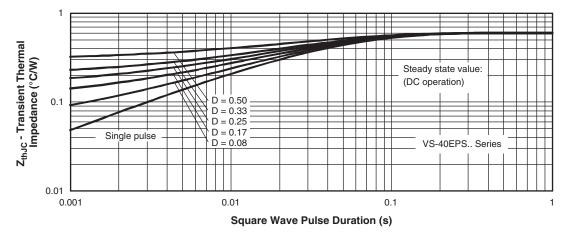


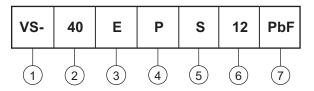
Fig. 7 - Thermal Impedance ZthJC Characteristics

VS-40EPS...PbF Series, VS-40EPS...-M3 Series

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 = 40 A)

3 - Circuit configuration:

E = Single diode

4 - Package:

P = TO-247AC modified

5 - Type of silicon:

S = Standard recovery rectifier

08 = 800 V

6 - Voltage rating

12 = 1200 V

7 - Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

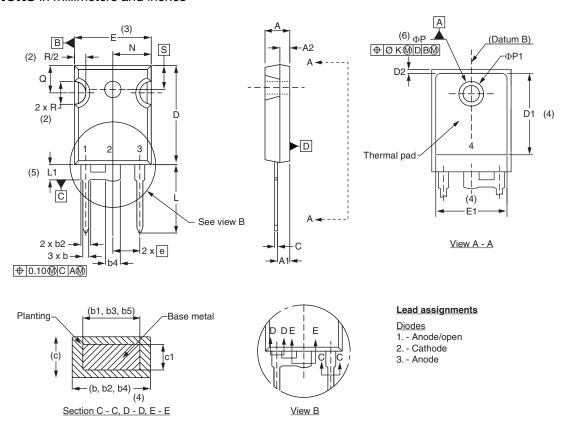
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-40EPS08PbF	25	500	Antistatic plastic tubes			
VS-40EPS08-M3	25	500	Antistatic plastic tubes			
VS-40EPS12PbF	25	500	Antistatic plastic tubes			
VS-40EPS12-M3	25	500	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS				
Dimensions		www.vishay.com/doc?95253		
Part marking information	TO-247AC modified PbF	www.vishay.com/doc?95255		
Part marking information	TO-247AC modified -M3	www.vishay.com/doc?95442		



Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
Е	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
ΦК	2.54		0.0	10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
Ν	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerance per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) ΦP to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



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