

# EMCL13E2J-8.000M TR [Click part number to visit Part Number Details page](#)

## REGULATORY COMPLIANCE (Data Sheet downloaded on Nov 20, 2017)



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## ITEM DESCRIPTION

MEMS Clock Oscillators LVPECL (PECL) 3.3Vdc 6 Pad 5.0mm x 7.0mm Plastic Surface Mount (SMD) 8.000MHz  $\pm 25$ ppm over 0°C to +70°C

## ELECTRICAL SPECIFICATIONS

|                                                   |                                                                                                                                                                                                                                                                  |
|---------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Nominal Frequency                                 | 8.000MHz                                                                                                                                                                                                                                                         |
| Frequency Tolerance/Stability                     | $\pm 25$ ppm Maximum over 0°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Reflow, Shock, and Vibration) |
| Aging at 25°C                                     | $\pm 1$ ppm Maximum First Year                                                                                                                                                                                                                                   |
| Supply Voltage                                    | 3.3Vdc $\pm 10\%$                                                                                                                                                                                                                                                |
| Input Current                                     | 80mA Maximum (Excluding Load Termination Current)                                                                                                                                                                                                                |
| Output Voltage Logic High (Voh)                   | Vdd-1.10Vdc Minimum, 2.40Vdc Typical, Vdd-0.70Vdc Maximum                                                                                                                                                                                                        |
| Output Voltage Logic Low (Vol)                    | Vdd-2.0Vdc Minimum, 1.60Vdc Typical, Vdd-1.40Vdc Maximum                                                                                                                                                                                                         |
| Rise/Fall Time                                    | 150pSec Typical, 300pSec Maximum (Measured over 20% to 80% of waveform)                                                                                                                                                                                          |
| Duty Cycle                                        | 50 $\pm 5\%$ (Measured at 50% of waveform)                                                                                                                                                                                                                       |
| Output Swing (VOpp)                               | 600mVdc Minimum, 800mVdc Typical, 1000mVdc Maximum                                                                                                                                                                                                               |
| Load Drive Capability                             | 50 Ohms into Vdd-2.0Vdc                                                                                                                                                                                                                                          |
| Output Logic Type                                 | LVPECL                                                                                                                                                                                                                                                           |
| Logic Control / Additional Output                 | Standby (ST) and Complementary Output                                                                                                                                                                                                                            |
| Output Control Input Voltage                      | Vih of 70% of Vdd Minimum or No Connect to Enable Output and Complementary Output, Vil of 30% of Vdd Maximum to Disable Output and Complementary Output (High Impedance)                                                                                         |
| Standby Current                                   | 30 $\mu$ A Maximum (Without Load)                                                                                                                                                                                                                                |
| Period Jitter (Deterministic)                     | 0.2pSec Typical                                                                                                                                                                                                                                                  |
| Period Jitter (Random)                            | 2.0pSec Typical                                                                                                                                                                                                                                                  |
| Period Jitter (RMS)                               | 1.5pSec Typical, 3.0pSec Maximum                                                                                                                                                                                                                                 |
| Period Jitter (pk-pk)                             | 20pSec Typical, 25pSec Maximum                                                                                                                                                                                                                                   |
| RMS Phase Jitter (Fj = 637kHz to 10MHz; Random)   | 1.7pSec Typical                                                                                                                                                                                                                                                  |
| RMS Phase Jitter (Fj = 1MHz to 20MHz; Random)     | 1.4pSec Typical                                                                                                                                                                                                                                                  |
| RMS Phase Jitter (Fj = 1.875MHz to 20MHz; Random) | 1.1pSec Typical                                                                                                                                                                                                                                                  |
| Start Up Time                                     | 10mSec Maximum                                                                                                                                                                                                                                                   |
| Storage Temperature Range                         | -55°C to +125°C                                                                                                                                                                                                                                                  |

## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

|                              |                                                                   |
|------------------------------|-------------------------------------------------------------------|
| ESD Susceptibility           | MIL-STD-883, Method 3015, Class 2, HBM 2000V                      |
| Flammability                 | UL94-V0                                                           |
| Mechanical Shock             | MIL-STD-883, Method 2002, Condition G, 30,000G                    |
| Moisture Resistance          | MIL-STD-883, Method 1004                                          |
| Moisture Sensitivity Level   | J-STD-020, MSL 1                                                  |
| Resistance to Soldering Heat | MIL-STD-202, Method 210, Condition K                              |
| Resistance to Solvents       | MIL-STD-202, Method 215                                           |
| Solderability                | MIL-STD-883, Method 2003 (Six I/O Pads on bottom of package only) |



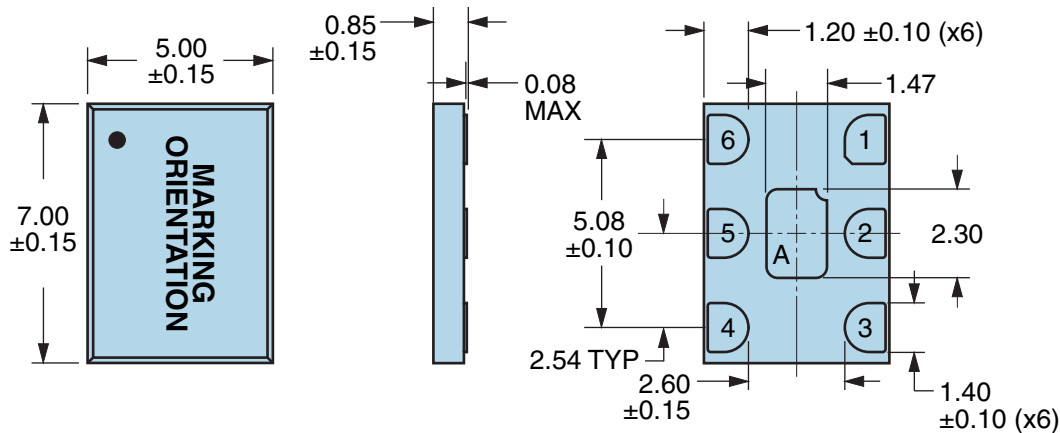
**EMCL13E2J-8.000M TR** [Click part number to visit Part Number Details page](#)

| ENVIRONMENTAL & MECHANICAL SPECIFICATIONS CONTINUED |                                            |
|-----------------------------------------------------|--------------------------------------------|
| Temperature Cycling                                 | MIL-STD-883, Method 1010, Condition B      |
| Thermal Shock                                       | MIL-STD-883, Method 1011, Condition B      |
| Vibration                                           | MIL-STD-883, Method 2007, Condition A, 20G |

EMCL13E2J-8.000M TR

[Click part number to visit Part Number Details page](#)

MECHANICAL DIMENSIONS (all dimensions in millimeters)

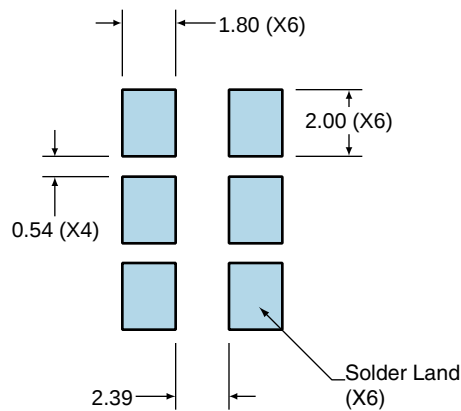


| PIN | CONNECTION           |
|-----|----------------------|
| 1   | Standby (ST)         |
| 2   | No Connect           |
| 3   | Case Ground          |
| 4   | Output               |
| 5   | Complementary Output |
| 6   | Supply Voltage       |

| LINE | MARKING                                                          |
|------|------------------------------------------------------------------|
| 1    | XXXX or XXXXX<br>XXXX or XXXXX=Ecliptek Manufacturing Identifier |

Suggested Solder Pad Layout

All Dimensions in Millimeters

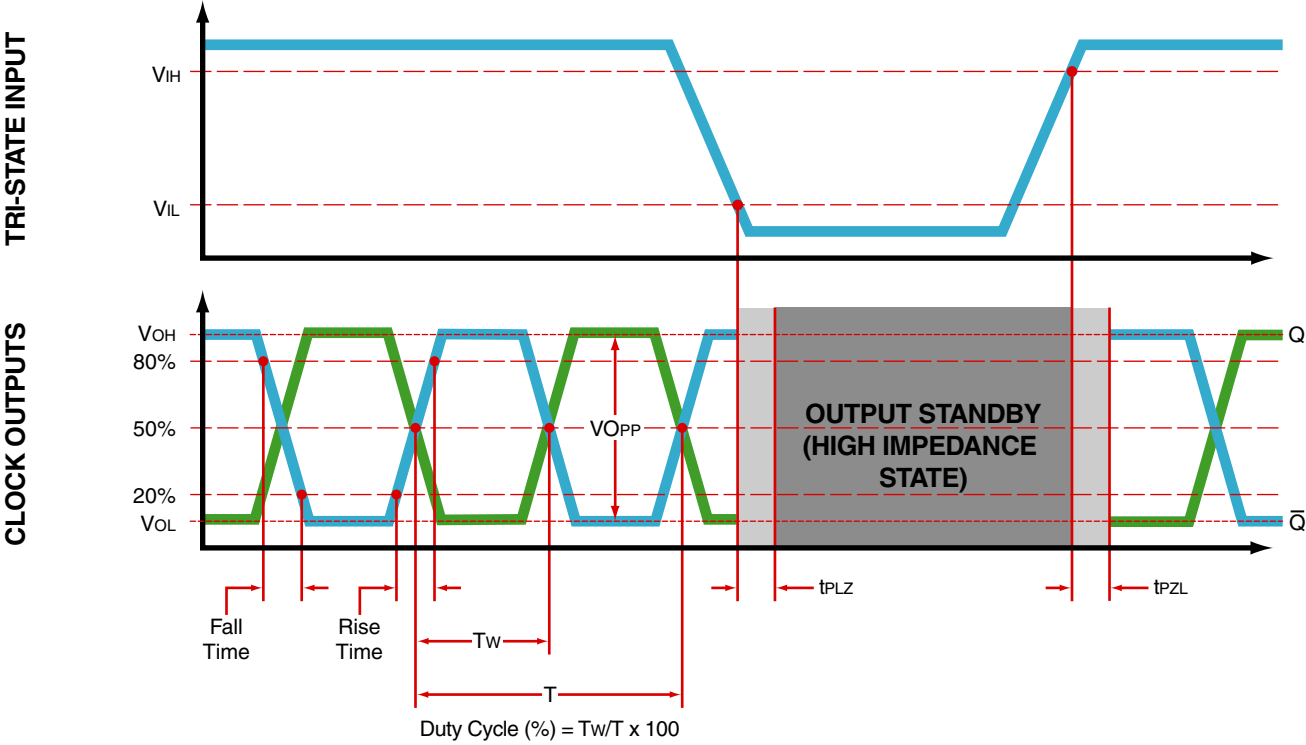


All Tolerances are ±0.1

EMCL13E2J-8.000M TR

 Click part number to visit  
Part Number Details page

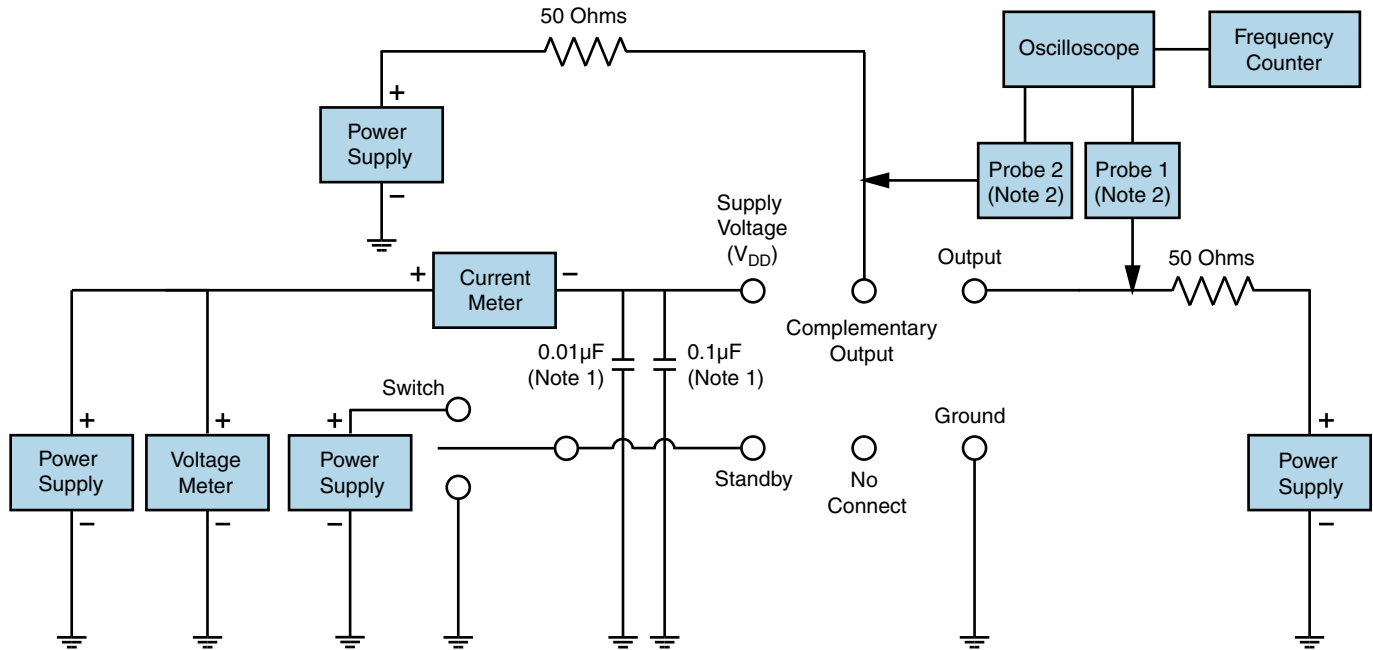
OUTPUT WAVEFORM & TIMING DIAGRAM



# EMCL13E2J-8.000M TR

[Click part number to visit Part Number Details page](#)

## Test Circuit for Standby (Pad 1) and Complementary Output



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>500MHz) passive probe is recommended.

Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.

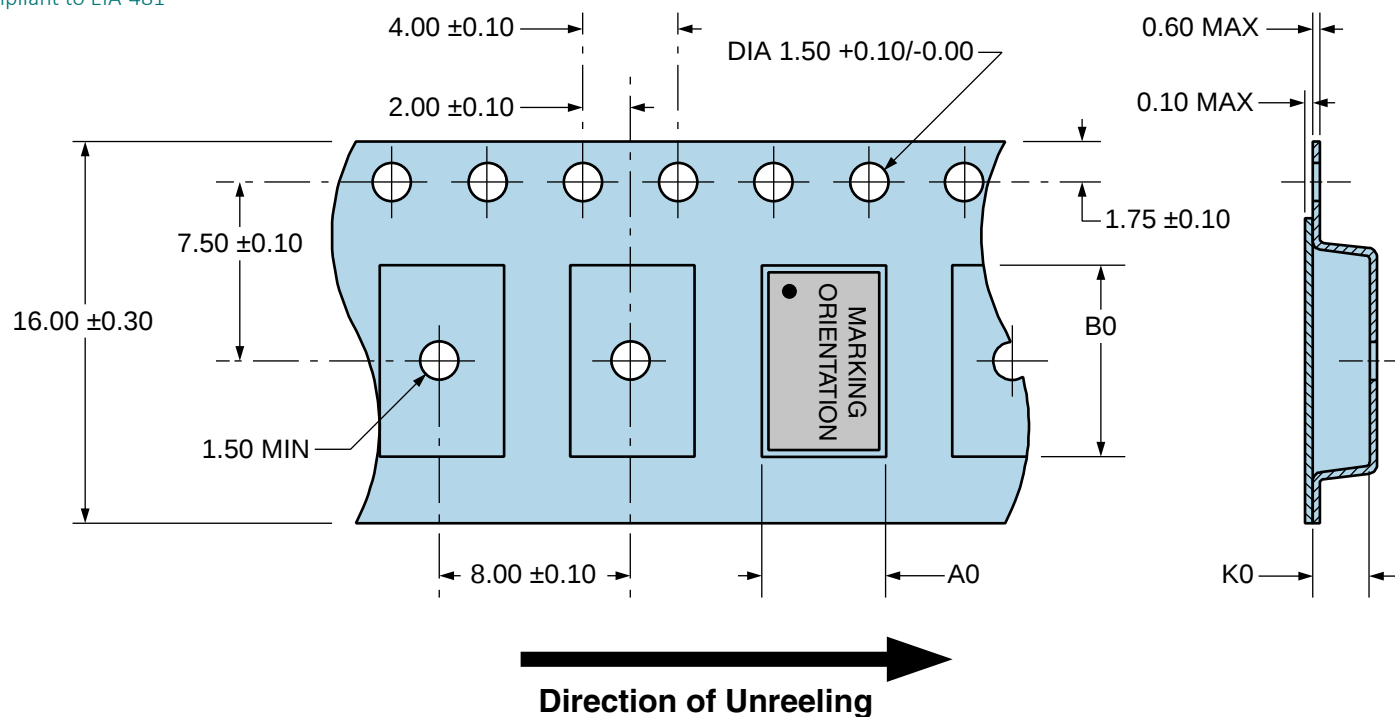
# EMCL13E2J-8.000M TR

## Tape & Reel Dimensions

Quantity Per Reel: 1,000 units

All Dimensions in Millimeters

Compliant to EIA-481



EMCL13E2J-8.000M TR

[Click part number to visit Part Number Details page](#)

Recommended Solder Reflow Methods



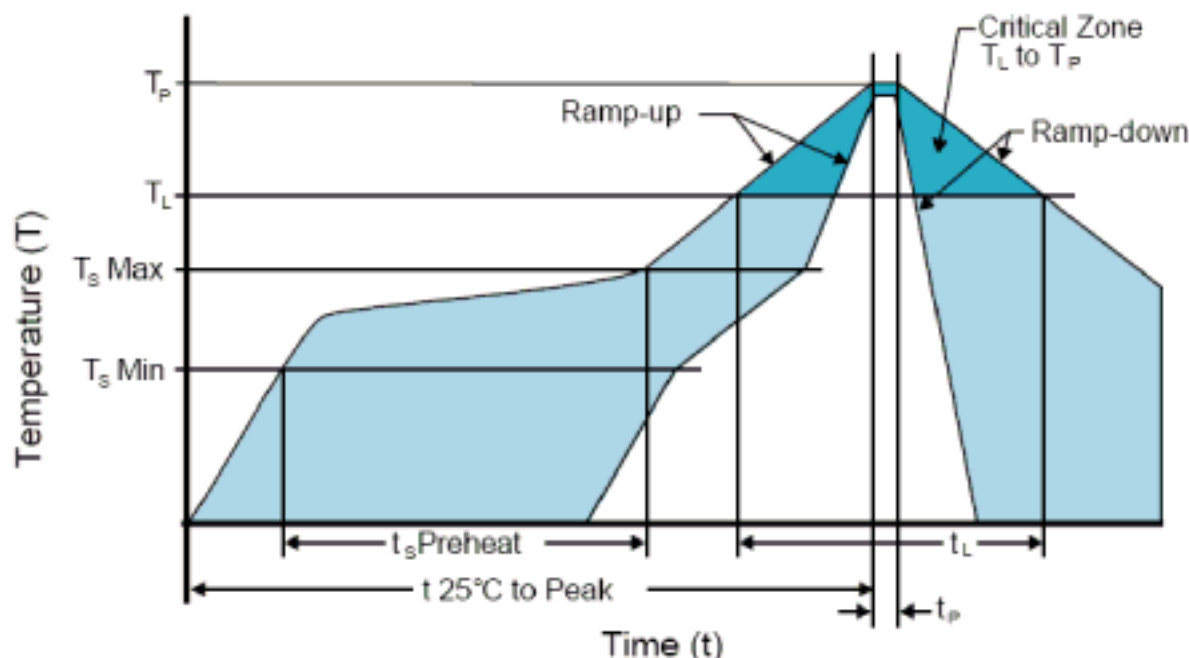
High Temperature Infrared/Convection

|                                     |                                      |
|-------------------------------------|--------------------------------------|
| Ts MAX to TL (Ramp-up Rate)         | 3°C/Second Maximum                   |
| Preheat                             |                                      |
| - Temperature Minimum (Ts MIN)      | 150°C                                |
| - Temperature Typical (Ts TYP)      | 175°C                                |
| - Temperature Maximum (Ts MAX)      | 200°C                                |
| - Time (ts MIN)                     | 60 - 180 Seconds                     |
| Ramp-up Rate (TL to TP)             | 3°C/Second Maximum                   |
| Time Maintained Above:              |                                      |
| - Temperature (TL)                  | 217°C                                |
| - Time (tL)                         | 60 - 150 Seconds                     |
| Peak Temperature (TP)               | 260°C Maximum for 10 Seconds Maximum |
| Target Peak Temperature (TP Target) | 250°C +0/-5°C                        |
| Time within 5°C of actual peak (tp) | 20 - 40 Seconds                      |
| Ramp-down Rate                      | 6°C/Second Maximum                   |
| Time 25°C to Peak Temperature (t)   | 8 Minutes Maximum                    |
| Moisture Sensitivity Level          | Level 1                              |

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[Click part number to visit Part Number Details page](#)

## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

T<sub>s</sub> MAX to T<sub>L</sub> (Ramp-up Rate) 5°C/Second Maximum

#### Preheat

- Temperature Minimum (T<sub>s</sub> MIN) N/A
- Temperature Typical (T<sub>s</sub> TYP) 150°C
- Temperature Maximum (T<sub>s</sub> MAX) N/A
- Time (t<sub>s</sub> MIN) 60 - 120 Seconds

Ramp-up Rate (T<sub>L</sub> to T<sub>P</sub>) 5°C/Second Maximum

#### Time Maintained Above:

- Temperature (T<sub>L</sub>) 150°C
- Time (t<sub>L</sub>) 200 Seconds Maximum

Peak Temperature (T<sub>P</sub>) 240°C Maximum

Target Peak Temperature (T<sub>P</sub> Target) 240°C Maximum 2 Times / 230°C Maximum 1 Time

Time within 5°C of actual peak (t<sub>p</sub>) 10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time

Ramp-down Rate 5°C/Second Maximum

Time 25°C to Peak Temperature (t) N/A

Moisture Sensitivity Level Level 1

### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum.

### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum.