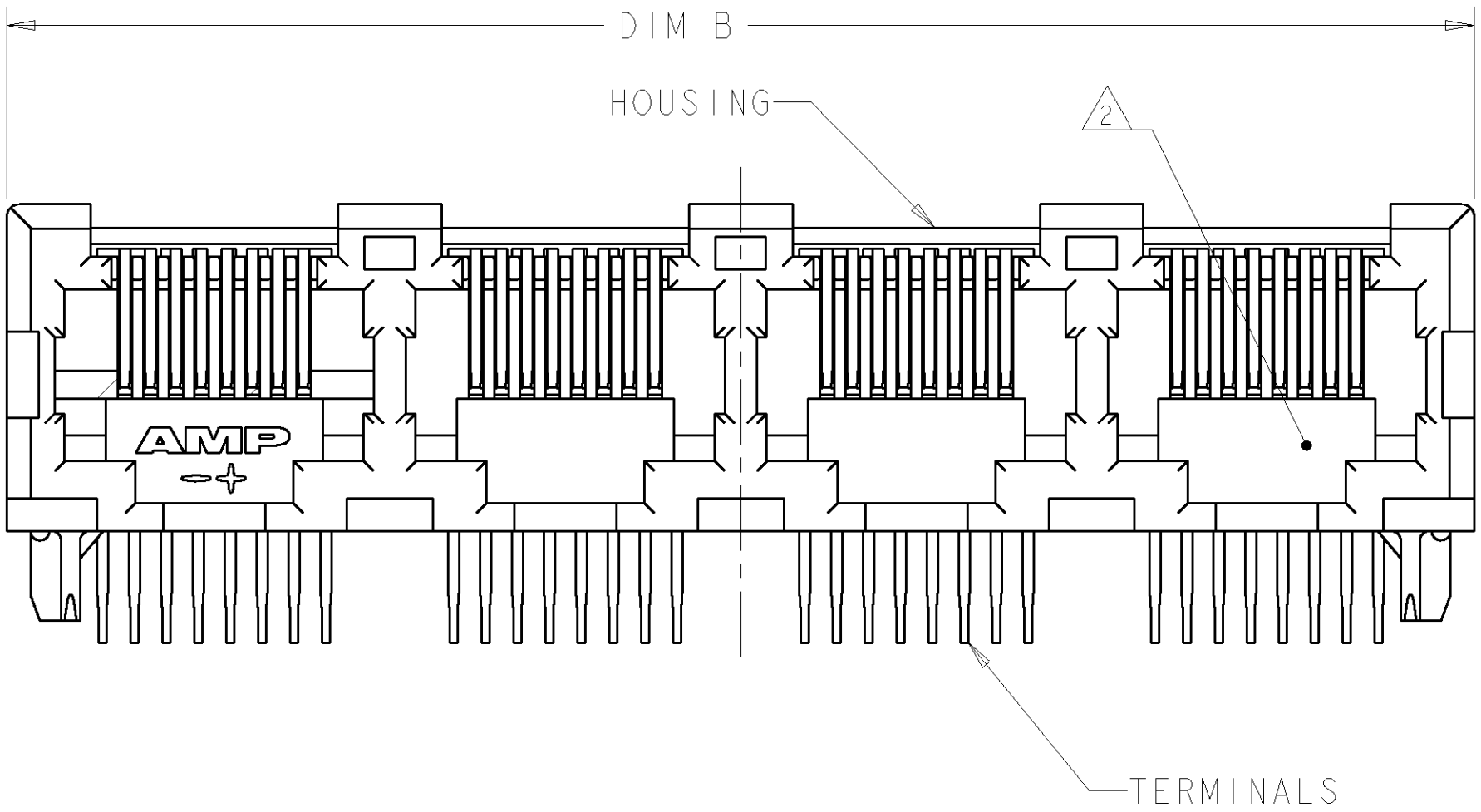
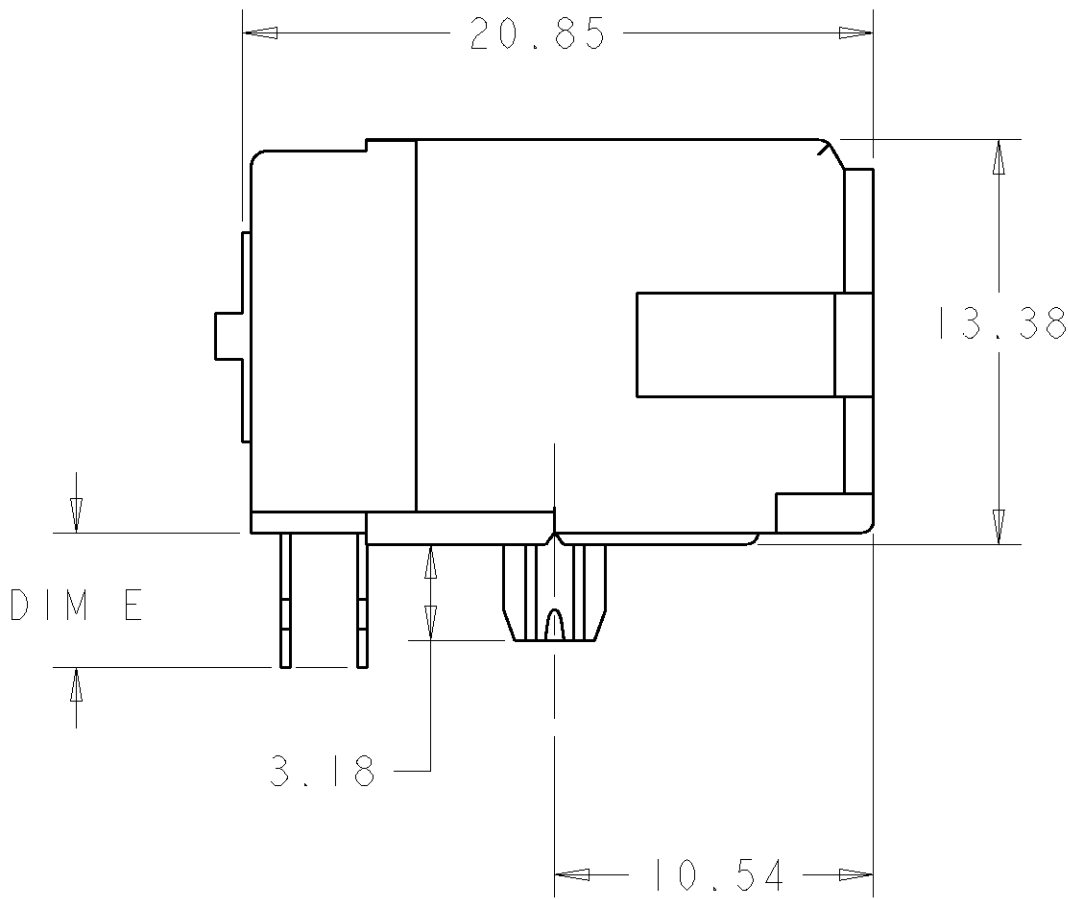


LOC	DIST	REVISIONS						
		P	LTR	DESCRIPTION	DATE	DWN	APVD	
AA	22		A	ECOS11-0201-04	10MAY2005	LAM	SF	



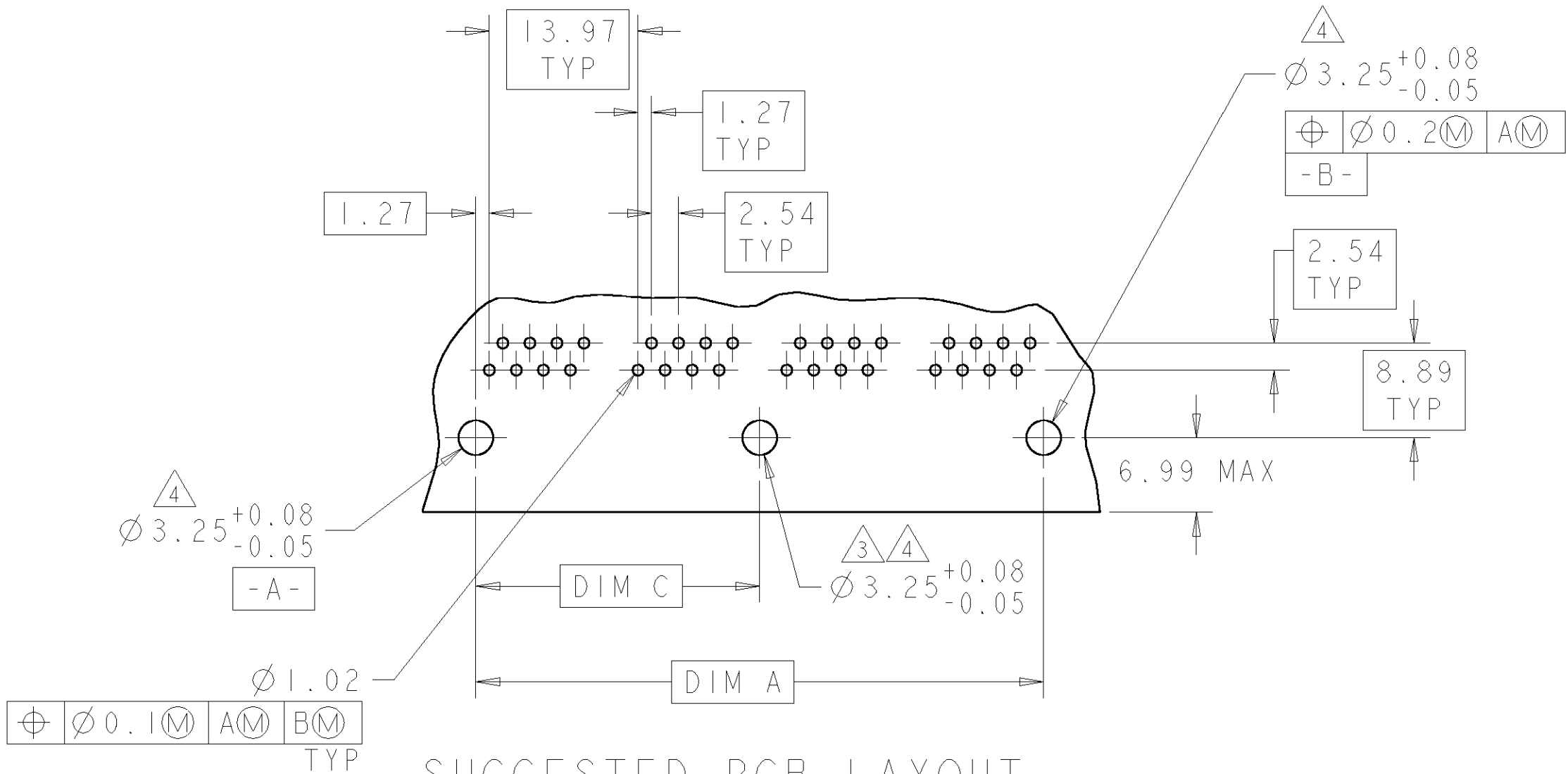
1. MATERIAL: HOUSING - PCT POLYESTER, BLACK, UL 94V-0.
TERMINALS: 0.305 THICK PHOS BRONZE PLATED WITH .0013mm MINIMUM THICK HARD GOLD IN LOCALIZED AREA AND .002mm MINIMUM THICK MATTE TIN IN SOLDER AREA OVER .0013mm MINIMUM THICK NICKEL UNDERPLATE

2. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F

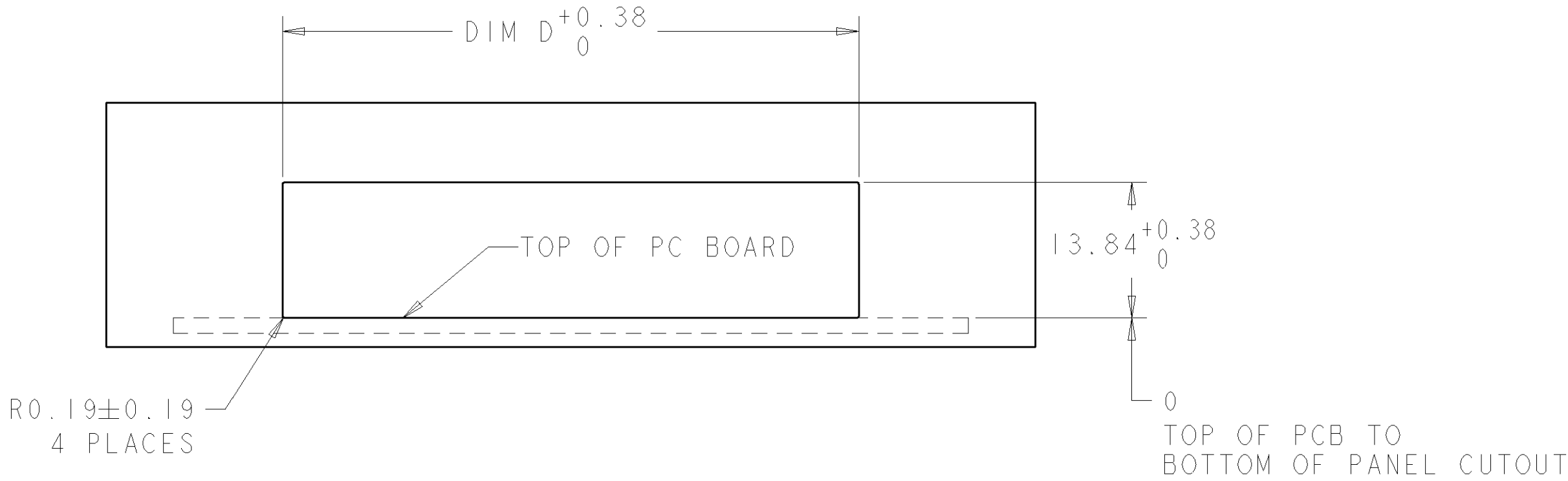
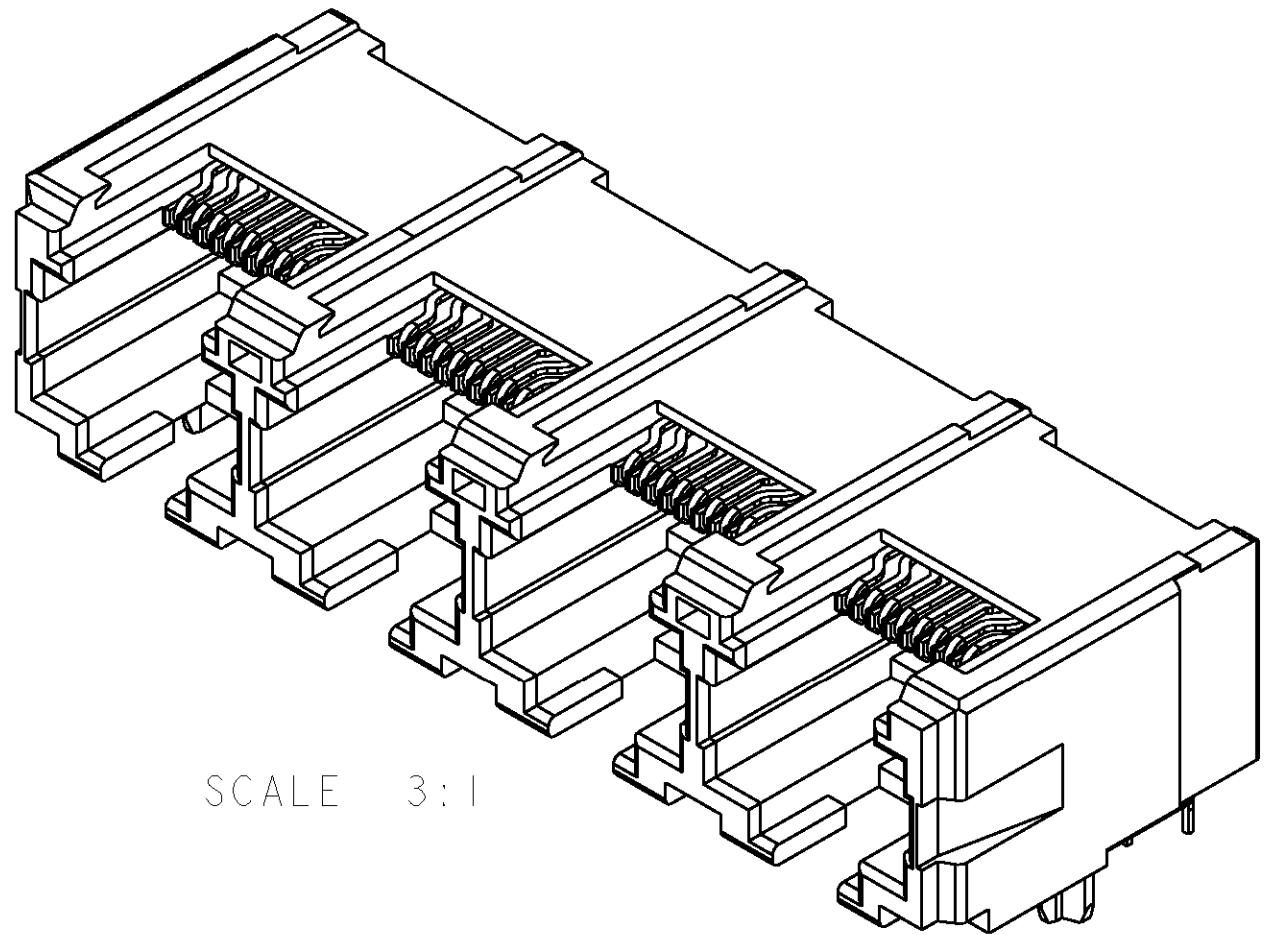
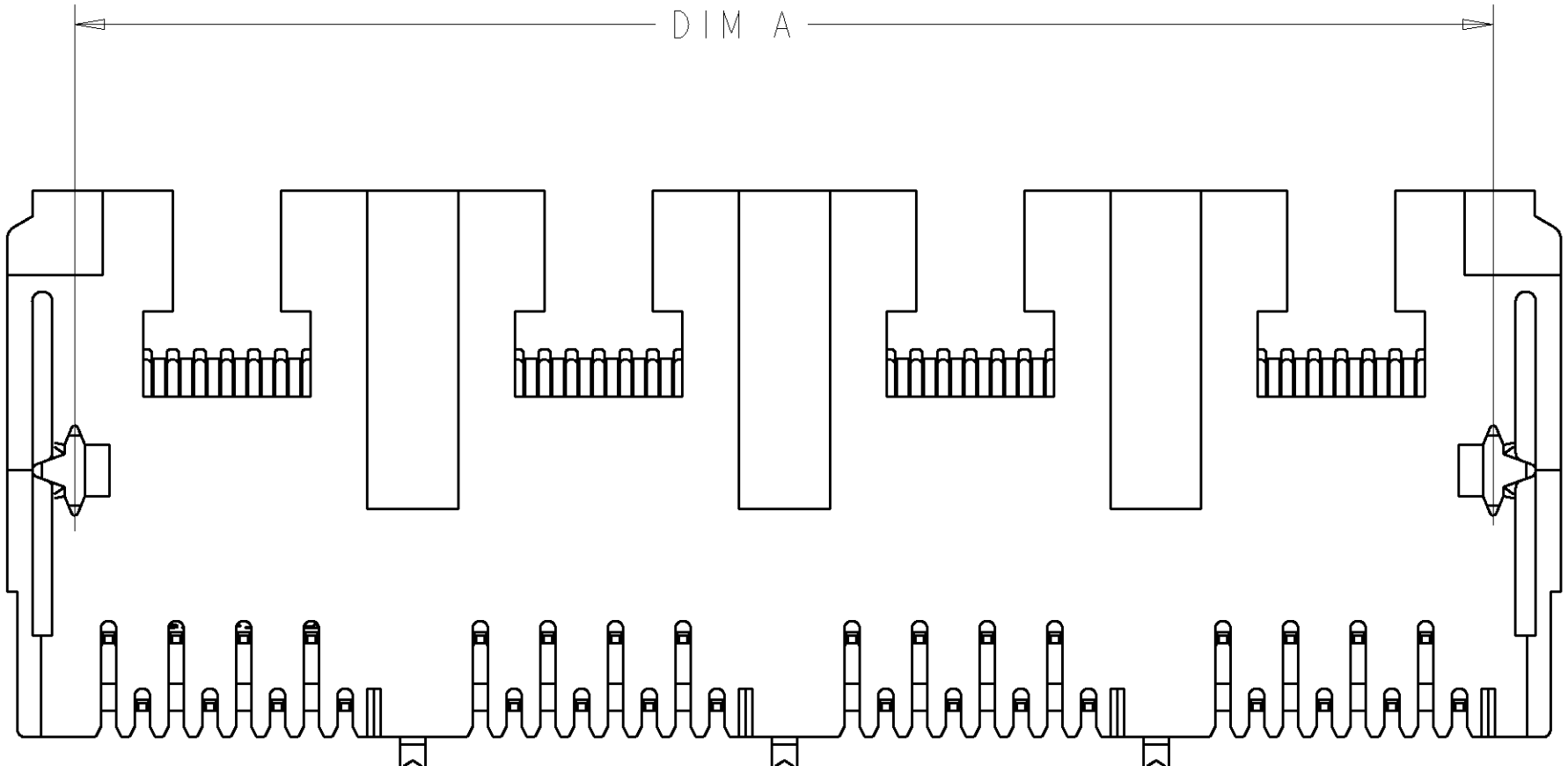
3. CENTER BOARD LOCATOR AND PCB HOLE INCLUDED ON 6339207-7 ONLY

4. USE #30 DRILL BIT OR 3.25mm DRILL BIT WHEN PRODUCING THESE PCB HOLES

5. RECOMMENDED APPLICATIONS: FAST ETHERNET, 10 BASE-T ETHERNET, AND 10/100 BASE-T ETHERNET (CAT 5 TWO PAIR 1/2, 3/6)



SUGGESTED PCB LAYOUT
VIEWED FROM COMPONENT SIDE
SCALE 2:1



SUGGESTED PANEL CUTOUT DIMENSIONS
SCALE: 2:1

3.56	114.8	54.61	114.3 (1X8)	109.22	6339207-7
3.05	58.9	-	58.42 (1X4)	53.34	6339207-1
DIM E	DIM D	DIM C	3 DIM B	DIM A	PART NUMBER
DIMENSIONS: mm					
TOLERANCES UNLESS OTHERWISE SPECIFIED:					
9 PLC $\pm$					
1 PLC ± 0.25					
3 PLC $\pm$					
4 PLC $\pm$					
ANGLES $\pm$					
FINISH					
MATERIAL SEE NOTE 1					
SEE NOTE 1					
CUSTOMER DRAWING					
SCALE 4:1 SHEET 1 OF 1 REV A					