

To all our customers

---

**Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.**

---

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.  
Customer Support Dept.  
April 1, 2003

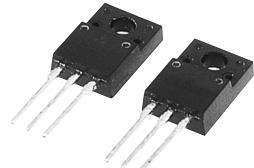
**PRELIMINARY**  
Notice: This is not a final specification.  
Some parametric limits are subject to change.

MITSUBISHI Pch POWER MOSFET

**FX30K MJ-2**

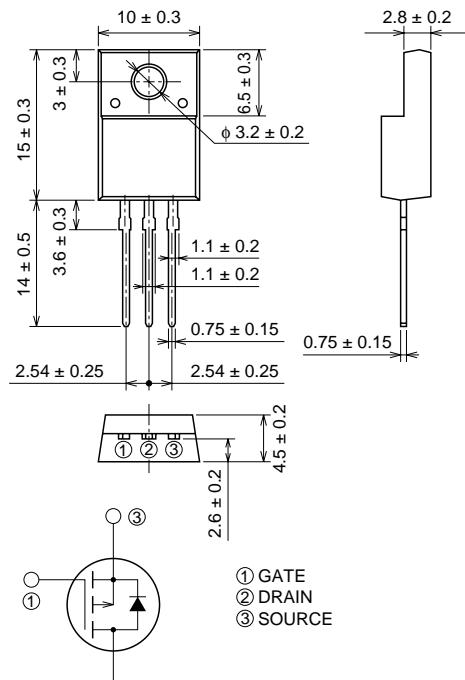
HIGH-SPEED SWITCHING USE

**FX30K MJ-2**



- 4V DRIVE
- $V_{DSS}$  ..... -100V
- $r_{DS\ (ON)}$  (MAX) .....  $0.143\Omega$
- $I_D$  ..... -30A
- Integrated Fast Recovery Diode (TYP.) ..... 100ns
- $V_{iso}$  ..... 2000V

**OUTLINE DRAWING**



TO-220FN

**APPLICATION**

Motor control, Lamp control, Solenoid control  
DC-DC converter, etc.

**MAXIMUM RATINGS** ( $T_C = 25^\circ\text{C}$ )

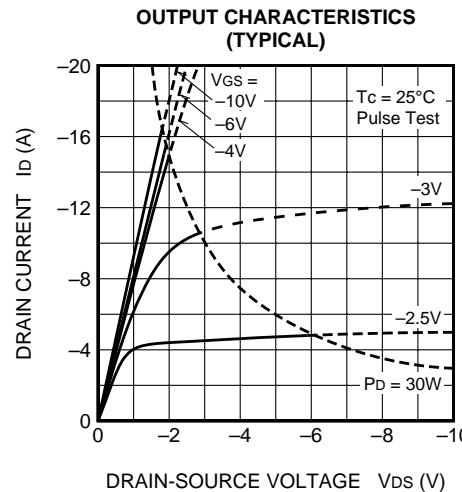
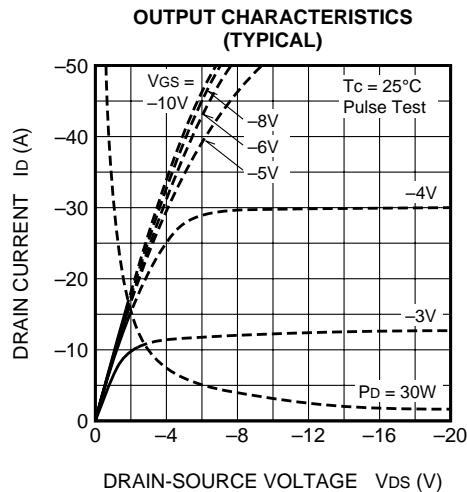
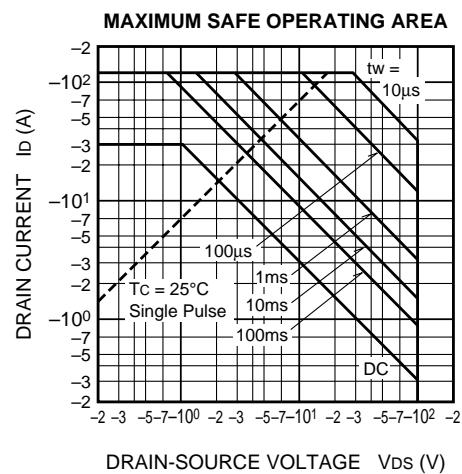
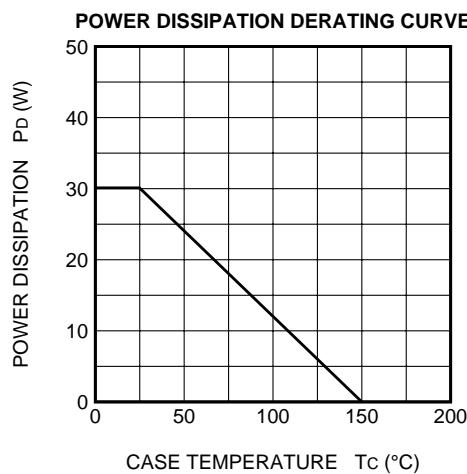
Symbol	Parameter	Conditions	Ratings	Unit
$V_{DSS}$	Drain-source voltage	$V_{GS} = 0\text{V}$	-100	V
$V_{GSS}$	Gate-source voltage	$V_{DS} = 0\text{V}$	$\pm 20$	V
$I_D$	Drain current		-30	A
$I_{DM}$	Drain current (Pulsed)		-120	A
$I_{DA}$	Avalanche drain current (Pulsed)	$L = 30\mu\text{H}$	-30	A
$I_S$	Source current		-30	A
$I_{SM}$	Source current (Pulsed)		-120	A
$P_D$	Maximum power dissipation		30	W
$T_{ch}$	Channel temperature		-55 ~ +150	$^\circ\text{C}$
$T_{stg}$	Storage temperature		-55 ~ +150	$^\circ\text{C}$
$V_{iso}$	Isolation voltage	AC for 1 minute, Terminal to case	2000	V
—	Weight	Typical value	2.0	g

Jan.1999

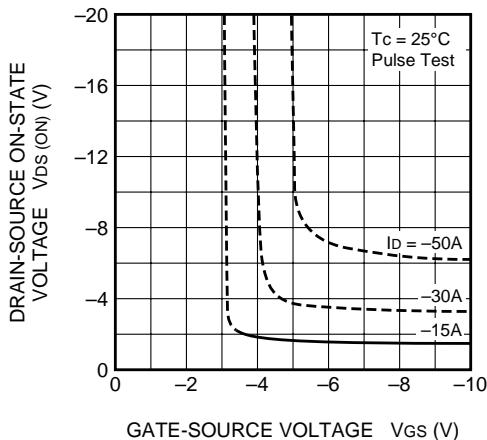
**ELECTRICAL CHARACTERISTICS (T<sub>ch</sub> = 25°C)**

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	Id = -1mA, V <sub>GS</sub> = 0V	-100	—	—	V
I <sub>GSS</sub>	Gate-source leakage current	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	—	—	±0.1	μA
I <sub>DSS</sub>	Drain-source leakage current	V <sub>DS</sub> = -100V, V <sub>GS</sub> = 0V	—	—	-0.1	mA
V <sub>GS</sub> (th)	Gate-source threshold voltage	Id = -1mA, V <sub>DS</sub> = -10V	-1.0	-1.5	-2.0	V
r <sub>D5 (ON)</sub>	Drain-source on-state resistance	Id = -15A, V <sub>GS</sub> = -10V	—	0.113	0.143	Ω
r <sub>D6 (ON)</sub>	Drain-source on-state resistance	Id = -15A, V <sub>GS</sub> = -4V	—	0.135	0.176	Ω
V <sub>DS</sub> (ON)	Drain-source on-state voltage	Id = -15A, V <sub>GS</sub> = -10V	—	-1.65	-2.15	V
y <sub>fs</sub>	Forward transfer admittance	Id = -15A, V <sub>DS</sub> = -10V	—	20	—	S
C <sub>iss</sub>	Input capacitance	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V, f = 1MHz	—	4450	—	pF
C <sub>oss</sub>	Output capacitance		—	330	—	pF
C <sub>rss</sub>	Reverse transfer capacitance		—	170	—	pF
t <sub>d (on)</sub>	Turn-on delay time		—	16	—	ns
t <sub>r</sub>	Rise time	V <sub>DD</sub> = -50V, Id = -15A, V <sub>GS</sub> = -10V, R <sub>GEN</sub> = R <sub>GS</sub> = 50Ω	—	54	—	ns
t <sub>d (off)</sub>	Turn-off delay time		—	270	—	ns
t <sub>f</sub>	Fall time		—	129	—	ns
V <sub>SD</sub>	Source-drain voltage	Is = -15A, V <sub>GS</sub> = 0V	—	-1.0	-1.5	V
R <sub>th (ch-c)</sub>	Thermal resistance	Channel to case	—	—	4.17	°C/W
t <sub>rr</sub>	Reverse recovery time	Is = -30A, dIs/dt = 100A/μs	—	100	—	ns

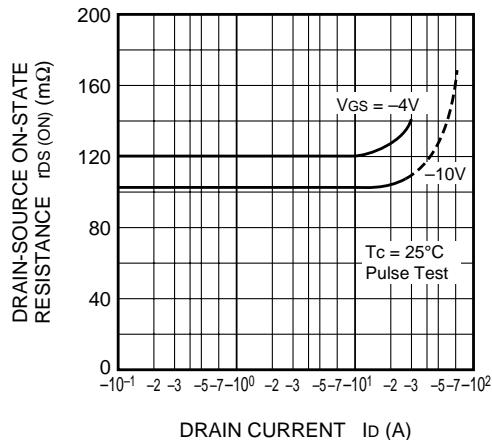
**PERFORMANCE CURVES**



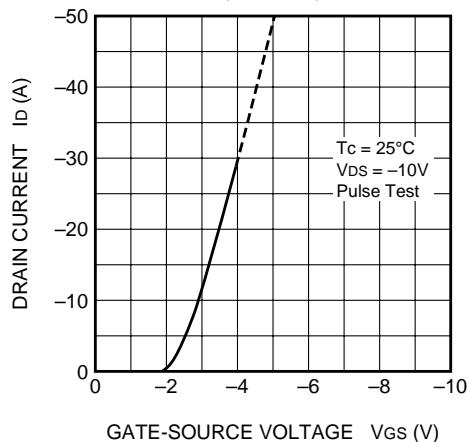
**ON-STATE VOLTAGE VS.  
GATE-SOURCE VOLTAGE  
(TYPICAL)**



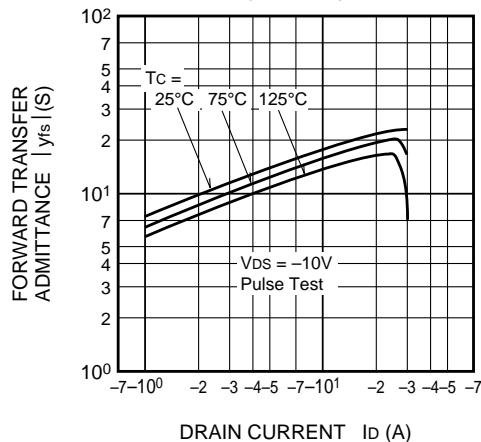
**ON-STATE RESISTANCE VS.  
DRAIN CURRENT  
(TYPICAL)**



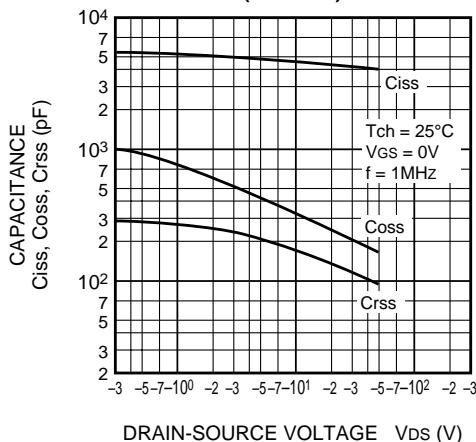
**TRANSFER CHARACTERISTICS  
(TYPICAL)**



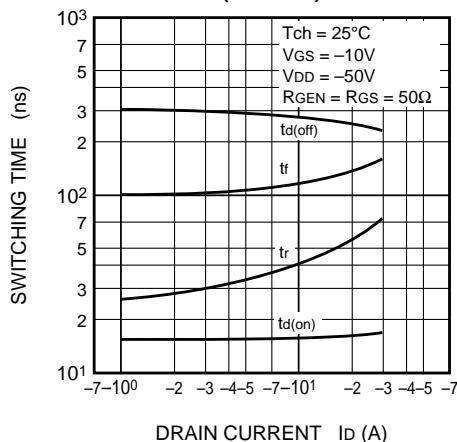
**FORWARD TRANSFER ADMITTANCE  
VS.DRAIN CURRENT  
(TYPICAL)**



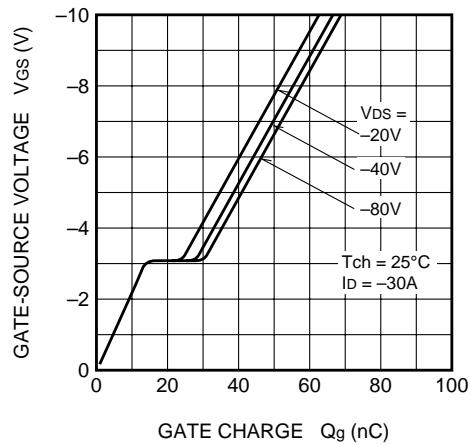
**CAPACITANCE VS.  
DRAIN-SOURCE VOLTAGE  
(TYPICAL)**



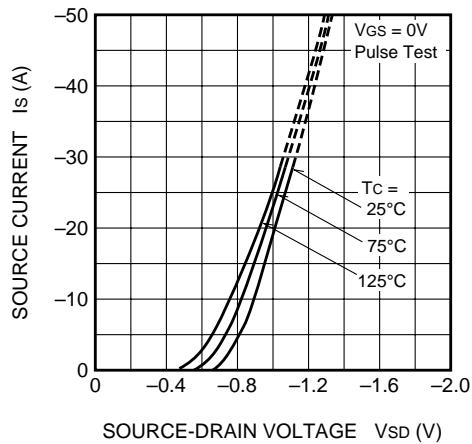
**SWITCHING CHARACTERISTICS  
(TYPICAL)**



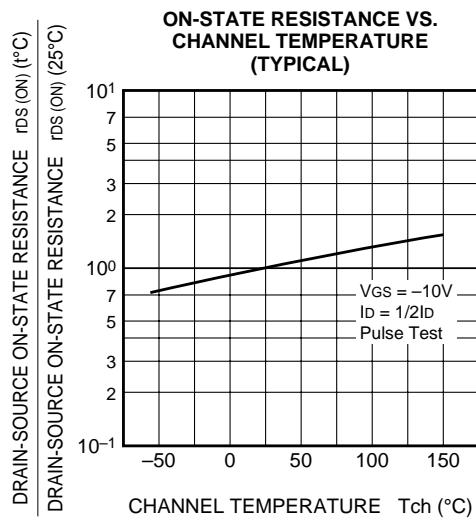
**GATE-SOURCE VOLTAGE  
VS.GATE CHARGE  
(TYPICAL)**



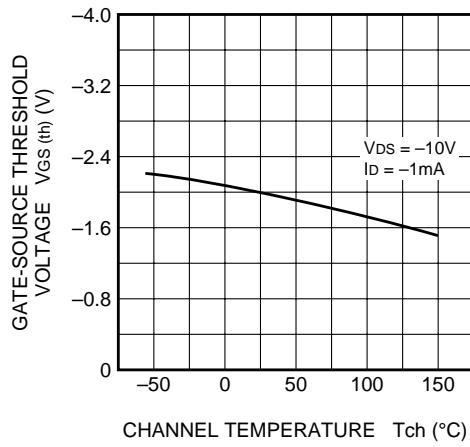
**SOURCE-DRAIN DIODE  
FORWARD CHARACTERISTICS  
(TYPICAL)**



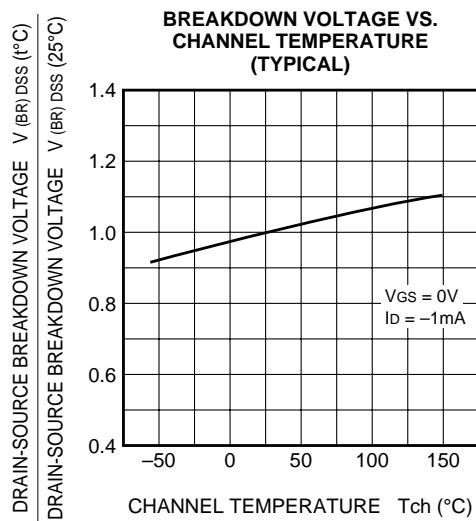
**ON-STATE RESISTANCE VS.  
CHANNEL TEMPERATURE  
(TYPICAL)**



**THRESHOLD VOLTAGE VS.  
CHANNEL TEMPERATURE  
(TYPICAL)**



**BREAKDOWN VOLTAGE VS.  
CHANNEL TEMPERATURE  
(TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE  
CHARACTERISTICS**

