

November 2003

Features

- Logic Level Gate Drive
- Internal Voltage Clamp
- ESD Gate Protection
- $T_J = 175^{\circ}\text{C}$
- Ignition Energy Capable

Description

This N-Channel IGBT is a MOS gated, logic level device which is intended to be used as an ignition coil driver in automotive ignition circuits. Unique features include an active voltage clamp between the collector and the gate which provides Self Clamped Inductive Switching (SCIS) capability in ignition circuits. Internal diodes provide ESD protection for the logic level gate. Both a series resistor and a shunt resistor are provided in the gate circuit.

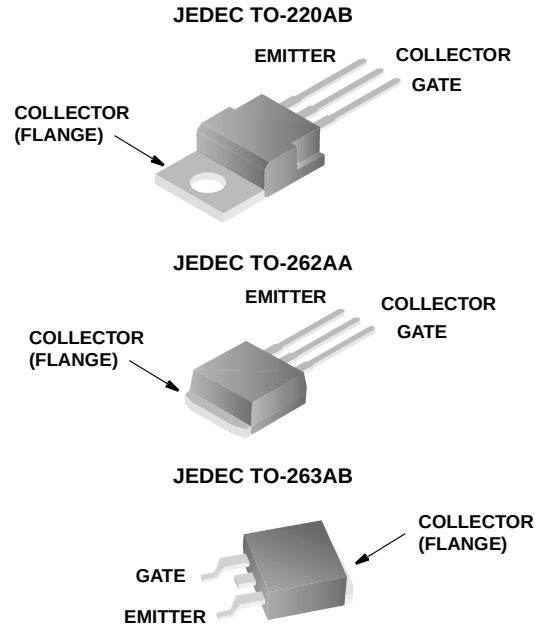
PACKAGING AVAILABILITY

PART NUMBER	PACKAGE	BRAND
HGTP20N35G3VL	T0-220AB	20N35GVL
HGT1S20N35G3VL	T0-262AA	20N35GVL
HGT1S20N35G3VLS	T0-263AB	20N35GVL

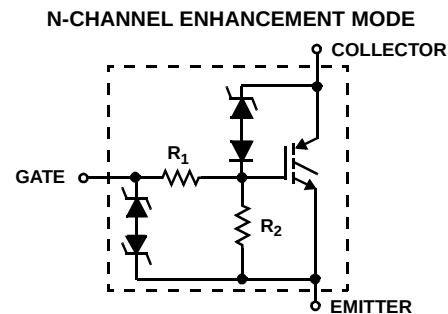
NOTE: When ordering, use the entire part number. Add the suffix 9A to obtain the TO-263AB variant in the tape and reel, i.e., HGT1S20N35G3VLS9A.

The development type number for this device is TA49076.

Packages



Terminal Diagram



Absolute Maximum Ratings $T_C = +25^{\circ}\text{C}$, Unless Otherwise Specified

	HGTP20N35G3VL HGT1S20N35G3VL HGT1S20N35G3VLS	UNITS
Collector-Emitter Bkdn Voltage At 10mA, $R_{GE} = 1\text{k}\Omega$	375	V
Emitter-Collector Bkdn Voltage At 10mA	24	V
Collector Current Continuous At $V_{GE} = 5.0\text{V}$, $T_C = +25^{\circ}\text{C}$, Figure 7	20	A
At $V_{GE} = 5.0\text{V}$, $T_C = +100^{\circ}\text{C}$	20	A
Gate-Emitter-Voltage (Note)	± 10	V
Inductive Switching Current At $L = 2.3\text{mH}$, $T_C = +25^{\circ}\text{C}$	21	A
At $L = 2.3\text{mH}$, $T_C = +150^{\circ}\text{C}$	16	A
Collector to Emitter Avalanche Energy At $L = 2.3\text{mH}$, $T_C = +25^{\circ}\text{C}$	500	mJ
Power Dissipation Total At $T_C = +25^{\circ}\text{C}$	150	W
Power Dissipation Derating $T_C > +25^{\circ}\text{C}$	1.0	W/ $^{\circ}\text{C}$
Operating and Storage Junction Temperature Range	-40 to +175	$^{\circ}\text{C}$
Maximum Lead Temperature for Soldering	260	$^{\circ}\text{C}$
Electrostatic Voltage at 100pF, 1500 Ω	6	KV

NOTE: May be exceeded if I_{GEM} is limited to 10mA.

Specifications HGTP20N35G3VL, HGT1S20N35G3VL, HGT1S20N35G3VLS

Electrical Specifications $T_C = +25^{\circ}\text{C}$, Unless Otherwise Specified

PARAMETERS	SYMBOL	TEST CONDITIONS		LIMITS			UNITS
				MIN	TYP	MAX	
Collector-Emitter Breakdown Voltage	BV _{CES}	I _C = 10mA, V _{GE} = 0V	T _C = +175°C	310	345	380	V
			T _C = +25°C	320	350	380	V
			T _C = -40°C	320	355	390	V
Collector-Emitter Breakdown Voltage	BV _{CER}	I _C = 10mA V _{GE} = 0V R _{GE} = 1kΩ	T _C = +175°C	300	340	375	V
			T _C = +25°C	315	345	375	V
			T _C = -40°C	315	350	390	V
Gate-Emitter Plateau Voltage	V _{GEP}	I _C = 10A V _{CE} = 12V	T _C = +25°C	-	3.7	-	V
Gate Charge	Q _{G(ON)}	I _C = 10A V _{GE} = 5V V _{CE} = 12V	T _C = +25°C	-	28.7	-	nC
Collector-Emitter Clamp Bkdn. Voltage	BV _{CE(CL)}	I _C = 10A R _G = 0Ω	T _C = +175°C	325	360	395	V
Emitter-Collector Breakdown Voltage	BV _{ECS}	I _C = 10mA	T _C = +25°C	20	32	-	V
Collector-Emitter Leakage Current	I _{CES}	V _{CE} = 250V	T _C = +25°C	-	-	5	μA
		V _{CE} = 250V	T _C = +175°C	-	-	250	μA
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	I _C = 10A V _{GE} = 4.5V	T _C = +25°C	-	1.3	1.6	V
			T _C = +175°C	-	1.25	1.5	V
		I _C = 20A V _{GE} = 5.0V	T _C = +25°C	-	1.6	2.8	V
			T _C = +175°C	-	1.9	3.5	V
Gate-Emitter Threshold Voltage	V _{GE(TH)}	I _C = 1mA V _{CE} = V _{GE}	T _C = +25°C	1.3	1.8	2.3	V
Gate Series Resistance	R ₁		T _C = +25°C	-	1.0	-	kΩ
Gate-Emitter Resistance	R ₂		T _C = +25°C	10	17	25	kΩ
Gate-Emitter Leakage Current	I _{GES}	V _{GE} = ±10V		±400	±590	±1000	μA
Gate-Emitter Breakdown Voltage	BV _{GES}	I _{GES} = ±2mA		±12	±14	-	V
Current Turn-Off Time-Inductive Load	t _{D(OFF)} I + t _{F(OFF)} I	I _C = 10A, R _G = 25Ω, L = 550μH, R _L = 26.4Ω, V _{GE} = 5V, V _{CL} = 300V, T _C = +175°C		-	15	30	μs
Inductive Use Test	I _{SCIS}	L = 2.3mH, V _G = 5V, R _G = 0Ω	T _C = +150°C	16	-	-	A
			T _C = +25°C	21	-	-	A
Thermal Resistance	R _{θJC}			-	-	1.0	°C/W

Typical Performance Curves

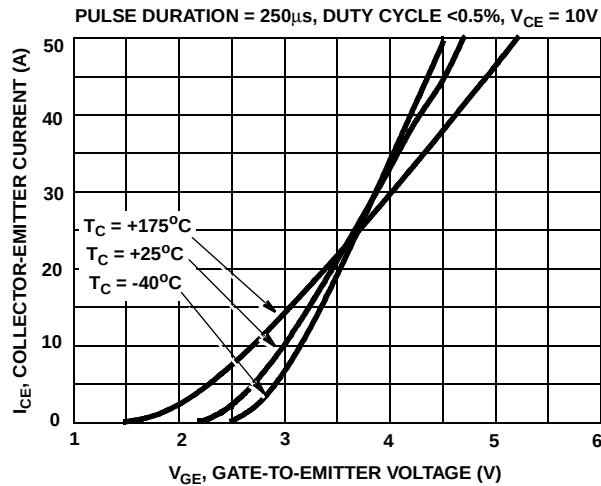


FIGURE 1. TRANSFER CHARACTERISTICS

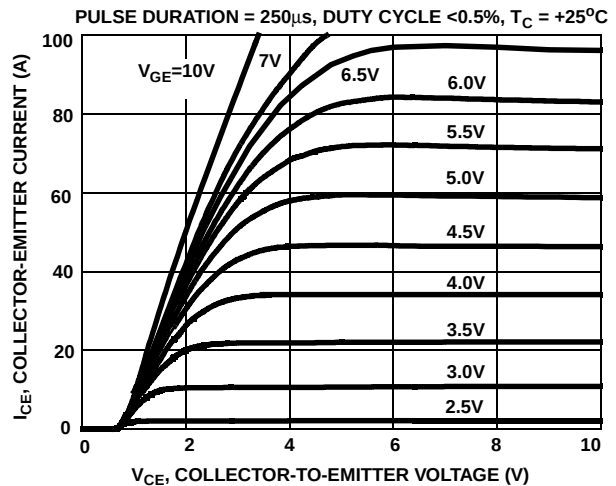


FIGURE 2. SATURATION CHARACTERISTICS

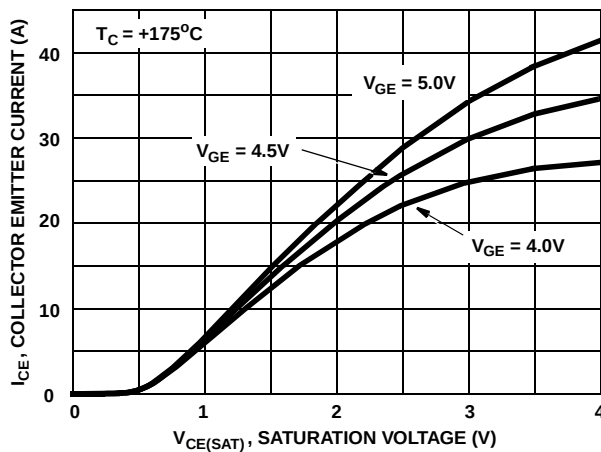


FIGURE 3. COLLECTOR-EMITTER CURRENT AS A FUNCTION OF SATURATION VOLTAGE

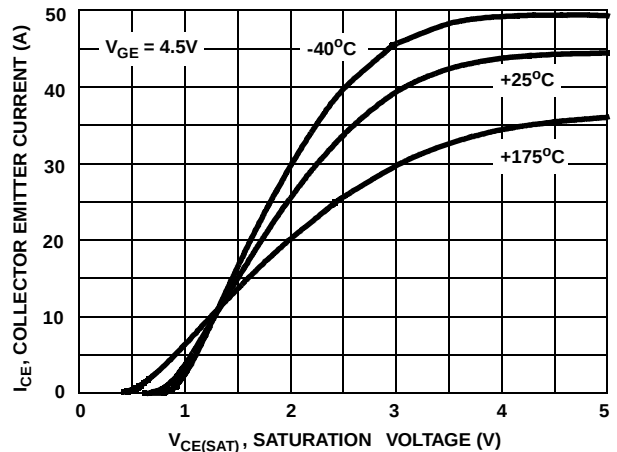


FIGURE 4. COLLECTOR-EMITTER CURRENT AS A FUNCTION OF SATURATION VOLTAGE

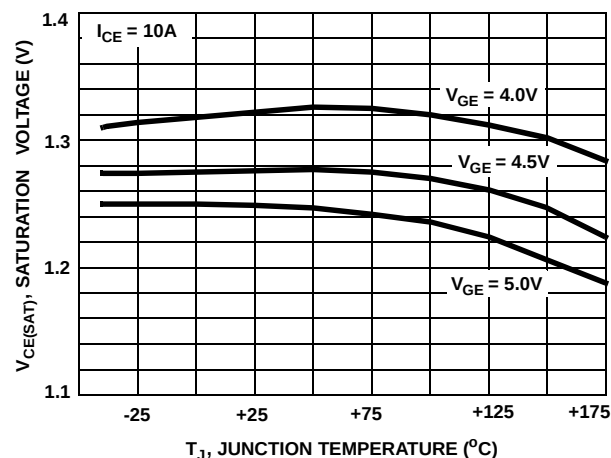


FIGURE 5. SATURATION VOLTAGE AS A FUNCTION OF JUNCTION TEMPERATURE

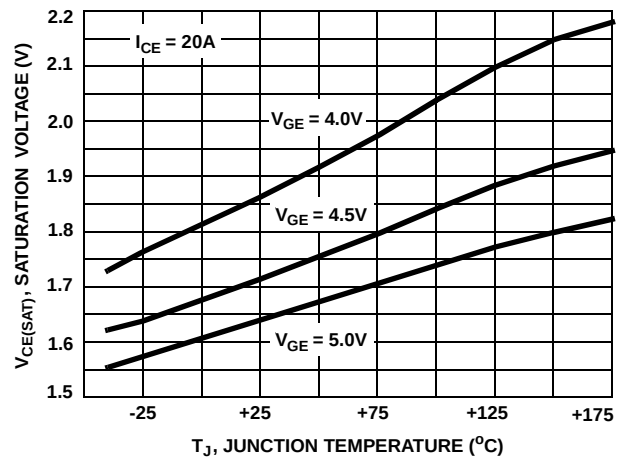


FIGURE 6. SATURATION VOLTAGE AS A FUNCTION OF JUNCTION TEMPERATURE

Typical Performance Curves (Continued)

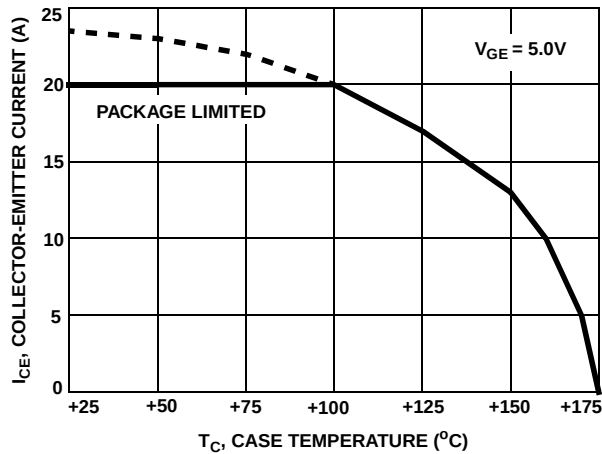


FIGURE 7. COLLECTOR-EMITTER CURRENT AS A FUNCTION OF CASE TEMPERATURE

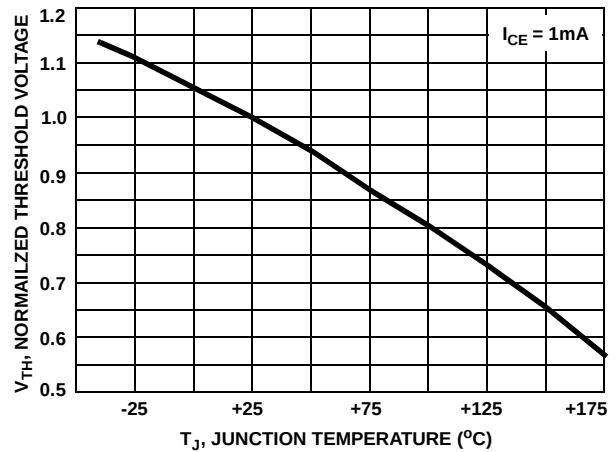


FIGURE 8. NORMALIZED THRESHOLD VOLTAGE AS A FUNCTION OF JUNCTION TEMPERATURE

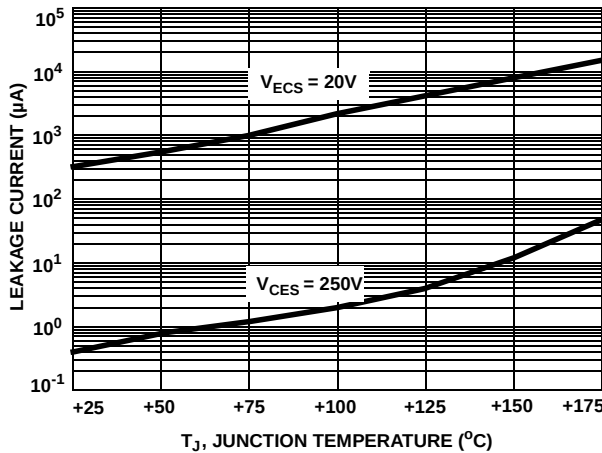


FIGURE 9. LEAKAGE CURRENT AS A FUNCTION OF JUNCTION TEMPERATURE

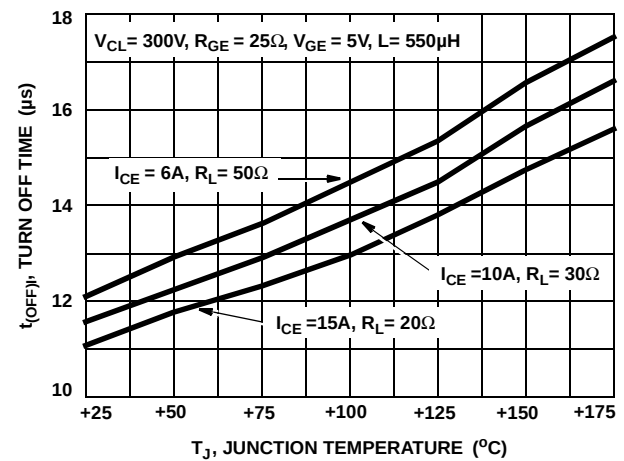


FIGURE 10. TURN-OFF TIME AS A FUNCTION OF JUNCTION TEMPERATURE

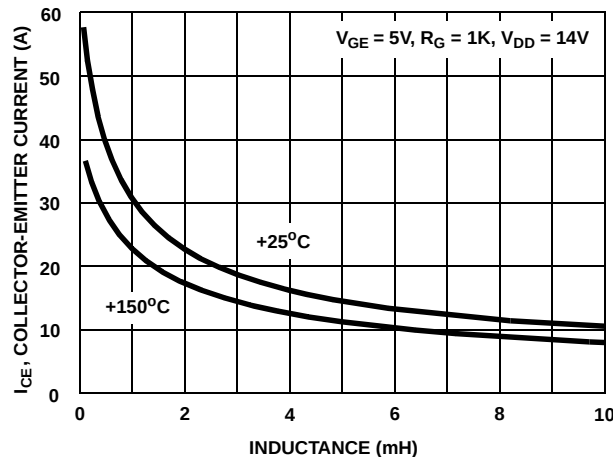


FIGURE 11. SELF CLAMPED INDUCTIVE SWITCHING CURRENT vs INDUCTANCE

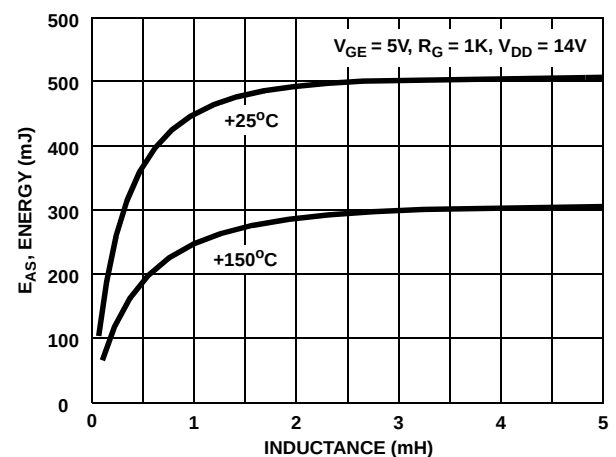


FIGURE 12. SELF CLAMPED INDUCTIVE SWITCHING ENERGY vs INDUCTANCE

Typical Performance Curves (Continued)

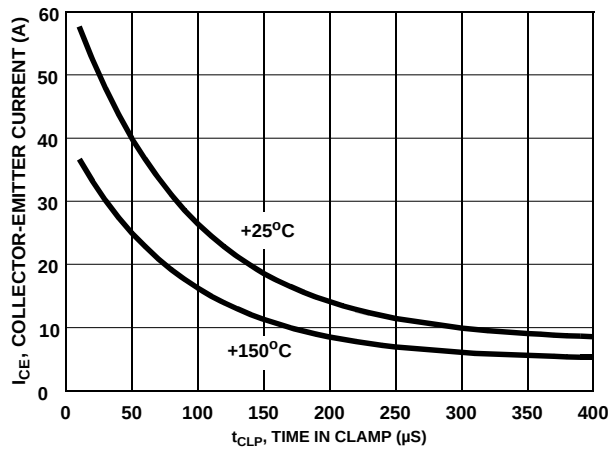


FIGURE 13. SELF CLAMPED INDUCTIVE SWITCHING CURRENT vs TIME IN CLAMP

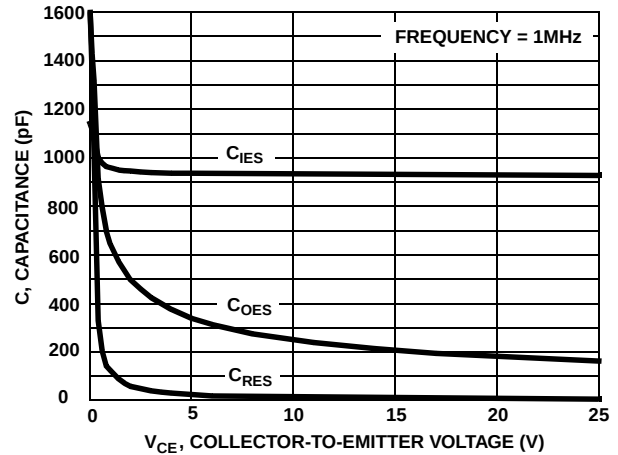


FIGURE 14. CAPACITANCE AS A FUNCTION OF COLLECTOR-EMITTER VOLTAGE

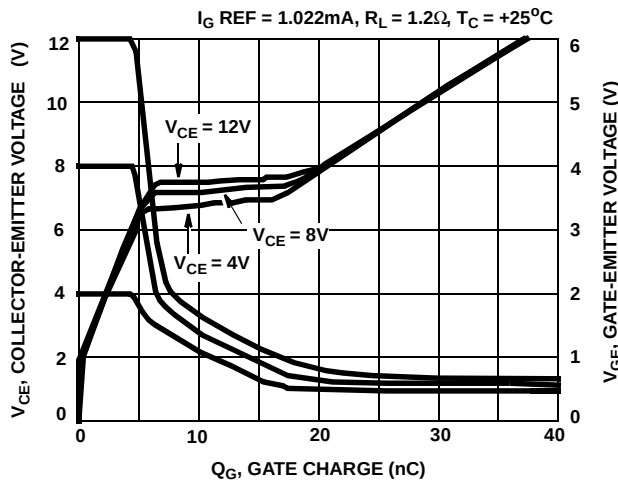


FIGURE 15. GATE CHARGE

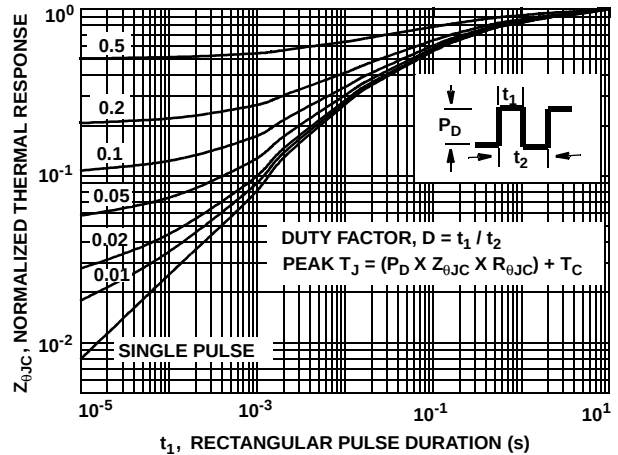


FIGURE 16. NORMALIZED TRANSIENT THERMAL IMPEDANCE, JUNCTION TO CASE

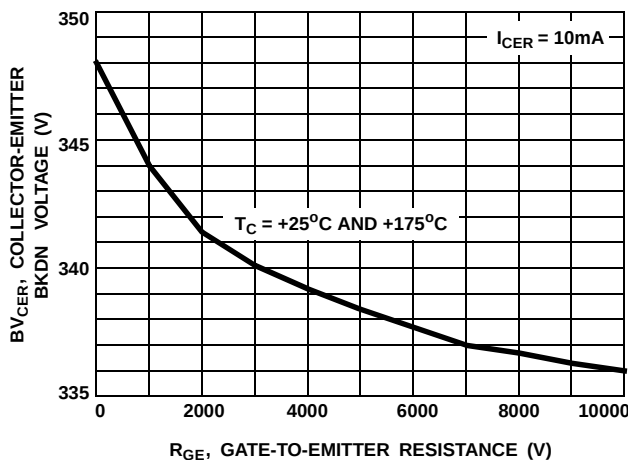


FIGURE 17. BREAKDOWN VOLTAGE AS A FUNCTION OF GATE - Emitter RESISTANCE

Test Circuits

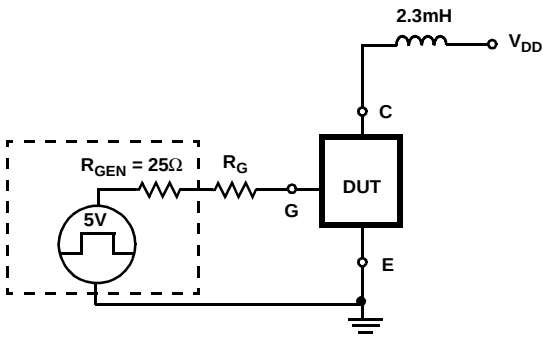


FIGURE 18. USE TEST CIRCUIT

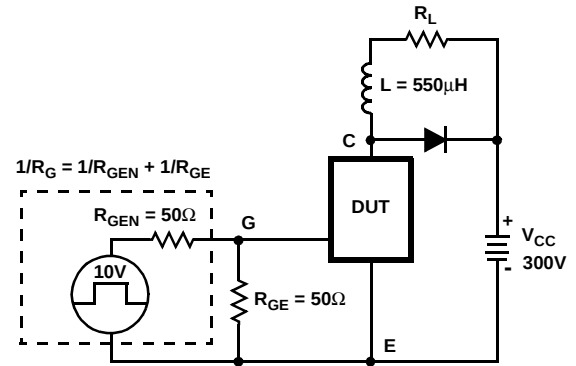


FIGURE 19. INDUCTIVE SWITCHING TEST CIRCUIT

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FACT Quiet Series™	LittleFET™	Power247™	SuperSOT™-6
ActiveArray™	FAST®	MICROCOUPLER™	PowerTrench®	SuperSOT™-8
Bottomless™	FASTr™	MicroFET™	QFET®	SyncFET™
CoolFET™	FRFET™	MicroPak™	QS™	TinyLogic®
CROSSVOLT™	GlobalOptoisolator™	MICROWIRE™	QT Optoelectronics™	TINYOPTO™
DOME™	GTO™	MSX™	Quiet Series™	TruTranslation™
EcoSPARK™	HiSeC™	MSXPro™	RapidConfigure™	UHC™
E ² CMOS™	I ² C™	OCX™	RapidConnect™	UltraFET®
EnSigna™	ImpliedDisconnect™	OCXPro™	SILENT SWITCHER®	VCX™
FACT™	ISOPPLANAR™	OPTOLOGIC®	SMART START™	
Across the board. Around the world.™	OPTOPLANAR™	SPM™		
The Power Franchise™	PACMAN™	Stealth™		
Programmable Active Droop™	POP™	SuperSOT™-3		

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.