

### INTELLIGENT POWER LOW SIDE SWITCH

#### Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

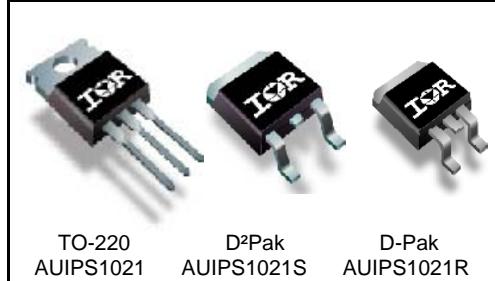
#### Product Summary

Rds(on)	25mΩ (max.)
Vclamp	39V
Ishutdown	45A (typ.)

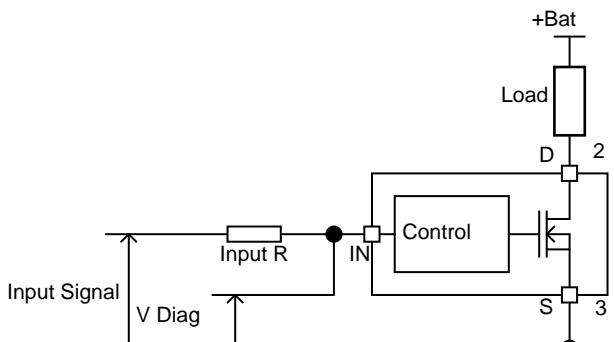
#### Description

The AUIPS1021(S)(R) is a three terminal Intelligent Power Switch (IPS) that features a low side MOSFET with over-current, over-temperature, ESD protection and drain to source active clamp. This device offers protections and the high reliability required in harsh environments. The switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 45A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

#### Packages



#### Typical Connection



**Qualification Information<sup>†</sup>**

<b>Qualification Level</b>		Automotive (per AEC-Q100 <sup>††</sup> )  Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
<b>Moisture Sensitivity Level</b>		DPAK-3L	MSL1, 260°C (per IPC/JEDEC J-STD-020)
		D2PAK-3L	MSL1, 260°C (per IPC/JEDEC J-STD-020)
		TO220-5L	Not applicable
<b>ESD</b>	Machine Model	Class M4 (+/-450V) (per AEC-Q100-003)	
	Human Body Model	Class H2 (+/-2500V) (per AEC-Q100-002)	
	Charged Device Model	Class C4 (+/-1000V) (per AEC-Q100-011)	
<b>IC Latch-Up Test</b>		Class II, Level A (per AEC-Q100-004)	
<b>RoHS Compliant</b>		Yes	

<sup>†</sup> Qualification standards can be found at International Rectifier's web site <http://www.irf.com/>

<sup>††</sup> Exceptions to AEC-Q100 requirements are noted in the qualification report.

## Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. ( $T_j = -40^\circ\text{C}..150^\circ\text{C}$ ,  $V_{cc} = 6..36\text{V}$  unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
$V_{ds}$	Maximum drain to source voltage	-0.3	36	V
$V_{ds}$ cont.	Maximum continuous drain to source voltage	-	28	V
$V_{in}$	Maximum input voltage	-0.3	6	V
$I_{sd}$ cont.	Max. diode continuous current (limited by thermal dissipation)	—	4.5	A
$P_d$	Maximum power dissipation (internally limited by thermal protection)	—	25	W
	$R_{th} = 5^\circ\text{C}/\text{W}$ AUIPS1021	—	3.1	
	$R_{th} = 40^\circ\text{C}/\text{W}$ AUIPS1021S 1" sqr. footprint	—	2.5	
$T_j$ max.	Max. storage & operating temperature junction temperature	-40	150	°C

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{th1}$	Thermal resistance junction to ambient AUIPS1021 TO-220 free air	50	—	°C/W
$R_{th2}$	Thermal resistance junction to case IPS1021 TO-220	2.6	—	
$R_{th1}$	Thermal resistance junction to ambient IPS1021S D <sup>2</sup> Pak std. footprint	60	—	
$R_{th2}$	Thermal resistance junction to ambient IPS1021S D <sup>2</sup> Pak 1" sqr. footprint	40	—	
$R_{th3}$	Thermal resistance junction to case IPS1021S D <sup>2</sup> Pak	2.6	—	
$R_{th1}$	Thermal resistance junction to ambient IPS1021R D-Pak std. footprint	70	—	
$R_{th2}$	Thermal resistance junction to ambient IPS1021R D-Pak 1" sqr. footprint	50	—	
$R_{th3}$	Thermal resistance junction to case IPS1021R D-Pak	2.6	—	

## Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
$V_{IH}$	High level input voltage	4.5	5.5	
$V_{IL}$	Low level input voltage	0	0.5	
$I_{ds}$	Continuous drain current, $T_{ambient} = 85^\circ\text{C}$ , $T_j = 125^\circ\text{C}$ , $V_{in} = 5\text{V}$	—	13.5	A
	$R_{th} = 5^\circ\text{C}/\text{W}$ AUIPS1021	—	4.8	
	$R_{th} = 40^\circ\text{C}/\text{W}$ AUIPS1021S 1" sqr. footprint	—	4.3	
	$R_{th} = 50^\circ\text{C}/\text{W}$ AUIPS1021R 1" sqr. footprint	—	—	
$R_{in}$	Recommended resistor in series with IN pin to generate a diagnostic	0.5	10	kΩ
$Max\ L$	Max. recommended load inductance (including line inductance) (1)	—	20	μH
$Max\ F$	Max. frequency (switching losses = conduction losses)	—	500	Hz
$Max\ t\ rise$	Max. input rising time	—	1	μs

(1) Higher inductance is possible if maximum load current is limited - see figure 11

## Static Electrical Characteristics

T<sub>j</sub>= -40..150°C, V<sub>cc</sub>=6..28V (unless otherwise specified), typical value are given for T<sub>j</sub>=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R <sub>ds(on)</sub>	ON state resistance T <sub>j</sub> =25°C	—	20	25	mΩ	Vin=5V, I <sub>ds</sub> =8A
	ON state resistance T <sub>j</sub> =150°C (2)	—	38	48		
I <sub>dss1</sub>	Drain to source leakage current	—	0.1	2	μA	V <sub>cc</sub> =14V, T <sub>j</sub> =25°C
I <sub>dss2</sub>	Drain to source leakage current	—	0.2	4		V <sub>cc</sub> =28V, T <sub>j</sub> =25°C
V clamp1	Drain to source clamp voltage 1	36	39	—	V	I <sub>d</sub> =20mA
V clamp2	Drain to source clamp voltage 2	—	39	42		I <sub>d</sub> =2A
V <sub>in</sub> clamp	IN to source pin clamp voltage	5.5	6.5	7.5		I <sub>in</sub> =1mA
V <sub>th</sub>	Input threshold voltage	—	1.7	—		I <sub>d</sub> =10mA

## Switching Electrical Characteristics

V<sub>cc</sub>=14V, Resistive load=1.5Ω, R<sub>input</sub>=0Ω, V<sub>in</sub>=5V, T<sub>j</sub>=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T <sub>don</sub>	Turn-on delay time to 20%	10	30	100	μs	See figure 2
T <sub>r</sub>	Rise time 20% to 80%	10	30	60		
T <sub>off</sub>	Turn-off delay time to 80%	40	150	400		
T <sub>f</sub>	Fall time 80% to 20%	15	30	60		
E <sub>on</sub> + E <sub>off</sub>	Turn on and off energy	—	2	—		

## Protection Characteristics

T<sub>j</sub>= -40..150°C, V<sub>cc</sub>=6..28V (unless otherwise specified), typical value are given for T<sub>j</sub>=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T <sub>sd</sub>	Over temperature threshold	150(2)	165	—	°C	See figure 1
I <sub>sd</sub>	Over current threshold	20	45	58	A	See figure 1
O <sub>V</sub>	Over voltage protection ( not active when the device is ON )	34	37	—	V	
V <sub>reset</sub>	IN protection reset threshold	—	1.7	—	V	
T <sub>reset</sub>	Time to reset protection	15(2)	50	200	μs	V <sub>in</sub> =0V

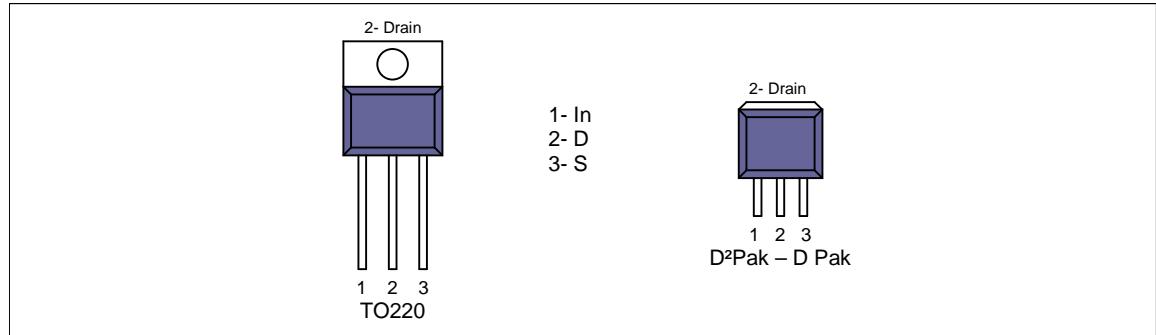
## Diagnostic

T<sub>j</sub>= -40..150°C, V<sub>cc</sub>=6..28V (unless otherwise specified), typical value are given for T<sub>j</sub>=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I <sub>in, on</sub>	ON state IN positive current	15	32	70	μA	Vin=5V Vin=5V
I <sub>in, off</sub>	OFF state IN positive current (after protection latched)	150	230	350		

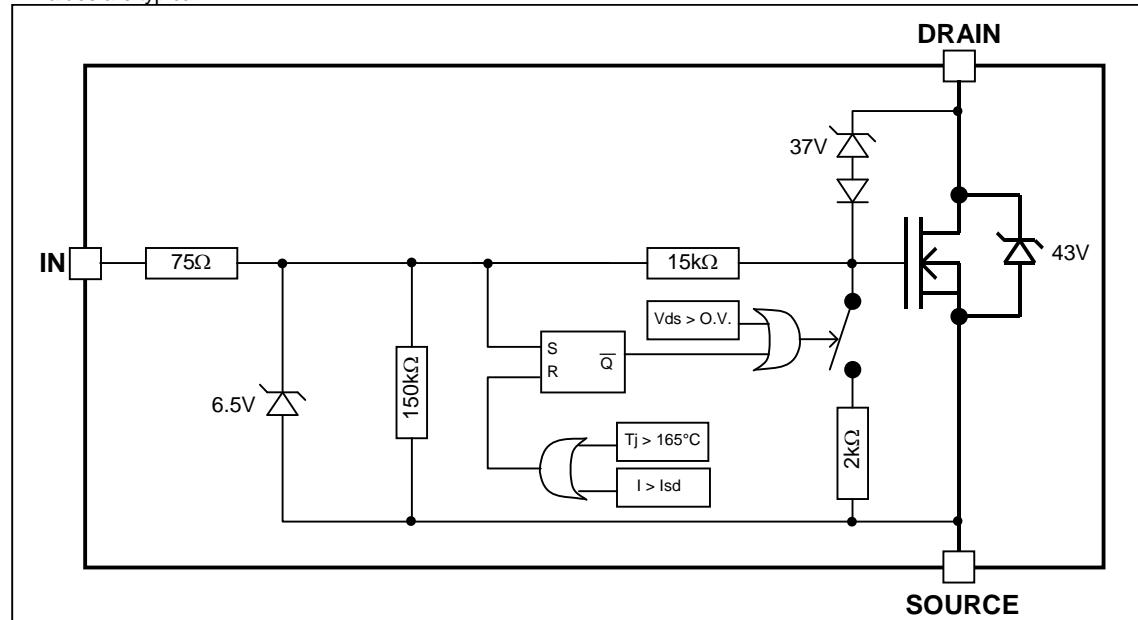
(2) Guaranteed by design

## Lead Assignments

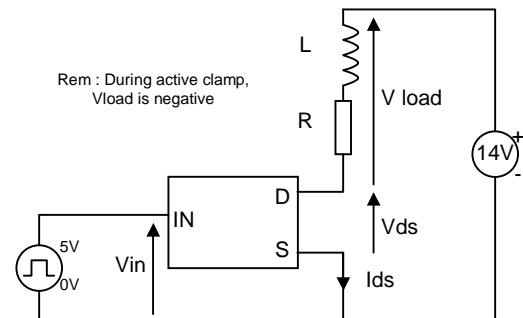
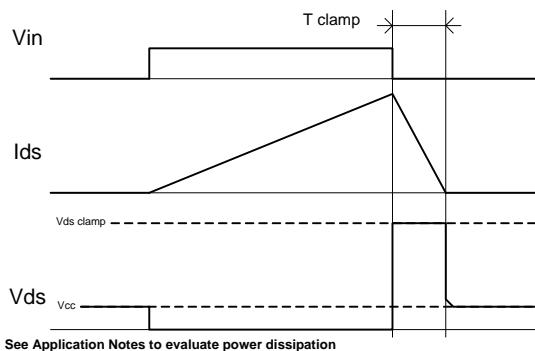
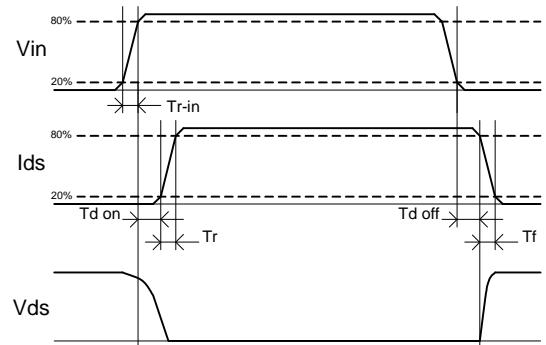
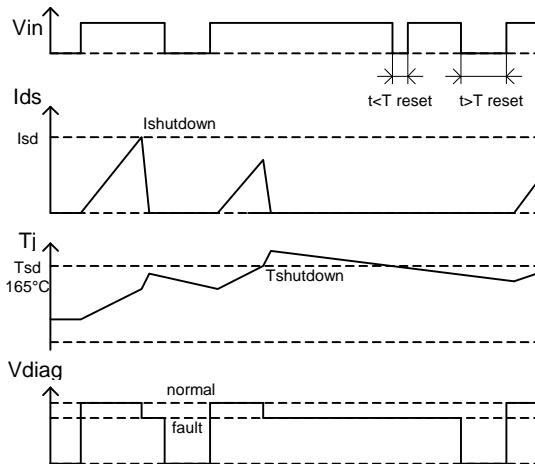


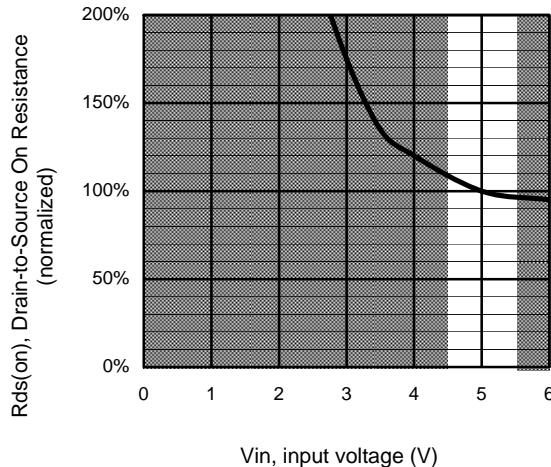
## Functional Block Diagram

All values are typical

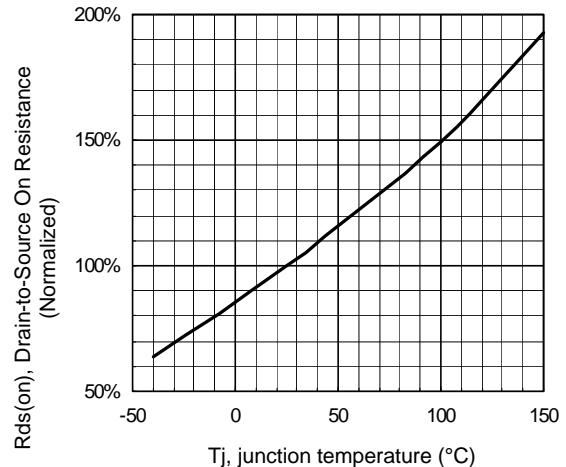


All curves are typical values. Operating in the shaded area is not recommended.

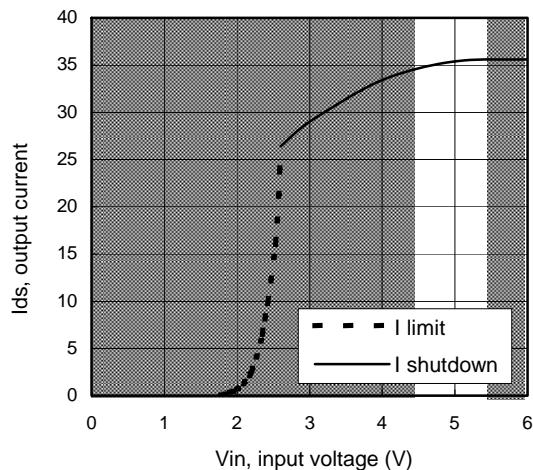




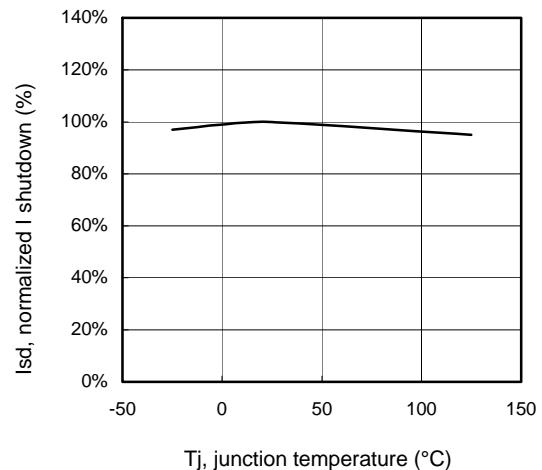
**Figure 5 – Normalized R<sub>ds(on)</sub> (%) Vs Input voltage (V)**



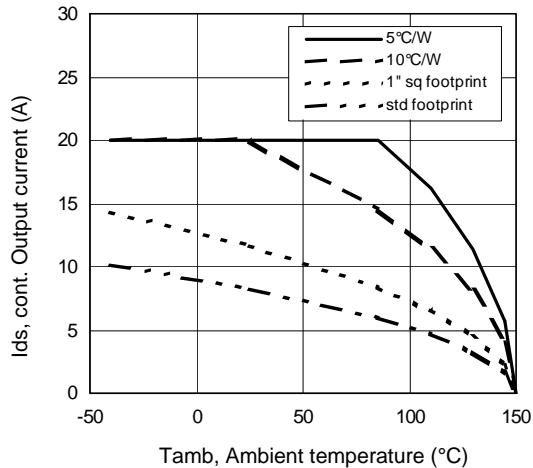
**Figure 6 - Normalized R<sub>ds(on)</sub> (%) Vs T<sub>j</sub> (°C)**



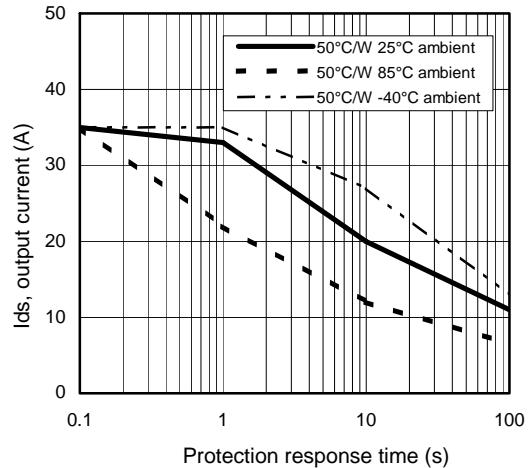
**Figure 7 – Current limitation and current shutdown Vs Input voltage (V)**



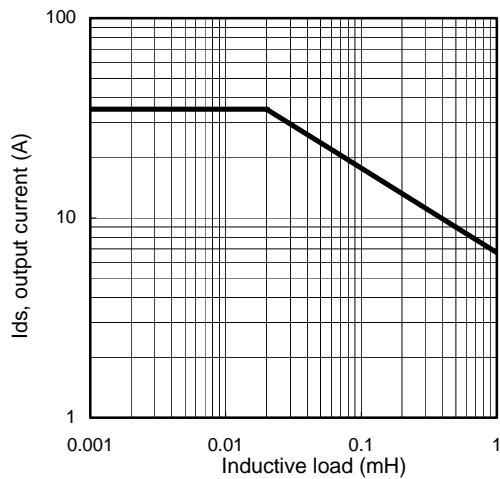
**Figure 8 – Normalized I shutdown (%) Vs junction temperature (°C)**



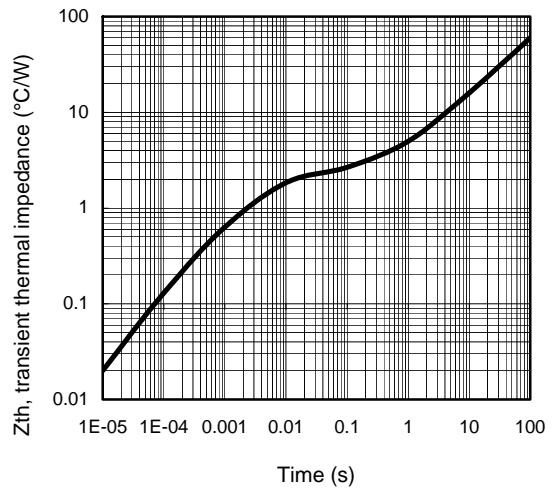
**Figure 9 – Max. continuous output current (A) Vs Ambient temperature (°C)**



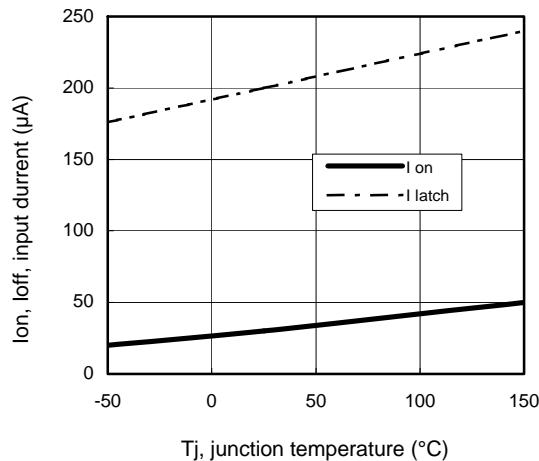
**Figure 10 –  $I_{DS}$  (A) Vs over temperature protection response time (s)**



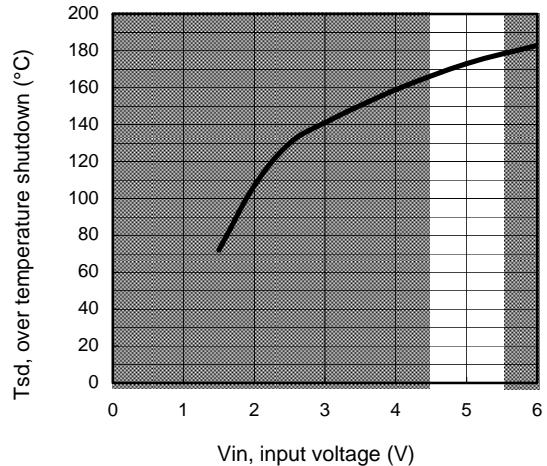
**Figure 11 – Max. output current (A) Vs Inductive load (mH)**



**Figure 12 – Transient thermal impedance (°C/W) Vs time (s)**

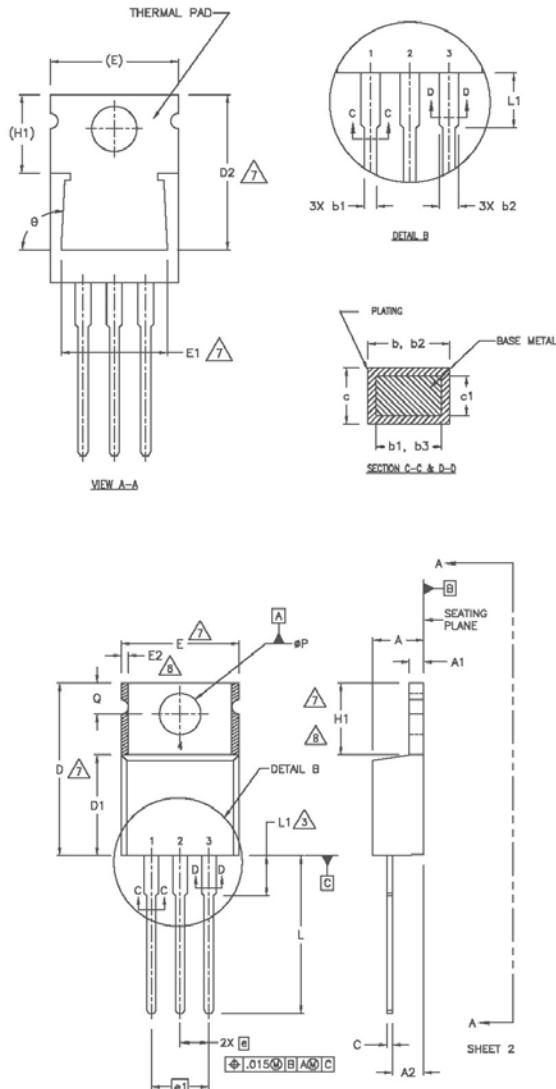


**Figure 13 – Input current ( $\mu\text{A}$ ) On and Off  
Vs junction temperature ( $^{\circ}\text{C}$ )**



**Figure 14 – Over temperature shutdown ( $^{\circ}\text{C}$ )  
Vs input voltage ( $\text{V}$ )**

**Case Outline – TO-220 AB – Automotive Q100 PbF qualified**

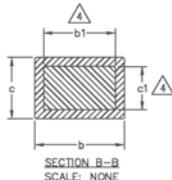
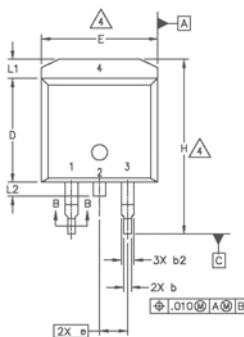
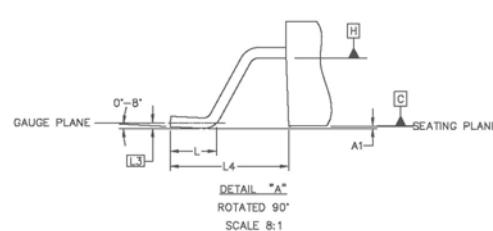
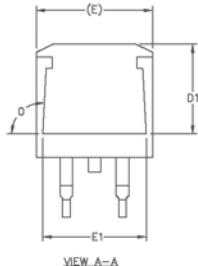


SYMBOL	DIMENSIONS				NOTES	
	MILLIMETERS		INCHES			
	MIN.	MAX.	MIN.	MAX.		
A	3.56	4.82	.140	.190		
A1	0.51	1.40	.020	.055		
A2	2.04	2.92	.080	.115		
b	0.38	1.01	.015	.040		
b1	0.38	0.96	.015	.038	5	
b2	1.15	1.77	.045	.070		
b3	1.15	1.73	.045	.068		
c	0.36	0.61	.014	.024		
c1	0.36	0.56	.014	.022	5	
D	14.22	16.51	.560	.650	4	
D1	8.38	9.02	.330	.355		
D2	12.19	12.88	.480	.507	7	
E	9.66	10.66	.380	.420	4,7	
E1	8.38	8.89	.330	.350	7	
e	2.54	BSC	.100	BSC		
e1	5.08		.200	BSC		
H1	5.85	6.55	.230	.270	7,8	
L	12.70	14.73	.500	.580		
L1	—	6.35	—	.250	3	
ØP	3.54	4.08	.139	.161		
Q	2.54	3.42	.100	.135		
Ø	90°–93°		90°–93°			

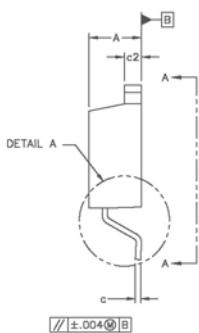
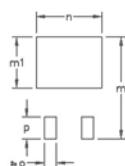
NOTES:

- 1 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
- 2 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- 3 LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
- 4 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 5 DIMENSION b1 & c1 APPLY TO BASE METAL ONLY.
- 6 CONTROLLING DIMENSION : INCHES.
- 7 THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1
- 8 DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED.
- 9 LEADS AND DRAIN ARE PLATED WITH 100% Sn

**Case Outline - D<sup>2</sup>Pak (SMD-220) - Automotive Q100 PbF MSL1 qualified**



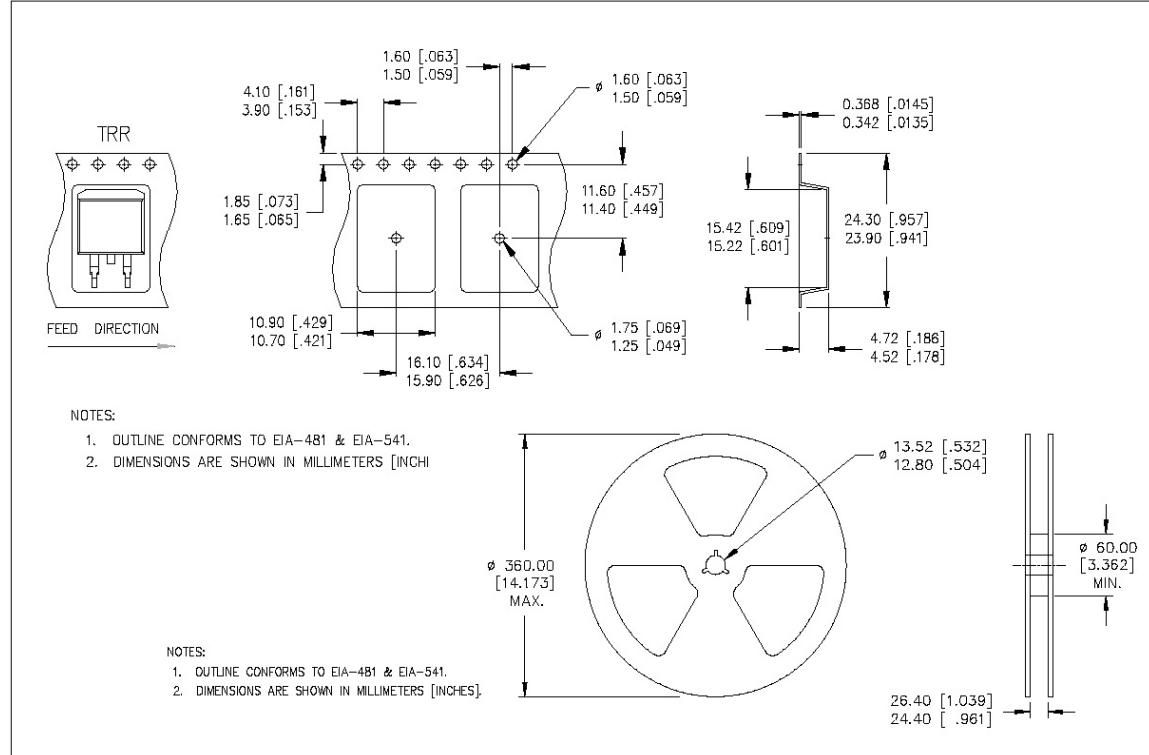
SYMBOL	DIMENSIONS				NOTES	
	MILLIMETERS		INCHES			
	MIN.	MAX.	MIN.	MAX.		
A	4.05	4.83	.160	.190		
A1	0.00	0.254	.000	.010		
b	0.51	0.99	.020	.039		
b1	0.51	0.89	.020	.035		
b2	1.14	1.78	.045	.070		
c	0.38	0.74	.015	.029		
c1	0.38	0.58	.015	.023	4	
c2	1.14	1.65	.045	.065		
D	8.51	9.65	.335	.380	3	
D1	8.86		.270			
E	9.65	10.67	.380	.420	3	
E1	6.22		.245			
e	2.54	BSC	.100	BSC		
H	14.61	15.88	.575	.625		
L	1.78	2.79	.070	.110		
L1		1.65		.065		
L2	1.27	1.78	.050	.070		
L3	0.25	BSC	.010	BSC		
L4	4.78	5.28	.188	.208		
m	17.78		.700			
m1	8.89		.350			
n	11.43		.450			
o	2.08		.082			
p	3.81		.150			
R	0.51	0.71	.020	.028		
θ	90°	93°	90°	93°		



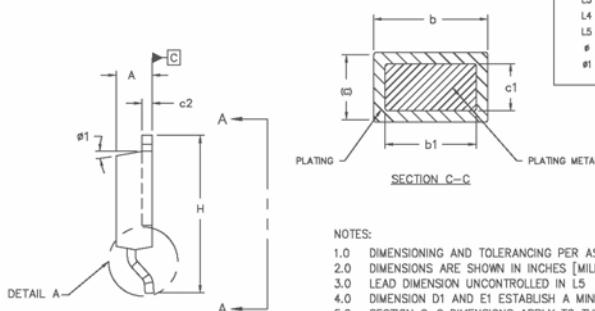
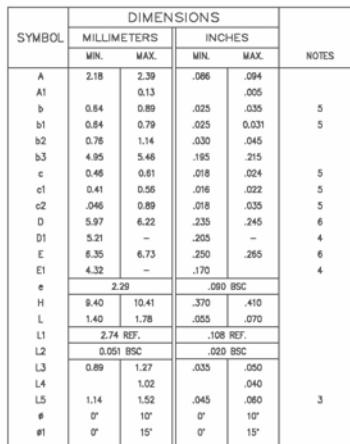
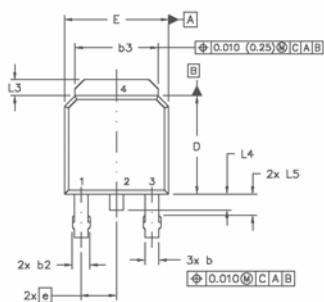
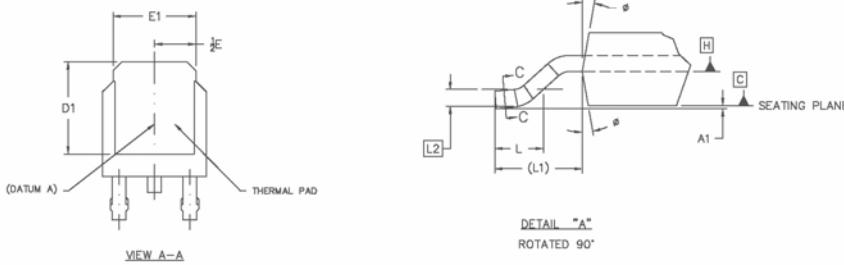
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
5. CONTROLLING DIMENSION: INCH.
6. LEADS & DRAIN CONTACT ARE PLATED : 100% Sn

**Tape & Reel - D<sup>2</sup>Pak (SMD220)**



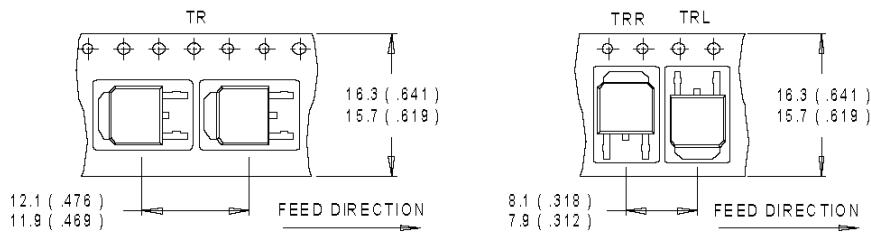
## Case Outline – D-Pak - Automotive Q100 PbF MSL1 qualified



#### NOTES-

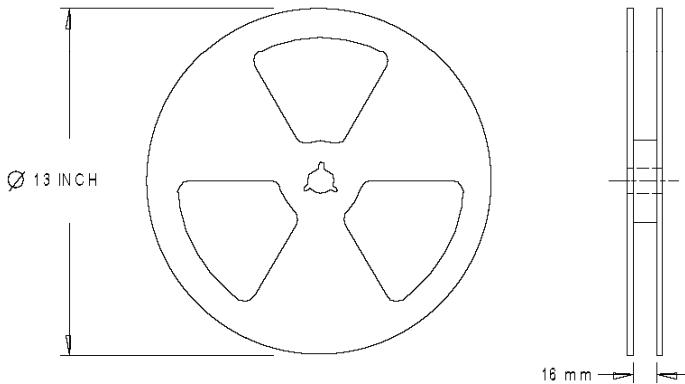
- 1.0 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
- 2.0 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- 3.0 LEAD DIMENSION UNCONTROLLED IN L5
- 4.0 DIMENSION D1 AND E1 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.
- 5.0 SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 [0.127] AND .010 [0.2540] FROM THE LEAD TIP.
- 6.0 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" [0.127] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.
  
- 8.0 LEADS AND DRAIN ARE PLATED WITH 100% Sn

**Tape & Reel - D-Pak**



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS ( INCHES ).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.

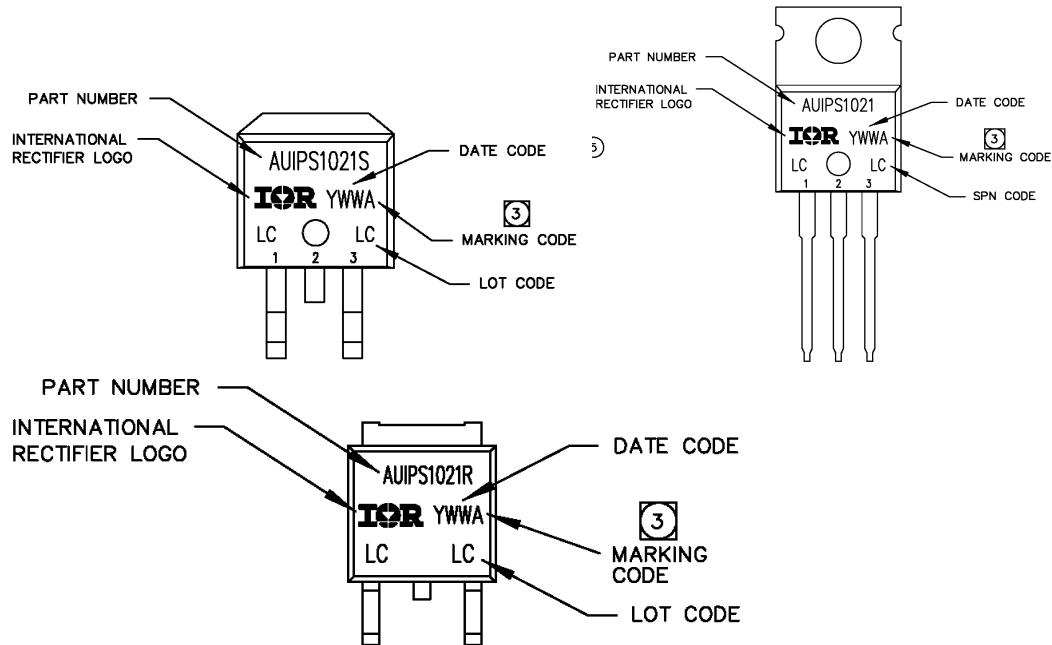


NOTES :

1. OUTLINE CONFORMS TO EIA-481.

Dimensions are shown in millimeters (inches)

## Part Marking Information



## Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIPS1021	TO220 – 5Leads	Tube	50	AUIPS1021
		Tube	50	AUIPS1021S
	D2-Pak-5-Leads	Tape and reel left	800	AUIPS1021STRL
		Tape and reel right	800	AUIPS1021STRR
	D-Pak-5-Lead	Tube	75	AUIPS1021R
		Tape and reel	2000	AUIPS1021RTR
		Tape and reel left	3000	AUIPS1021RTRL
		Tape and reel right	3000	AUIPS1021RTRR

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## Revision History

<b>Revision</b>	<b>Date</b>	<b>Notes/Changes</b>
D	November, 24 <sup>th</sup> , 2010	AU release
D1	December, 7th	Remove ESD section page 3
D2	December, 9 <sup>th</sup> 2010	Update qual page 2
E	February, 8th 2011	Update Vclamp page 1
F	February, 28th 2011	Update Max rating