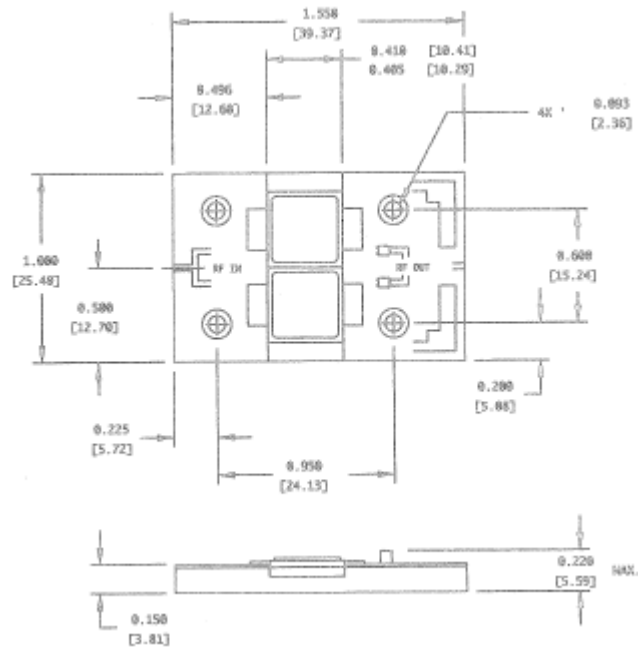


Rev. V4

- NPN Silicon Power Transistors
- Input and Output Matched to 50W
- Duroid Circuit Board
- Easily Combined For High Power Transmitters
- Plated Copper Flange



Absolute Maximum Ratings at 25°C¹

Parameter	Absolute Maximum
Supply Voltage	40V
Input Power	35W
Output Power	200W
Thermal Resistance	0.4° C/W
Power Dissipation	380W
Operating Case Temp.	-30 to +100°C
Storage Temperature	-40 to +125°C

Electrical Characteristics at °25 C

Parameter	Symbol	Test Conditions	Units	Min.	Max.
Output Power	P _{OUT}	V _{CC} =36, P _{IN} =28 W, F=2.70, 2.90, 3.10 GHz	W	140	-
Power Gain	G _P	V _{CC} =36, P _{OUT} =140 W, F=2.70, 2.90, 3.10 GHz	dB	7	-
Collector Efficiency	η _C	V _{CC} =36, P _{OUT} =140 W, F=2.70, 2.90, 3.10 GHz	%	35	-
Input VSWR	VSWR	V _{CC} =36, P _{OUT} =140 W, F=2.70, 2.90, 3.10 GHz	-	-	2:1
Load VSWR Tolerance	VSWR-T	V _{CC} =36, P _{OUT} =140 W, F=2.70, 2.90, 3.10 GHz	-	-	2:1
Load VSWR for Stability	VSWR-S	V _{CC} =36, P _{OUT} =140 W, F=2.70, 2.90, 3.10 GHz	-	-	1.5:1
Pulse Droop	D _P	V _{CC} =36, P _{OUT} =140 W, F=2.70, 2.90, 3.10 GHz	dB	-	1

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