

Product Summary

| V _{BR} (Min) | I _{PP} (Max) | C _T (Typ) |
|-----------------------|-----------------------|----------------------|
| 6.5V | 90A | 800pF |

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

Features

- Provides ESD Protection per IEC 61000-4-2 Standard: Air $\pm 30\text{kV}$, Contact $\pm 30\text{kV}$
- One Channels of ESD Protection
- Low Channel Input Capacitance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: U-DFN1610-2 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 **e4**
- Weight: 0.003 grams (Approximate)



Device Schematic

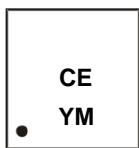
Ordering Information (Note 4)

| Product | Compliance | Marking | Reel Size (inches) | Tape Width (mm) | Quantity per Reel |
|----------------|------------|---------|--------------------|-----------------|--------------------|
| D6V3H1U2LP16-7 | Standard | CE | 7 | 8 | 10,000/Tape & Reel |

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



CE = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: D = 2016)
 M = Month (ex: 9 = September)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | Conditions |
|------------------------------------|--------------------|----------|------|-----------------------------|
| Peak Pulse Current | I_{PP} | 90 | A | 8/20 μs (Note 7) |
| ESD Protection – Contact Discharge | $V_{ESD_CONTACT}$ | ± 30 | kV | Standard IEC 61000-4-2 |
| ESD Protection – Air Discharge | V_{ESD_AIR} | ± 30 | kV | Standard IEC 61000-4-2 |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|-----------------|-------------|------|
| Power Dissipation (Note 5) | P_D | 500 | mW |
| Thermal Resistance, Junction to Ambient, $T_A = +25^\circ\text{C}$ | $R_{\theta JA}$ | 250 | °C/W |
| Operating and Storage Temperature Range | T_J, T_{STG} | -55 to +150 | °C |

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Conditions |
|--|-----------|-----|------|------|------|---|
| Reverse Standoff Voltage | V_{RWM} | — | — | 6.3 | V | — |
| Channel Leakage Current (Note 6) | I_R | — | — | 500 | nA | $V_R = 6.3\text{V}$ |
| Reverse Breakdown Voltage | V_{BR} | 6.5 | — | 9 | V | $I_R = 1\text{mA}$ |
| Clamping Voltage, Positive Transients (Note 7) | V_C | — | — | 8.7 | V | $I_{PP} = 10\text{A}, t_p = 8/20\mu\text{s}$ |
| | | — | — | 9.5 | V | $I_{PP} = 50\text{A}, t_p = 8/20\mu\text{s}$ |
| | | — | — | 11.5 | V | $I_{PP} = 90\text{A}, t_p = 8/20\mu\text{s}$ |
| Channel Input Capacitance (Note 8) | C_T | — | 800 | — | pF | $V_R = 0\text{V}, f = 1\text{MHz}$, Any I/O to GND |
| Dynamic Resistance | R_{DYN} | — | 0.05 | — | Ω | TLP, 10A, $t_p = 100\text{ns}$ |

Notes:

5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
6. Short duration pulse test used to minimize self-heating effect.
7. Clamping voltage value is based on an 8x20 μs peak pulse current (I_{PP}) waveform.
8. Measured from any I/O to GND.

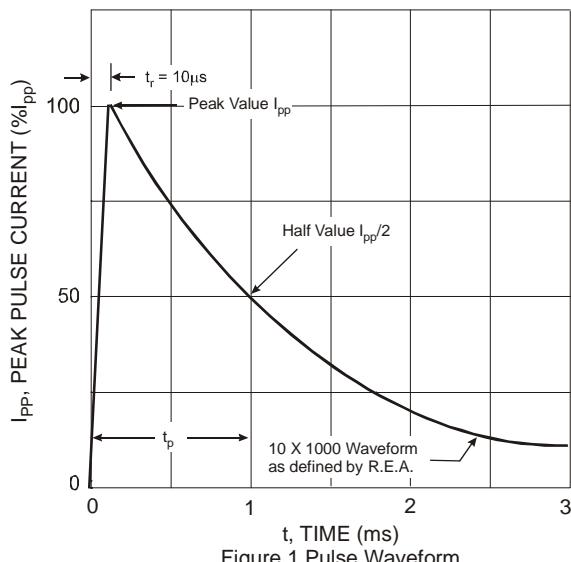


Figure 1 Pulse Waveform

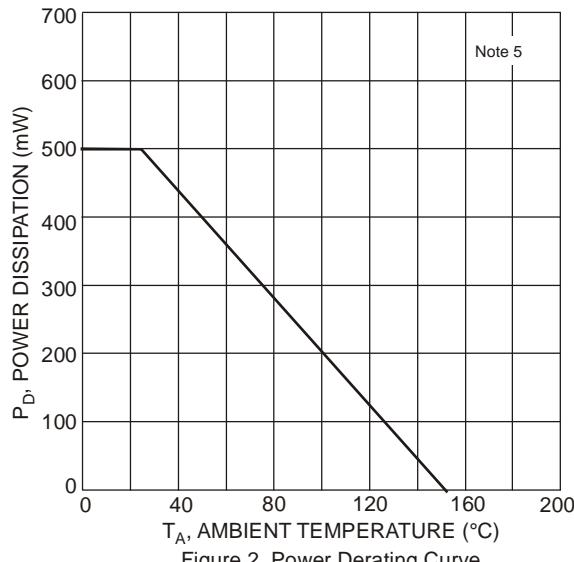


Figure 2 Power Derating Curve

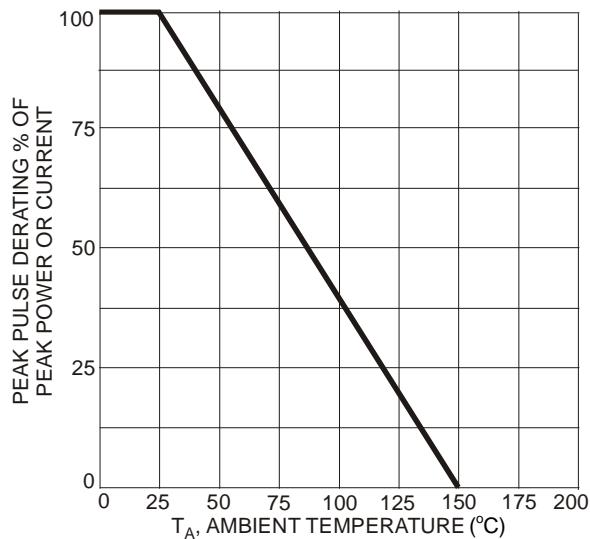


Figure 3 Power Dissipation vs. Ambient Temperature

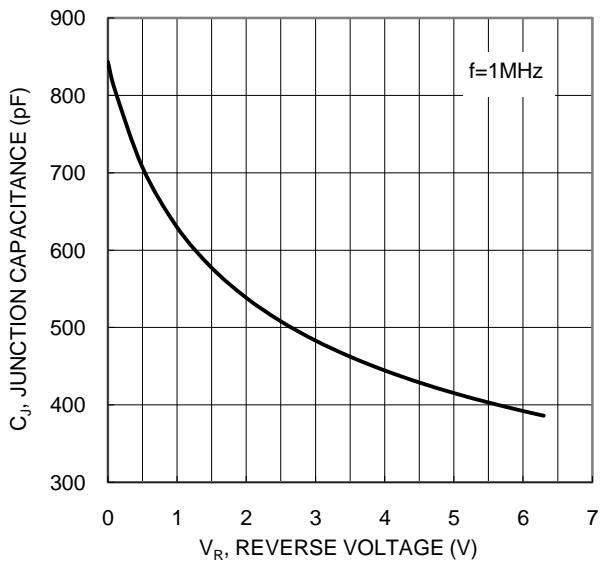
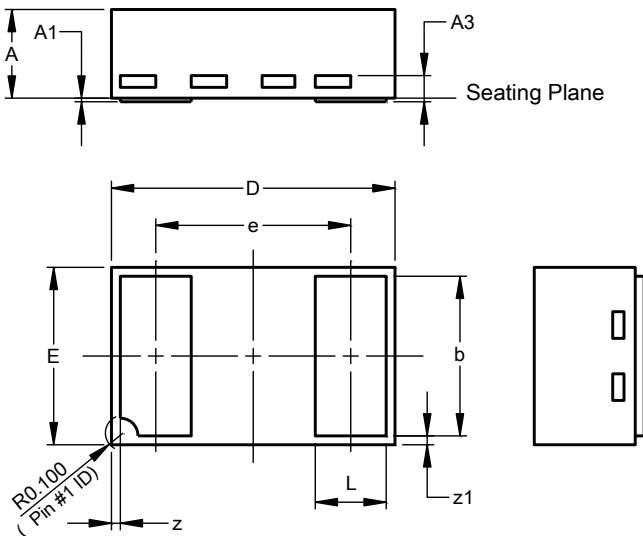


Figure 4 Typical Junction Capacitance

Package Outline Dimensions

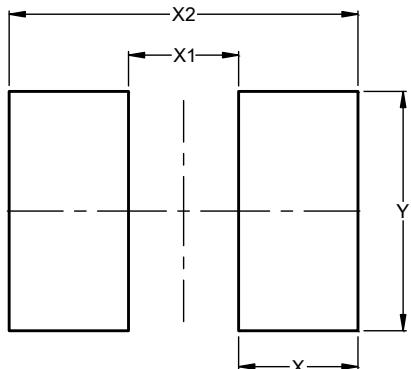
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



| U-DFN1610-2 (Type B) | | | |
|-------------------------|-------|------|-------|
| Dim | Min | Max | Typ |
| A | 0.45 | 0.55 | 0.50 |
| A1 | 0.00 | 0.05 | 0.015 |
| A3 | - | - | 0.127 |
| b | 0.85 | 0.95 | 0.90 |
| D | 1.55 | 1.65 | 1.60 |
| E | 0.95 | 1.05 | 1.00 |
| e | - | - | 1.10 |
| L | 0.35 | 0.45 | 0.40 |
| z | 0.050 | REF | |
| z1 | 0.050 | REF | |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| X | 0.650 |
| X1 | 0.600 |
| X2 | 1.900 |
| Y | 1.300 |

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