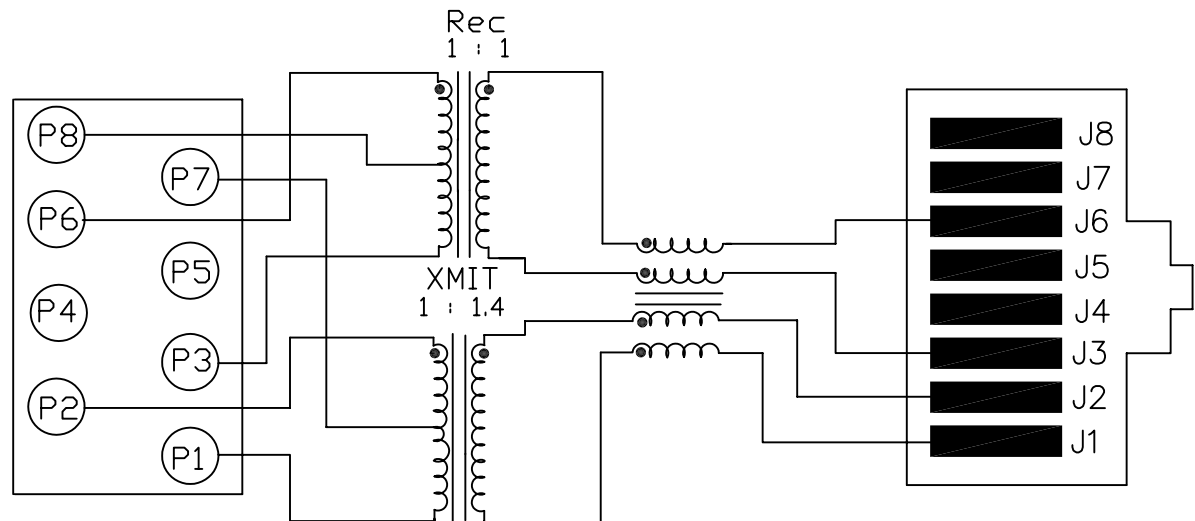


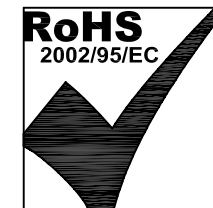


ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO	(P6-P8-P3) : (J6-J3)	: 1CT : 1 ± 3%
	(P2-P7-P1) : (J2-J1)	: 1CT : 1.4 ± 3%
2.0 INDUCTANCE	(P6-P3)=(J6-J3)	: 200uH MIN. @ 0.01V, 10KHz
	(P2-P1)	: 100uH MIN. @ 0.01V, 10KHz
3.0 LEAKAGE INDUCTANCE	P6-P3 (WITH J6 AND J3 SHORT)	: 0.3uH MAX. @ 1MHz
	P2-P1 (WITH J2 AND J1 SHORT)	: 0.3uH MAX. @ 1MHz
4.0 INTERWINDING CAPACITANCE	(P6,P3) TO (J6,J3)	: 20pf MAX @ 1MHz
	(P2,P1) TO (J2,J1)	: 20pf MAX. @ 1MHz
5.0 DC RESISTANCE	(J6-J3)=(J2-J1)	: 1.0 ohms Max.
	(P6-P8)=(P8-P3)	: 0.5 ohms Max.
	(P2-P7)=(P7-P1)	: 0.3 ohms Max.
6.0 DIELECTRIC WITHSTAND	(P6,P3) TO (J6,J3)	: 1500VAC for 60 seconds
	(P2,P1) TO (J2,J1)	: 1500VAC for 60 seconds

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTIONS ARE OMITTED.



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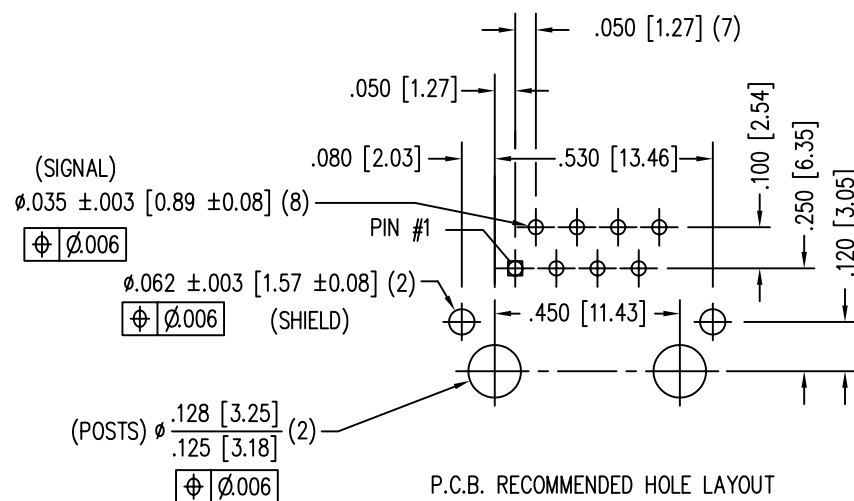
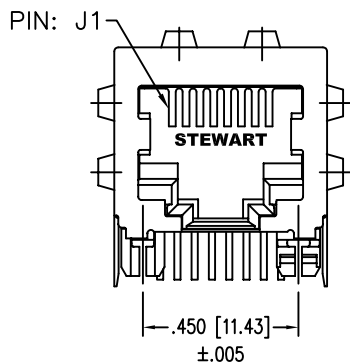
<http://www.stewartconnector.com>

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SHEET
1 OF 3

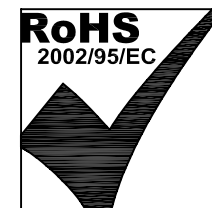
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SI-60162-F

REV.
09



P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.



NOTES:

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ± 0.005 [0.13]
5. WAVE SOLDER COMPATIBLE - PREHEAT 125°C/90SECS.

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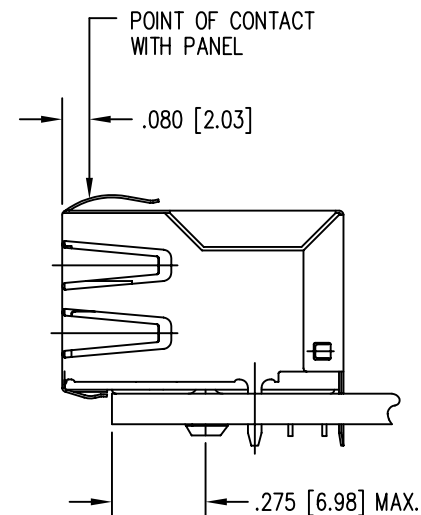
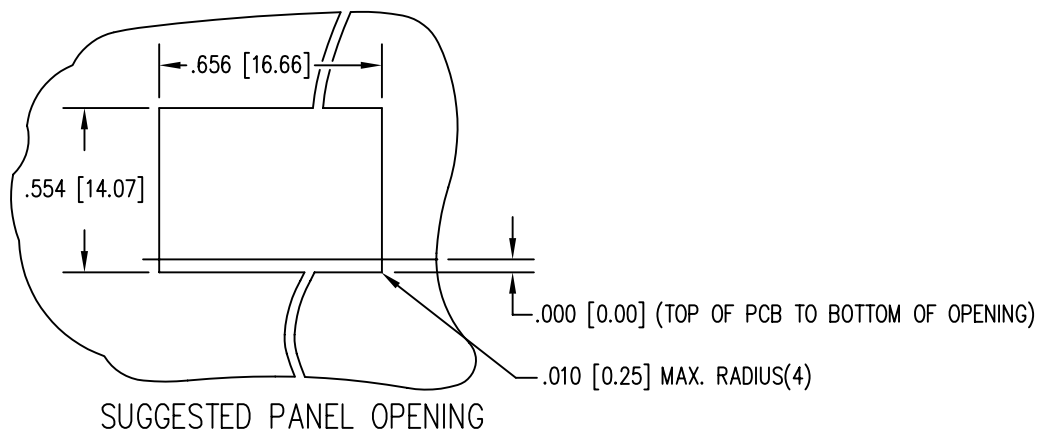
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SHEET
2 OF 3

DRAWING NO.	SI-60162-F
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REV.	12
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1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]

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SHEET
3 OF 3

DRAWING NO.
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REV.
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