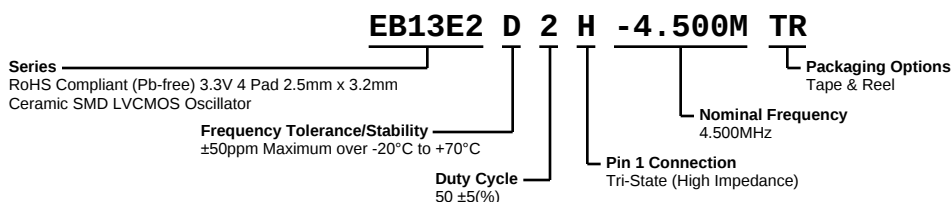


EB13E2D2H-4.500M TR



ECLIPTEK
CORPORATION



ELECTRICAL SPECIFICATIONS

Nominal Frequency	4.500MHz
Frequency Tolerance/Stability	±50ppm Maximum over -20°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging @25°C, Shock, and Vibration)
Supply Voltage	3.3Vdc ±5%
Input Current	3mA Maximum
Output Voltage Logic High (Voh)	90% of Vdd Minimum (IOH= -4mA)
Output Voltage Logic Low (Vol)	10% of Vdd Maximum (IOL= +4mA)
Rise/Fall Time	5nSec Maximum (Measured at 20% to 80% of waveform)
Duty Cycle	50 ±5(%) (Measured at 50% of waveform)
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Pin 1 Connection	Tri-State (High Impedance)
Tri-State Input Voltage (Vih and Vil)	80% of Vdd Minimum or No Connect to Enable Output, 20% of Vdd Maximum to Disable Output (High Impedance)
Standby Current	10µA Maximum
RMS Phase Jitter	1pSec Maximum (Fj = 12kHz to 20MHz)
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Flammability	UL94-V0
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A

Technical drawing of a square electronic component showing three views: top, side, and bottom views.

Top View Dimensions:

- Overall width: 2.50 ± 0.15
- Overall height: 3.20 ± 0.15
- Internal marking area width: 0.90 ± 0.20
- Internal marking area height: 1.20 ± 0.10
- Marking text: MARKING ORIENTATION
- Marking feature: A small black dot.

Side View Dimensions:

- Overall thickness: 0.75 ± 0.10

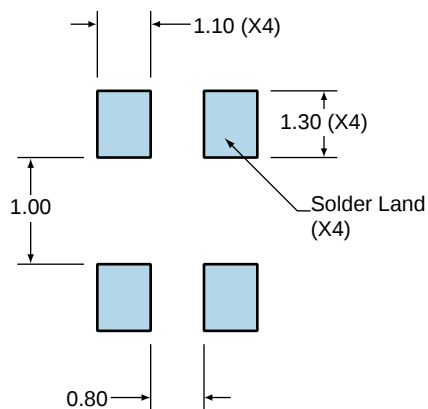
Bottom View Dimensions:

- Overall width: 2.50 ± 0.15
- Overall height: 3.20 ± 0.15
- Internal marking area width: 0.90 ± 0.20
- Internal marking area height: 1.20 ± 0.10
- Marking text: MARKING ORIENTATION
- Marking feature: A small black dot.
- Marking area corners: Labeled 1, 2, 3, and 4.

PIN	CONNECTION
1	Tri-State
2	Case/Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	E4.50 <i>E=Ecliptek Designator</i>
2	XXYZZ <i>XX=Ecliptek Manufacturing Code</i> <i>Y=Last Digit of the Year</i> <i>ZZ=Week of the Year</i>

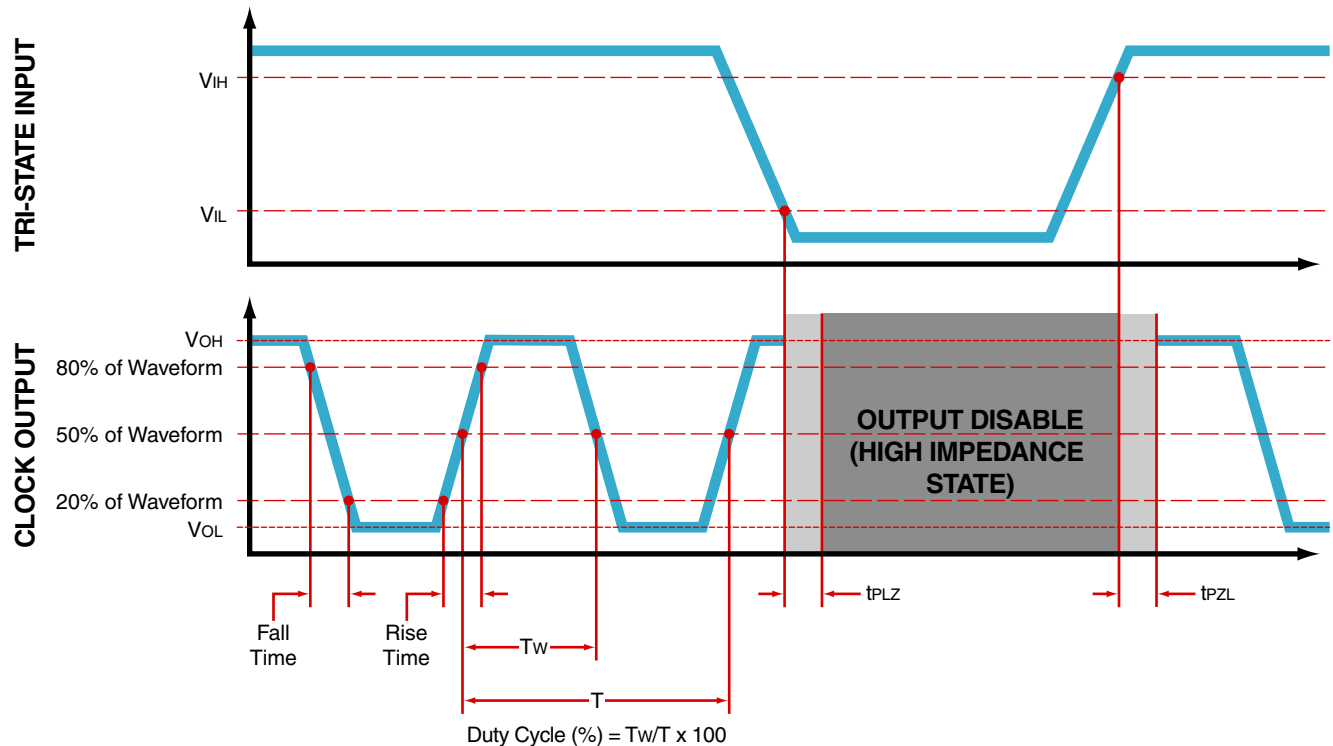
All Dimensions in Millimeters



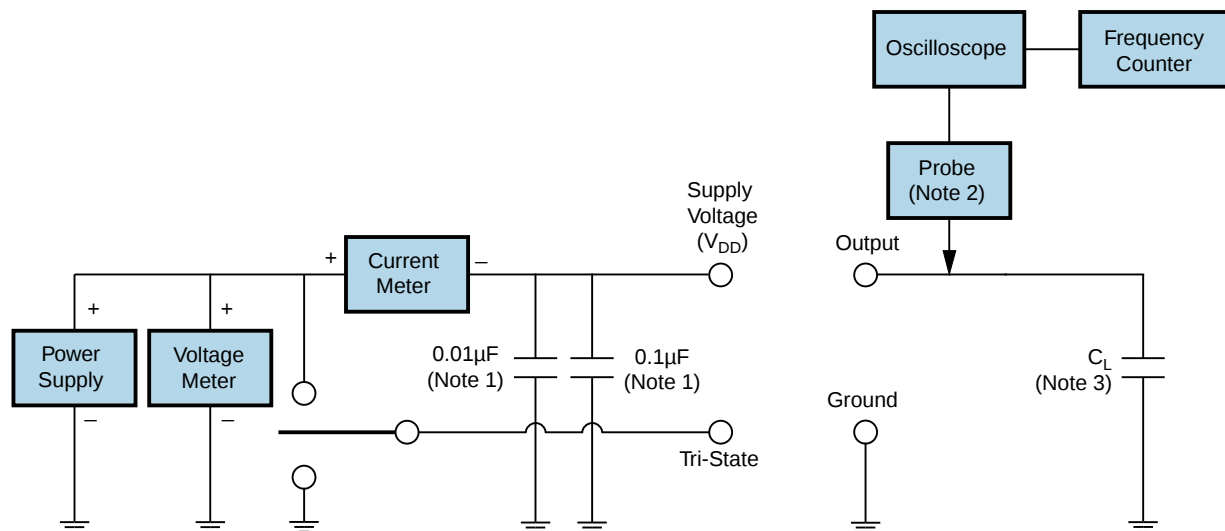
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OUTPUT WAVEFORM & TIMING DIAGRAM



Test Circuit for CMOS Output



Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and V_{DD} pin is required.

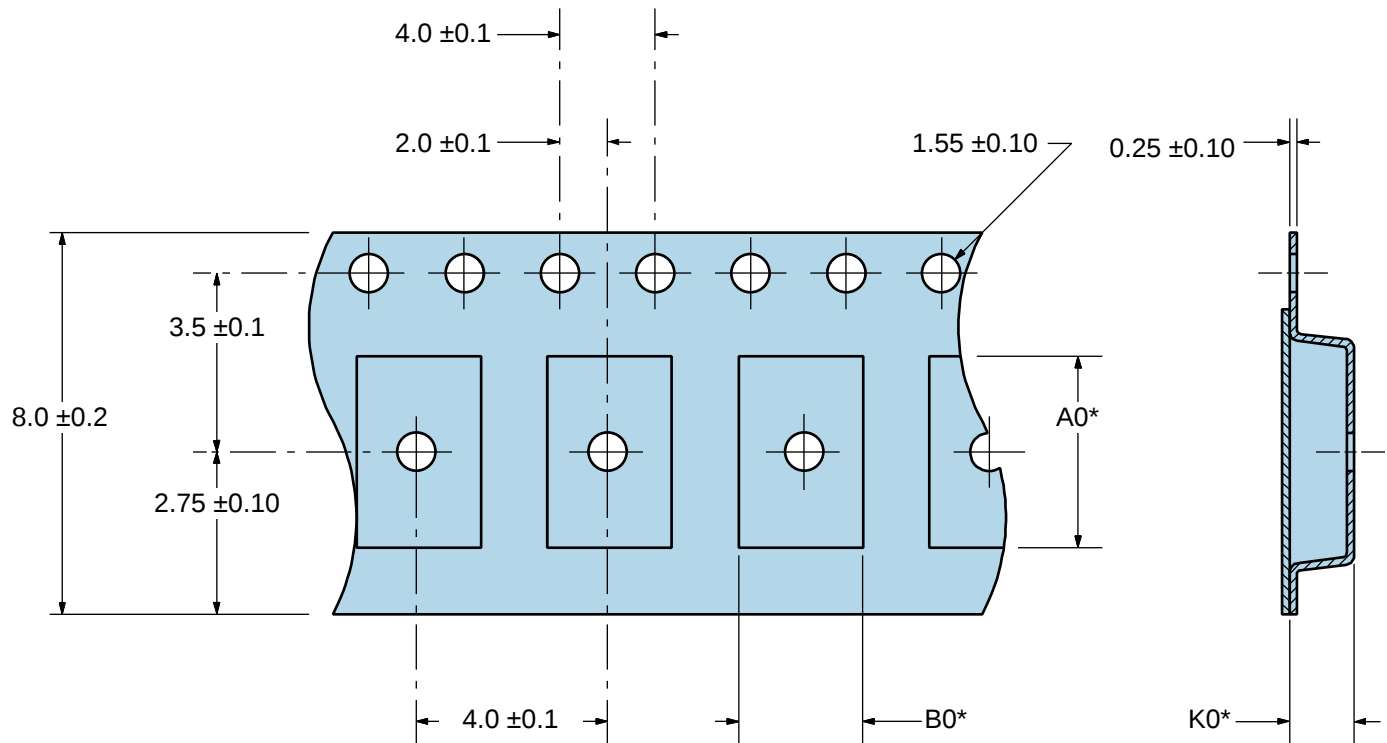
Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance.

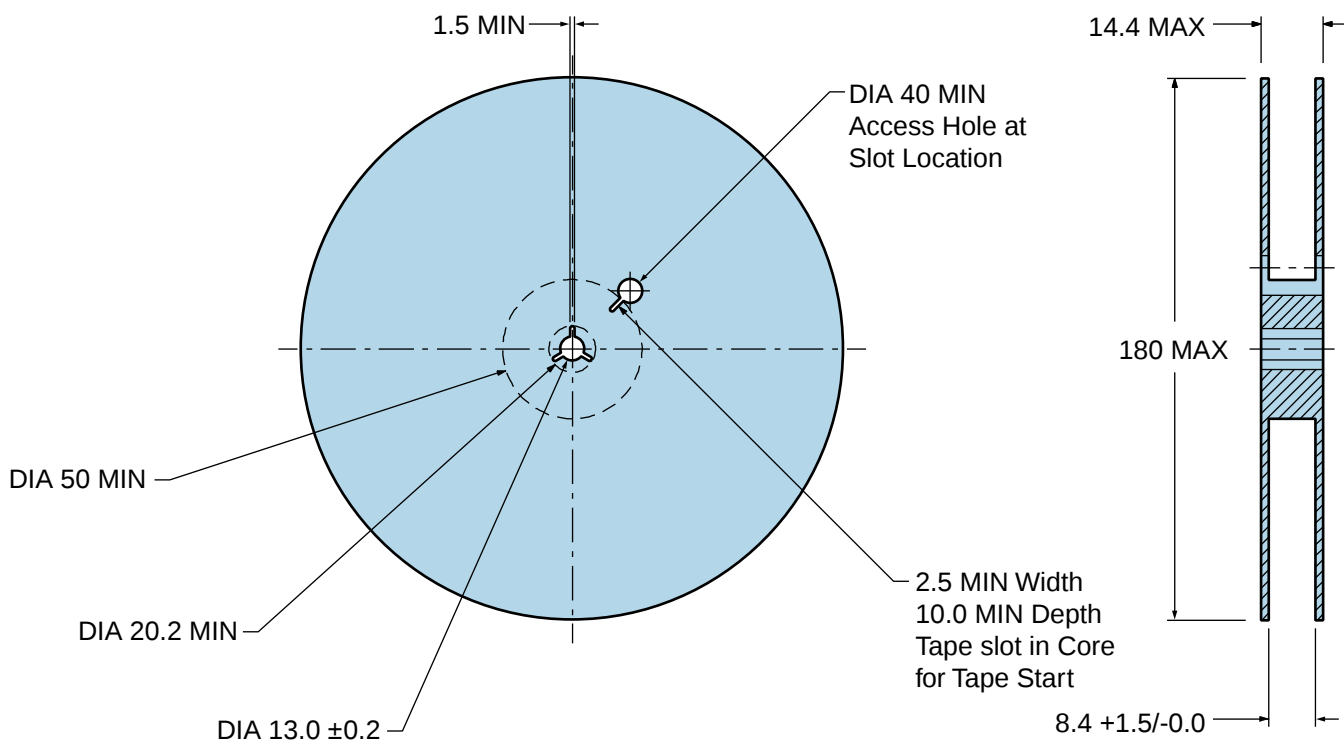
EB13E2D2H-4.500M TR

Tape & Reel Dimensions

Quantity Per Reel: 1,000 units



*Compliant to EIA 481A



Recommended Solder Reflow Methods



High Temperature Infrared/Convection

Ts MAX to TL (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (Ts MIN)	150°C
- Temperature Typical (Ts TYP)	175°C
- Temperature Maximum (Ts MAX)	200°C
- Time (ts MIN)	60 - 180 Seconds
Ramp-up Rate (TL to Tp)	3°C/second Maximum
Time Maintained Above:	
- Temperature (TL)	217°C
- Time (tL)	60 - 150 Seconds
Peak Temperature (Tp)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (Tp Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1

Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

T_s MAX to T_L (Ramp-up Rate) 5°C/second Maximum

Preheat

- Temperature Minimum (T_s MIN) N/A
- Temperature Typical (T_s TYP) 150°C
- Temperature Maximum (T_s MAX) N/A
- Time (t_s MIN) 60 - 120 Seconds

Ramp-up Rate (T_L to T_p) 5°C/second Maximum

Time Maintained Above:

- Temperature (T_L) 150°C
- Time (t_L) 200 Seconds Maximum

Peak Temperature (T_p) 240°C Maximum

Target Peak Temperature (T_p Target) 240°C Maximum 1 Time / 230°C Maximum 2 Times

Time within 5°C of actual peak (t_p) 10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time

Ramp-down Rate 5°C/second Maximum

Time 25°C to Peak Temperature (t) N/A

Moisture Sensitivity Level Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.