

## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$ $T_C = +25^\circ C$
-20V	2.2m $\Omega$ @ $V_{GS} = -10V$	-150A
	2.55m $\Omega$ @ $V_{GS} = -4.5V$	-120A
	4.0m $\Omega$ @ $V_{GS} = -2.5V$	-90A

## Description

This new generation MOSFET is designed to minimize  $R_{DS(ON)}$  and yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

## Applications

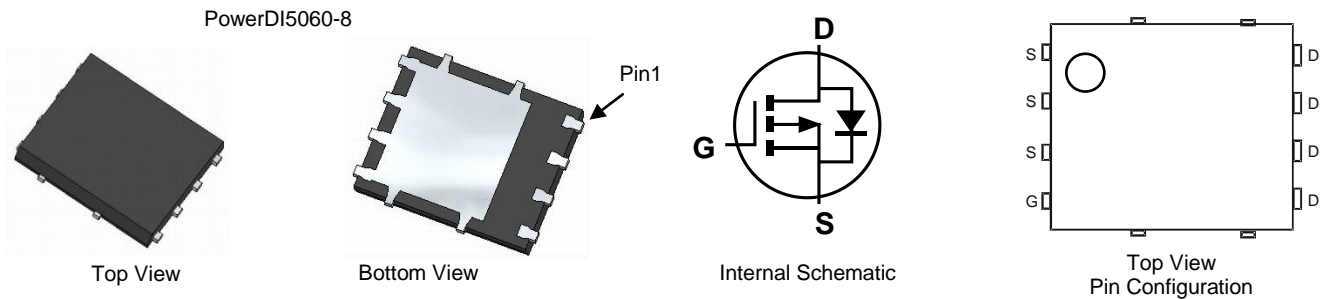
- Switch

## Features

- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low  $R_{DS(ON)}$  – Minimizes On State Losses
- <1.1mm Package Profile – Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: PowerDI<sup>®</sup> 5060-8
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208@3
- Weight: 0.097 grams (Approximate)

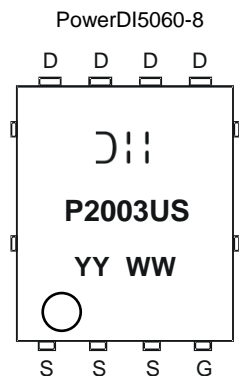


## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2003UPS-13	PowerDI5060-8	2,500 / Tape & Reel

- Notes:
- EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
  - See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



DII = Manufacturer's Marking  
P2003US = Product Type Marking Code  
YYWW = Date Code Marking  
YY = Last Two Digits of Year (ex: 17 = 2017)  
WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-Source Voltage	V <sub>GSS</sub>	±12	V
Continuous Drain Current, V <sub>GS</sub> = -10V (Note 7)	I <sub>D</sub>	-150 -120	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-350	A
Maximum Continuous Body Diode Forward Current (Note 7)	I <sub>S</sub>	-120	A
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)	I <sub>SM</sub>	-350	A
Avalanche Current, L = 0.1mH (Note 8)	I <sub>AS</sub>	-32	A
Avalanche Energy, L = 0.1mH (Note 8)	E <sub>AS</sub>	67	mJ

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	90	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	2.7	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	46	°C/W
Total Power Dissipation (Note 7)	P <sub>D</sub>	80	W
Thermal Resistance, Junction to Case (Note 7)	R <sub>θJC</sub>	1.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1	μA	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±12V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.5	—	-1.4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	1.7	2.2	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -25A
		—	1.9	2.55		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -20A
		—	2.5	4.0		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -15A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.6	-1.1	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -5A
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	C <sub>ISS</sub>	—	8352	—	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1MHz
Output Capacitance	C <sub>OSS</sub>	—	1406	—	pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	599	—	pF	
Gate Resistance	R <sub>g</sub>	—	13.2	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	79	—	nC	V <sub>DS</sub> = -10V, I <sub>D</sub> = -20A
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	177	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	14.3	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	19.8	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	7.8	—	ns	V <sub>DD</sub> = -10V, V <sub>GEN</sub> = -4.5V, R <sub>GEN</sub> = 1Ω, I <sub>D</sub> = -10A
Turn-On Rise Time	t <sub>r</sub>	—	4.9	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	377	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	189	—	ns	I <sub>F</sub> = -10A, di/dt = 100A/μs
Reverse Recovery Time	t <sub>RR</sub>	—	49	—	ns	
Reverse Recovery Charge	Q <sub>RR</sub>	—	39	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

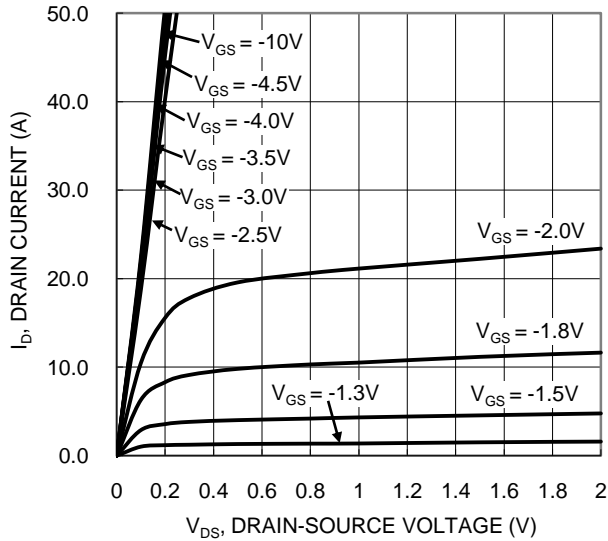


Figure 1. Typical Output Characteristic

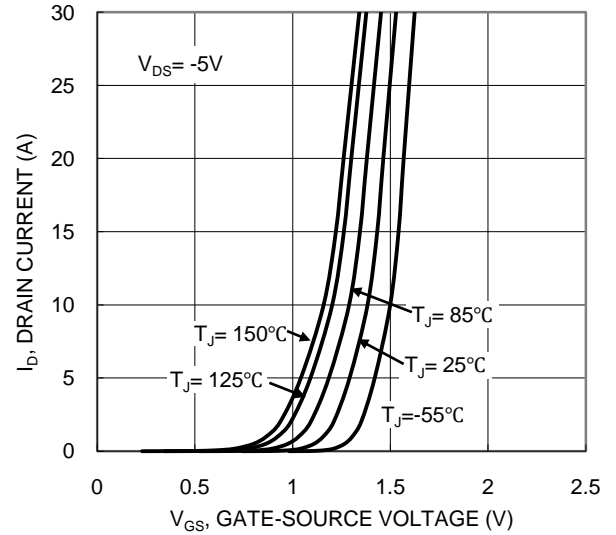


Figure 2. Typical Transfer Characteristic

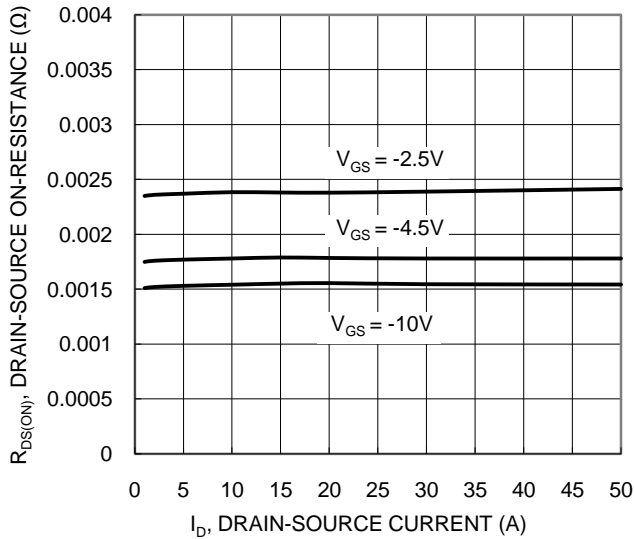


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

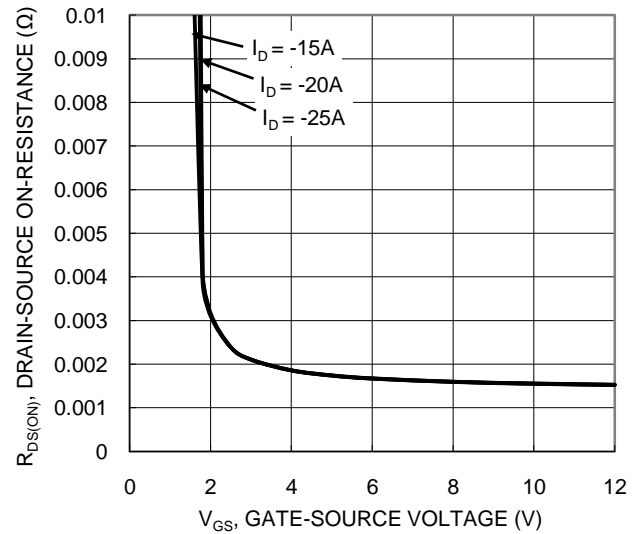


Figure 4. Typical Transfer Characteristic

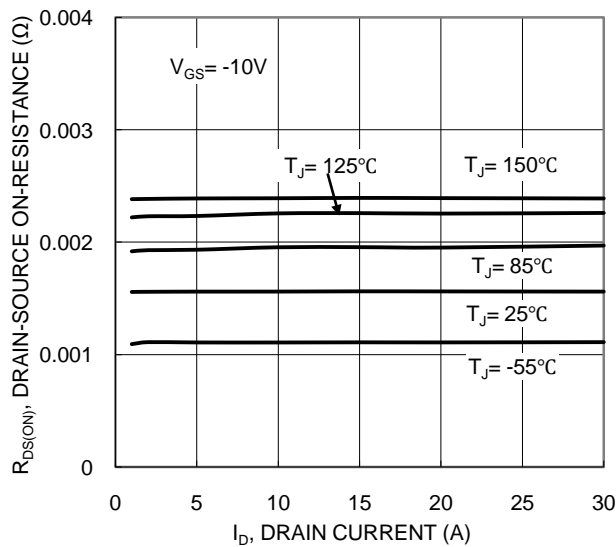


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

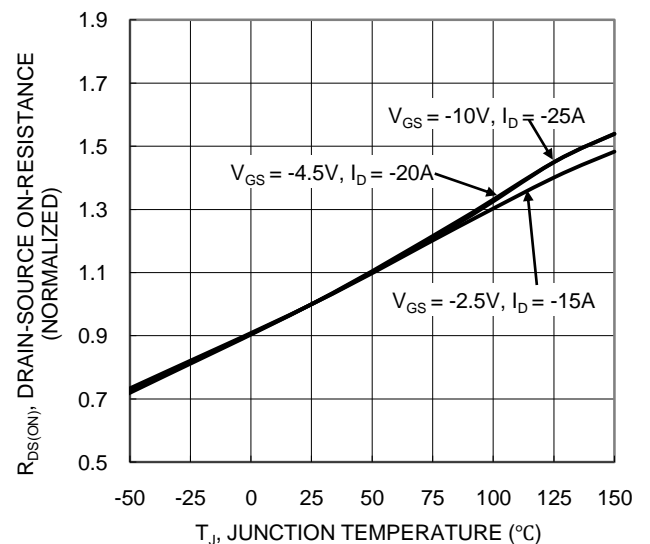
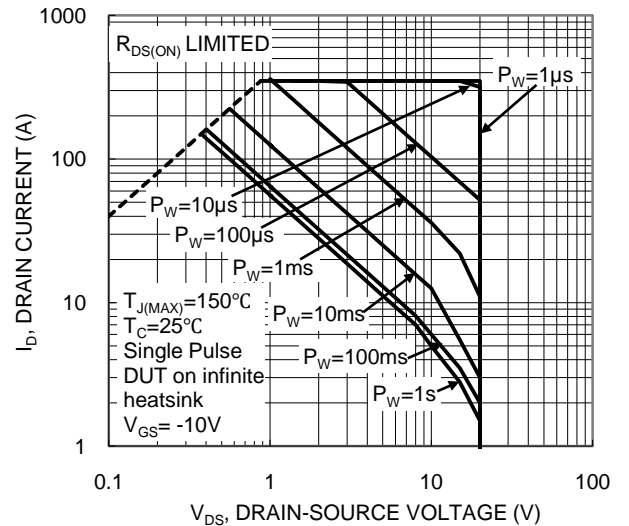
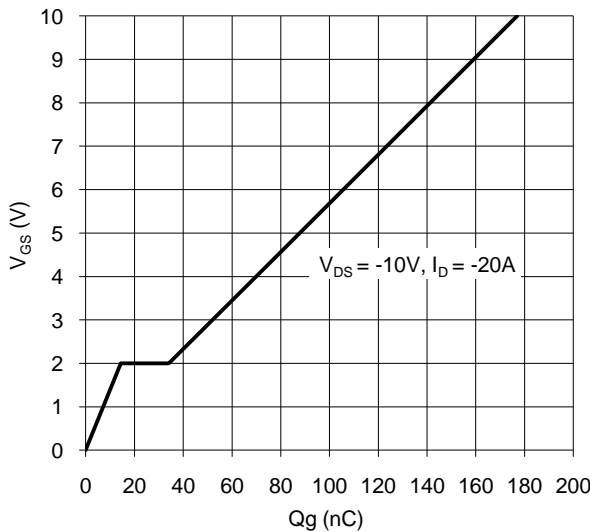
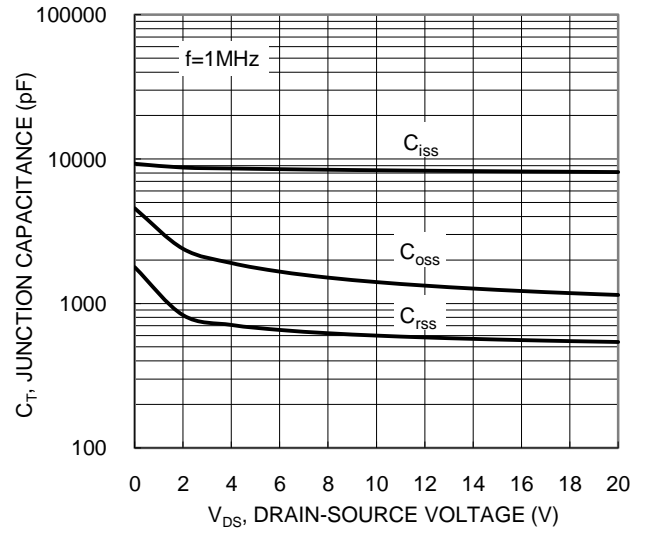
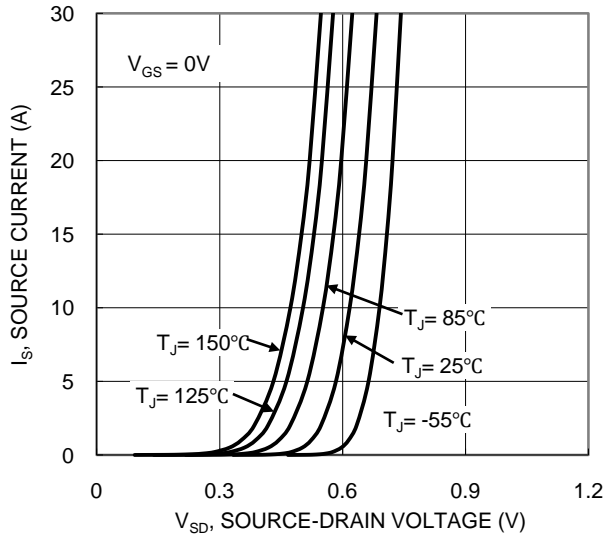
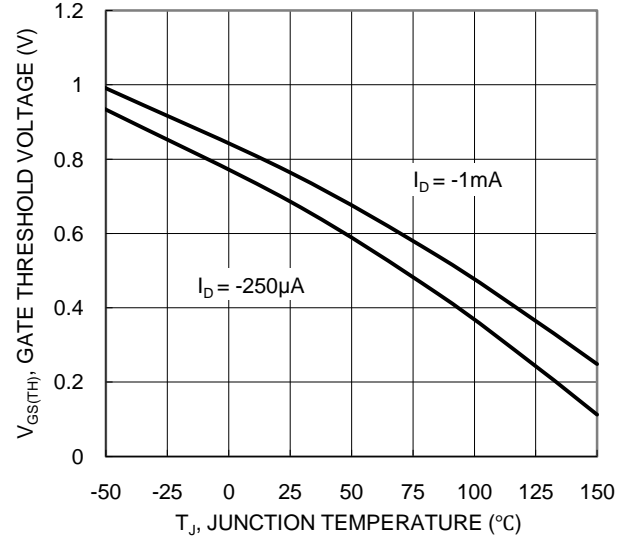
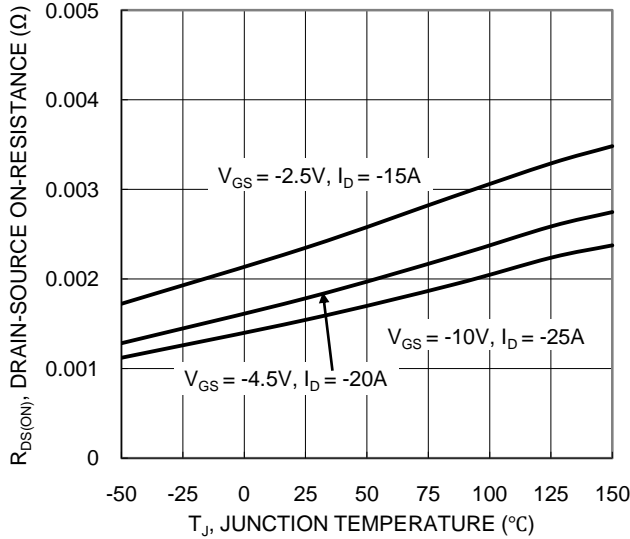


Figure 6. On-Resistance Variation with Junction Temperature



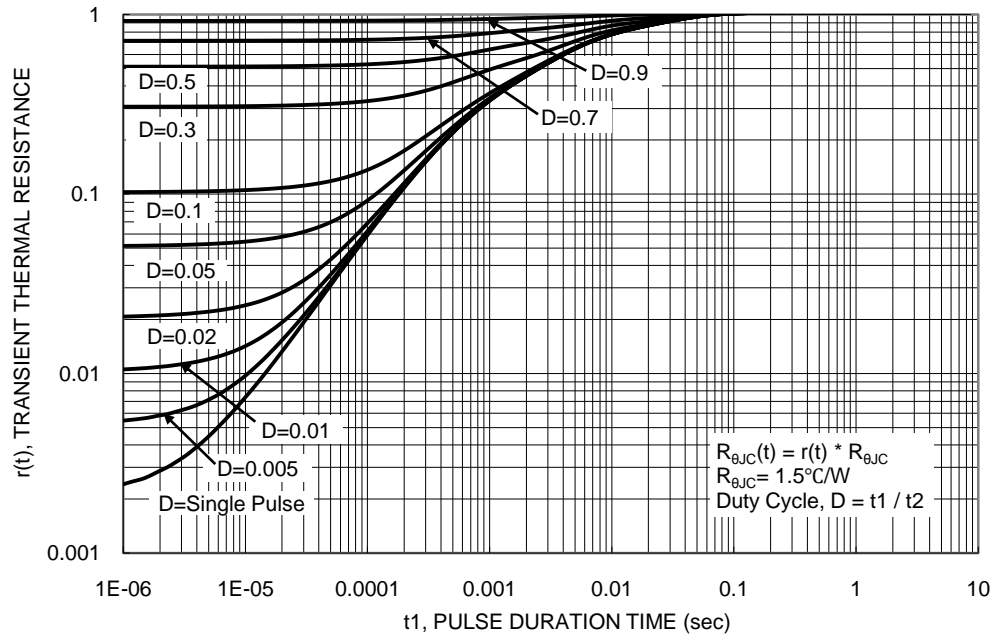
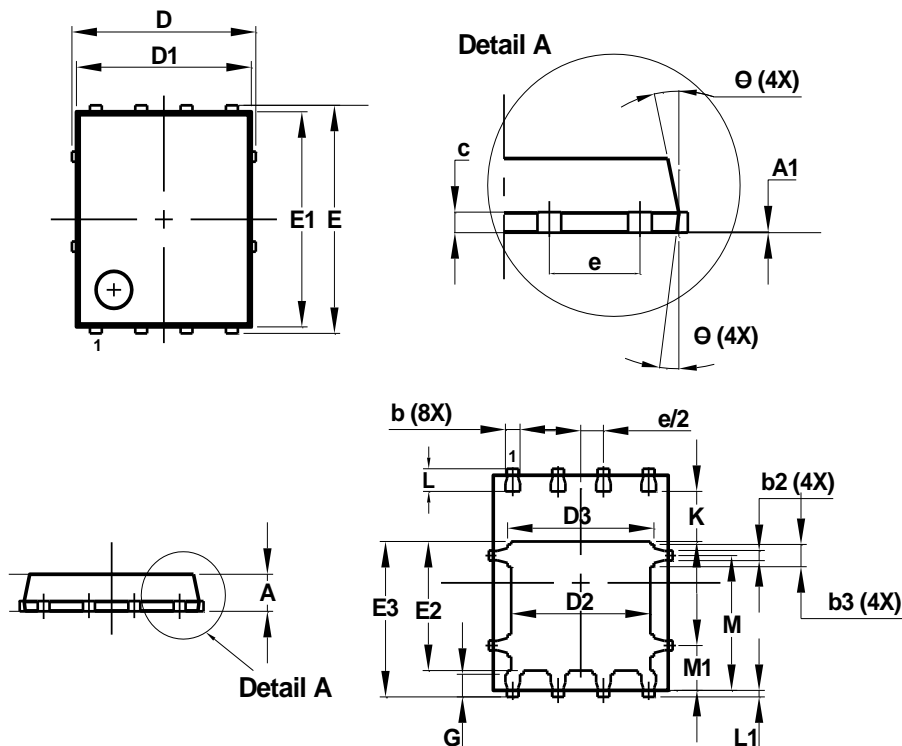


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8

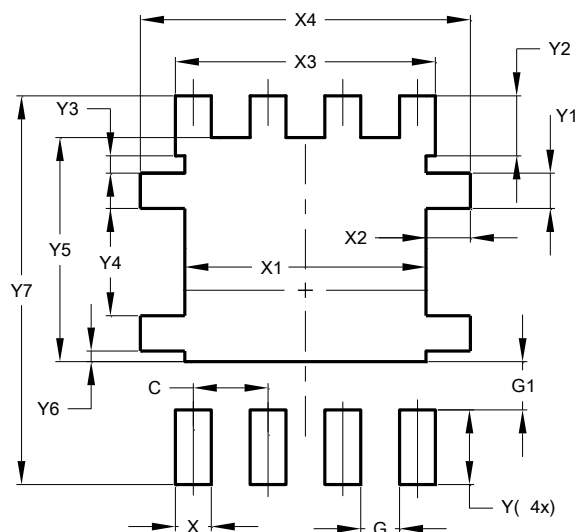


PowerDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	—
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	—	—
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
$\Theta$	10°	12°	11°
$\Theta$ 1	6°	8°	7°
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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