

H-Bridge Motor Driver/Amplifiers

FEATURES

- ◆ LOW COST COMPLETE H-BRIDGE
- ◆ SELF-CONTAINED SMART LOWSIDE/
HIGHSIDE DRIVE CIRCUITRY
- ◆ WIDE SUPPLY RANGE: UP TO 80V
- ◆ 10A CONTINUOUS OUTPUT
14A CONTINUOUS OUTPUT FOR A-GRADE
- ◆ ISOLATED CASE ALLOWS DIRECT
HEATSINKING
- ◆ FOUR QUADRANT OPERATION, TORQUE
CONTROL CAPABILITY
- ◆ INTERNAL/PROGRAMMABLE PWM
FREQUENCY GENERATION

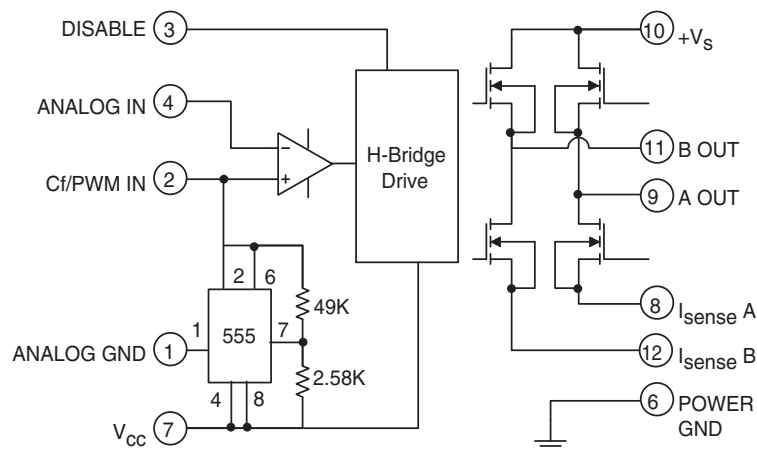
APPLICATIONS

- ◆ BRUSH TYPE MOTOR CONTROL
- ◆ CLASS D SWITCHMODE AMPLIFIER
- ◆ REACTIVE LOADS
- ◆ MAGNETIC COILS (MRI)
- ◆ ACTIVE MAGNETIC BEARING
- ◆ VIBRATION CANCELLING

DESCRIPTION

The SA160 is a pulse width modulation amplifier that can supply 10A continuous current to the load. The full bridge amplifier can be operated over a wide range of supply voltages. All of the drive/control circuitry for the lowside and highside switches are internal to the hybrid. The PWM circuitry is internal as well, leaving the user to only provide an analog signal for the motor speed/direction, or audio signal for switchmode audio amplification. The internal PWM frequency can be programmed by an external integrator capacitor. Alternatively, the user may provide an external TTL-compatible PWM signal for simultaneous amplitude and direction control for four quadrant mode.

BLOCK DIAGRAM



1. CHARACTERISTICS AND SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Min | Max | Units |
|---|----------|-----|-----|--------------------|
| SUPPLY VOLTAGE (Note 4) | V_s | | 80 | V |
| OUTPUT CURRENT, continuous | | | 15 | A |
| OUTPUT CURRENT, peak, $t = 100$ msec, $T_{cmax}=85^{\circ}\text{C}$ | | | 21 | A |
| LOGIC SUPPLY VOLTAGE | V_{CC} | | 16 | V |
| POWER DISSIPATION, internal (Note 3) | | | 156 | W |
| TEMPERATURE, pin solder, 10s max. | | | 260 | $^{\circ}\text{C}$ |
| TEMPERATURE, junction (Note 2) | | | 150 | $^{\circ}\text{C}$ |
| TEMPERATURE RANGE, storage | | -55 | 125 | $^{\circ}\text{C}$ |
| OPERATING TEMPERATURE RANGE, case | | -40 | 125 | $^{\circ}\text{C}$ |

CAUTION

The SA160 is constructed from MOSFET transistors. ESD handling procedures must be observed. The exposed substrate contains beryllia (BeO). Do not crush, machine, or subject to temperatures in excess of 850°C to avoid generating toxic fumes.

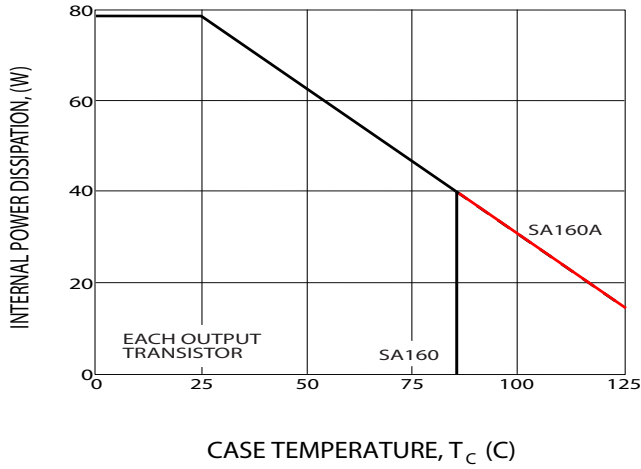
SPECIFICATIONS

| Parameter | Test Conditions ¹ | SA160 | | | SA160A | | | Units |
|---|--|-------|--------------|----------|--------|-----|-----|----------|
| | | Min | Typ | Max | Min | Typ | Max | |
| INPUT | | | | | | | | |
| ANALOG INPUT VOLTAGES | $V_{CC} = 12\text{V}$ | | | | | | | |
| A,B OUT = 50% Duty Cycle | | | $1/2 V_{CC}$ | | * | | | V |
| A OUT = 100% Duty Cycle High | | | $1/3 V_{CC}$ | | * | | | V |
| B OUT = 100% Duty Cycle High | | | $2/3 V_{CC}$ | | * | | | V |
| PWM INPUT | | | | | | | | |
| PWM PULSE LOW VOLTAGE | | 0 | | 0.8 | * | | * | V |
| PWM PULSE HIGH VOLTAGE | | 2.7 | | 5.0 | * | | * | V |
| PWM FREQUENCY | | | 45 | 250 | | * | * | kHz |
| DISABLE ON | | 2.7 | | V_{CC} | * | | * | V |
| DISABLE OFF | | 0 | | 0.8 | * | | * | V |
| OUTPUT | | | | | | | | |
| Total V_{DS} (ON) VOLTAGE, both MOSFETs | $I_{DS} = 10\text{A}$ $T_c = 85^{\circ}\text{C}$ | | 1.4 | 2.5 | | * | * | V |
| TOTAL R_{ON} , both MOSFETs | $I_{DS} = 10\text{A}$ $T_c = 85^{\circ}\text{C}$ | | .14 | 0.25 | | * | * | Ω |
| EFFICIENCY, 10A OUTPUT | $V_s = 80\text{V}$ | | 97 | | | * | | % |
| CURRENT, continuous | $T_{cmax}=85^{\circ}\text{C}$ for A-Grade | 10 | | | 14 | | | A |
| CURRENT, peak | $t = 100$ msec, $T_{cmax}=85^{\circ}\text{C}$ for A-Grade | 15 | | | 20 | | | A |
| SWITCHING FREQUENCY | $C_F = 270\text{pF}$ | | 45 | | | * | | kHz |
| DEAD TIME | | | 90 | | | * | | ns |

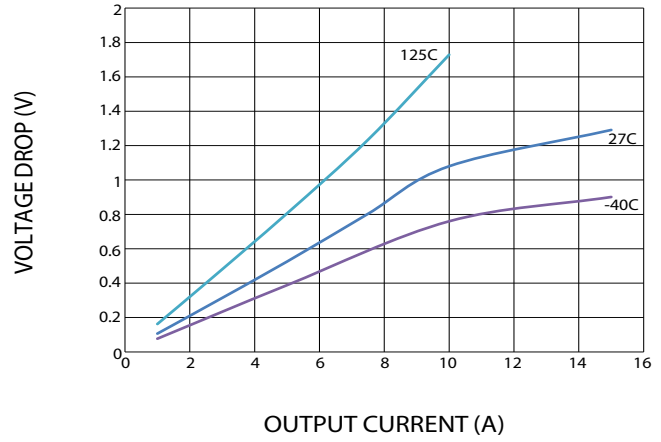
| Parameter | Test Conditions ¹ | SA160 | | | SA160A | | | Units |
|---------------------------------|---|-------|-----|------|--------|-----|------|-------|
| | | Min | Typ | Max | Min | Typ | Max | |
| POWER SUPPLY | | | | | | | | |
| V _S VOLTAGE (Note 4) | V _S Current = Load Current | | | 80 | | | * | V |
| V _{CC} VOLTAGE | | 9.5 | 12 | 15 | * | * | * | V |
| V _{CC} CURRENT | Fsw=50kHz | | 28 | 36 | | | | mA |
| V _S CURRENT | Fsw=50kHz, no load, V _S = 50V | | 6.5 | | | * | | mA |
| THERMAL (Note 3) | | | | | | | | |
| RESISTANCE, junction to case | Full temperature range, for each transistor | | 1.4 | 1.60 | | * | * | °C/W |
| RESISTANCE, junction to air | Full temperature range | | 30 | | | * | | °C/W |
| TEMPERATURE RANGE, case | | -40 | | +85 | * | | +125 | °C |

- NOTES: 1. (All Min/Max characteristics and specifications are guaranteed over the Specified Operating Conditions. Typical performance characteristics and specifications are derived from measurements taken at typical supply voltages and T_C = 25°C, V_{CC} = 12VDC).
2. Long term operation at the maximum junction temperature will result in reduced product life. Derate power dissipation to achieve high MTTF.
3. Each of the two active output transistors can dissipate 78W.
4. Derate to 70V below T_C = +25°C.

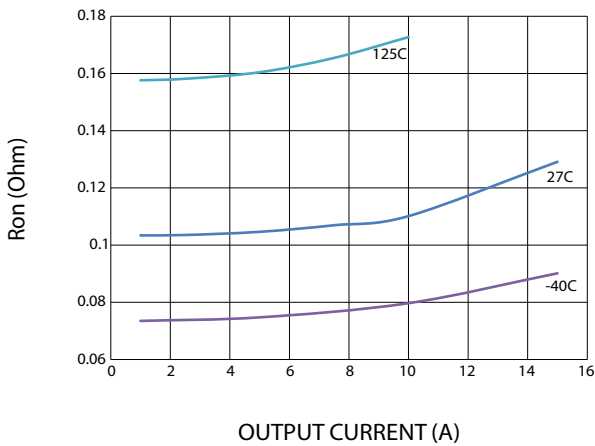
POWER DERATING



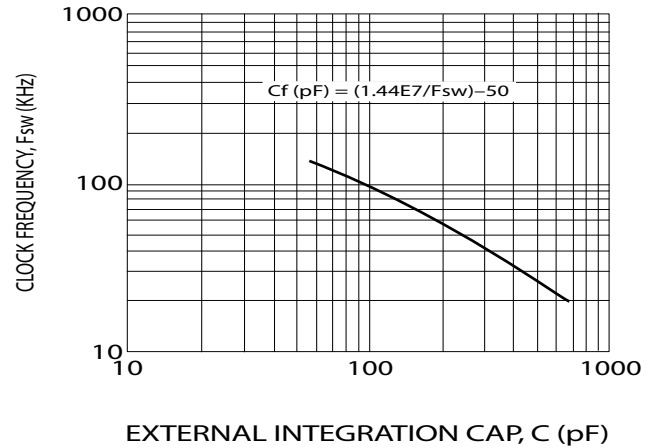
TOTAL VOLTAGE DROP



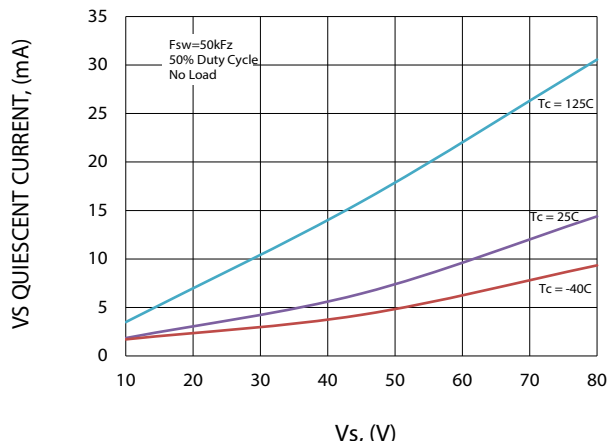
TOTAL R_{ON} both MOSFETs



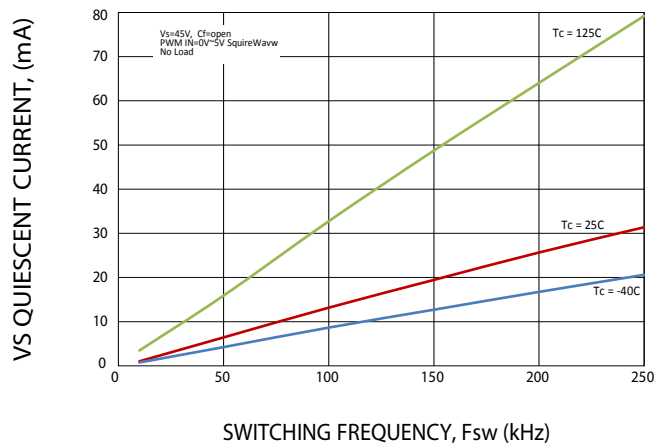
PWM FREQ VS EXT INT CAP



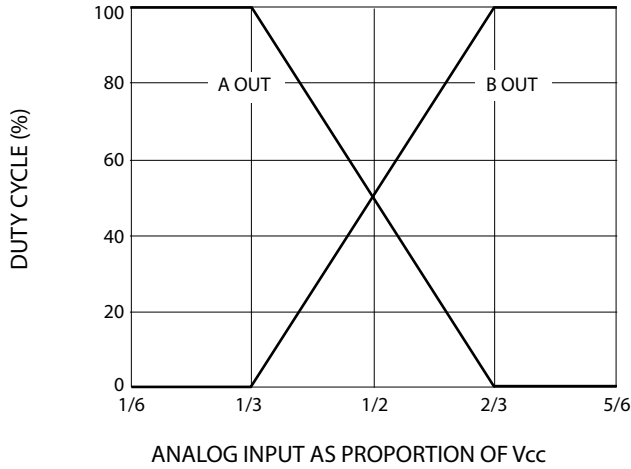
I_{QVs} vs. V_s VOLTAGE



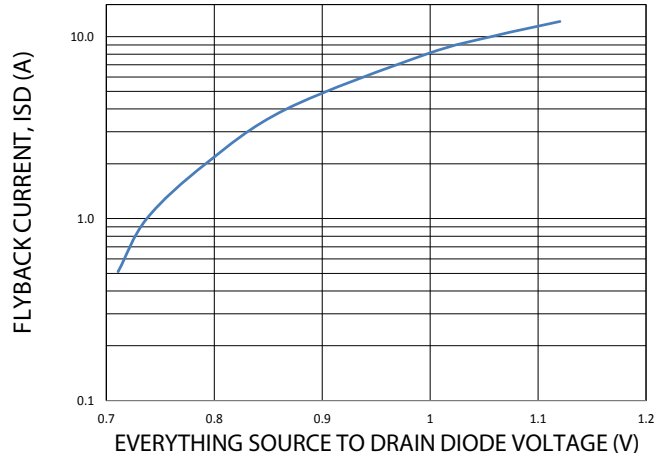
I_{QVs} vs. SWITCHING FREQ.



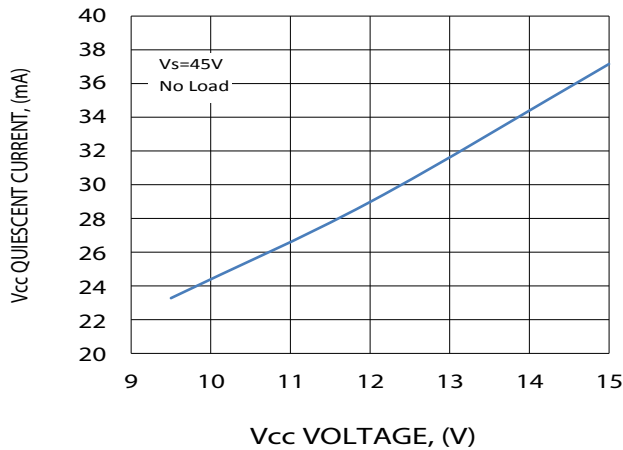
DUTY CYCLE VS ANALOG INPUT



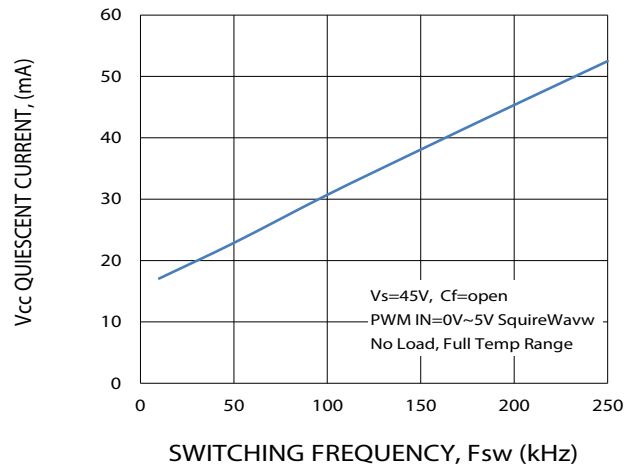
REVERSE DIODE



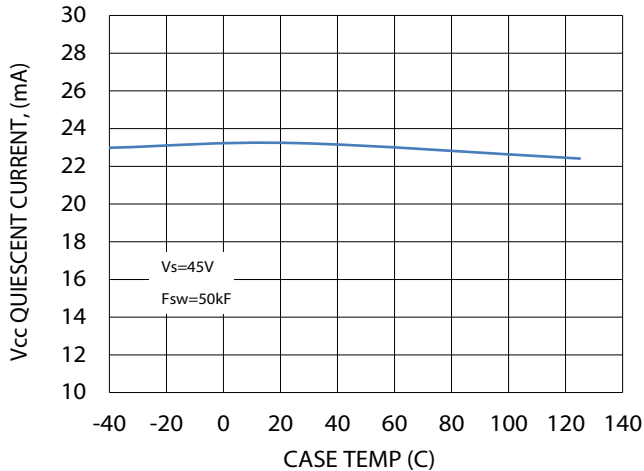
I_{QVcc} vs. Vcc VOLTAGE



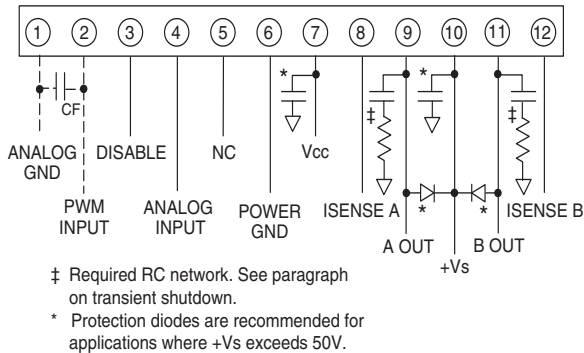
I_{QVcc} vs. SWITCHING FREQ.



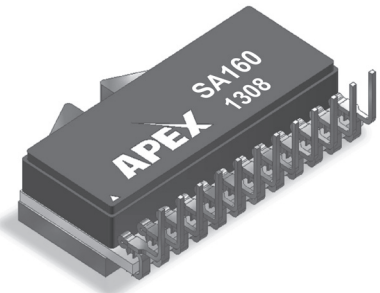
I_{QVcc} vs. CASE TEMP.



EXTERNAL CONNECTIONS



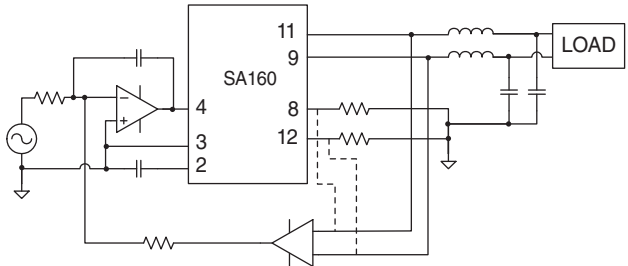
12-pin Power SIP PACKAGE STYLE DP



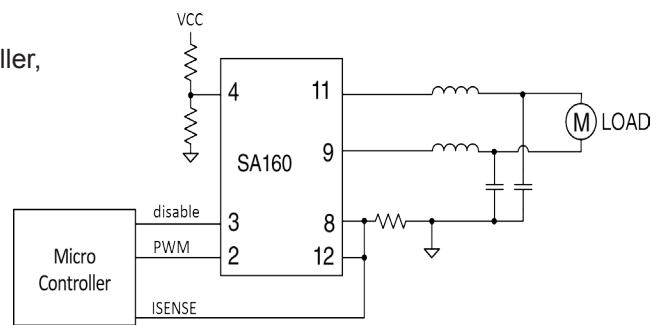
12-pin Power SIP PACKAGE STYLE EE

TYPICAL APPLICATION

A wide variety of loads can be driven in either the voltage mode or the current mode. The most common applications use three external blocks: a low pass filter converting pulse width data to an analog output, a difference amplifier to monitor voltage or current and an error amplifier. Filter inductors must be suitable for square waves at the switching frequency (laminated steel is generally not acceptable). Filter capacitors must be low ESR and rated for the expected ripple current. A difference amplifier with gain of less than one translates the differential output voltage to a single feedback voltage. Dashed line connections and a higher gain difference amplifier would be used for current control. The error amplifier integrates the difference between the input and feedback voltages to close the loop.



The SA160 also can be controlled through a microcontroller, See figure to right.



GENERAL

Please read Application Note 30 on "PWM Basics". Refer to Application Note 1 "General Operating Considerations" for helpful information regarding power supplies, heat sinking and mounting. Visit www.apexanalog.com for design tools that help automate pwm filter design; heat sink selection; Apex Microtechnology's complete Application Notes library; Technical Seminar Workbook; and Evaluation Kits.

PWM OSCILLATOR – INTERNAL OR EXTERNAL

The SA160 contains an internal PWM oscillator whose frequency is determined by an external capacitor connected between pin 1 and pin 2. Maximum frequency is 125 kHz. The user may also disregard the internal PWM oscillator and supply the SA160 with an external TTL pulse generator up to 250kHz.

PIN DESCRIPTION

V_{cc} - is the low voltage supply for powering internal logic and drivers for the lowside and highside MOSFETS. The supplies for the highside drivers are derived from this voltage.

V_s - is the higher voltage H-bridge supply. The MOSFETS obtain the output current from this supply pin. The voltage on this pin is limited to +80V by the drive IC. The MOSFETS are rated at 100 volts.

ISENSE A & B - These are tied to power gnd directly or through sense resistors.

ANALOG GND - is the reference for the internal PWM oscillator. Connect this pin to pin 6. Connect low side of Vcc supply and any other supply used to generate analog input signals to ANALOG GND.

ANALOG INPUT - is an analog input for controlling the PWM pulse width of the bridge. A voltage higher than Vcc/2 will produce greater than 50% duty cycle pulses out of B OUT. A voltage lower than Vcc/2 will produce greater than 50% duty cycle pulses out of A OUT. If using in the digital mode, bias this point at 1/2 the logic high level.

DISABLE - Is the connection for disabling all 4 output switches. DISABLE high overrides all other inputs. When taken low, everything functions normally. An internal pullup to Vcc will keep DISABLE high if pin left open.

PWM INPUT - Is the TTL compatible digital input for controlling the PWM pulse width of the bridge. A duty cycle greater than 50% will produce greater than 50% duty cycle pulses out of the A out. A duty cycle less than 50% will produce greater than 50% duty cycle from the B out. For analog inputs, the integration capacitor for the internal clock must be connected between this pin and analog ground. The internal switching frequency is programmable up to 125 kHz by selection of the integration capacitor. The formula is:

$$C_F \text{ (pF)} = \left(\frac{1.44 \times 10^7}{F_{sw}} \right) - 50$$

BYPASSING

Adequate bypassing of the power supplies is required for proper operation. Failure to do so can cause erratic and low efficiency operation as well as excessive ringing at the outputs. The Vs supply should be bypassed with at least a 1µF ceramic capacitor in parallel with another low ESR capacitor of at least 10µF per amp of output current. Capacitor types rated for switching applications are the only types that should be considered. The 1µF ceramic capacitor must be physically connected directly to the Vs and POWER GND pins. Even one inch of lead length will cause excessive ringing at the outputs. This is due to the very fast switching times and the inductance of the lead connection. The bypassing requirements of the Vcc supply are less stringent, but still necessary. A .1µF to .47µF ceramic capacitor connected directly to the Vcc and ANALOG GND pins will suffice.

PCB LAYOUT

The designer needs to appreciate that the SA160 combines in one circuit both high speed high power switching and low level analog signals. Certain layout rules of thumb must be considered when a circuit board layout is designed using the SA160:

1. Bypassing of the power supplies is critical. Capacitors must be connected directly to the power supply pins with very short lead lengths (well under 1 inch). Ceramic chip capacitors are best.
2. Connect ANALOG GND to POWER GND with a conductor having no intermediate connections. Connect all Vs power supply, filter and load related ground connections to POWER GND keeping these conductors separate until reaching pin 6. Connect all Vcc power supply and input signal related ground connections to ANALOG GND keeping conductors separate until reaching pin 1. Do not allow ground loops to form by making additional ground connections at the low side of the physic

There are two load current sensing pins, I SENSE A and I SENSE B. The two pins can be shorted to POWER GND in the voltage mode connection but both must be used in the current mode connection. It is recommended that R SENSE resistors be non-inductive. When A OUT is high and B OUT is low, the load current flows from A OUT to B OUT and out of the ISENSE B pin. When B OUT is high and A OUT is low, the load current flows from B OUT to A OUT and out of the INSENSE A pin. The SA160 has no internal current limit.

TRANSIENT SUPPRESSION

An RC network of a 100 pF Capacitor and a one ohm resistor is required as shown in the external connection diagram on page 1. This network assures proper operation under various loads. Minimal power is dissipated in the resistor.

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