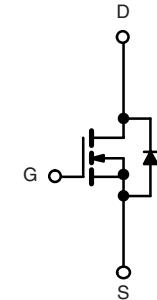
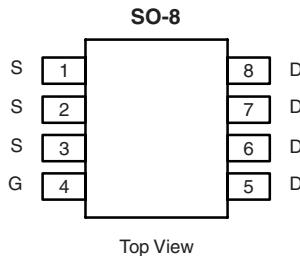


# N-Channel Reduced $Q_g$ , Fast Switching MOSFET

PRODUCT SUMMARY		
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
30	0.008 at $V_{GS} = 10$ V	16
	0.011 at $V_{GS} = 4.5$ V	15

## FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFETs
- PWM Optimized for High Efficiency
- 100 %  $R_g$  Tested



**Ordering Information:** Si4860DY-T1-E3 (Lead (Pb)-free)  
Si4860DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

## ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	10 s	Steady State	Unit
Drain-Source Voltage	$V_{DS}$	30	$\pm 20$	V
Gate-Source Voltage	$V_{GS}$			
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$I_D$	16	11	A
		13	8	
Pulsed Drain Current	$I_{DM}$	$\pm 50$		A
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	3.0	1.40	
Maximum Power Dissipation <sup>a</sup>	$P_D$	3.5	1.6	W
		2.2	1.0	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		°C

## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (MOSFET) <sup>a</sup>	$t \leq 10$ s	$R_{thJA}$	29	35
	Steady State		67	80
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	13	16	°C/W

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

**MOSFET SPECIFICATIONS**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$	1.0			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 20 \text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24 \text{ V}$ , $V_{GS} = 0 \text{ V}$		1		$\mu\text{A}$
		$V_{DS} = 24 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $T_J = 70^\circ\text{C}$		5		
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{DS} \geq 5 \text{ V}$ , $V_{GS} = 10 \text{ V}$	40			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$ , $I_D = 16 \text{ A}$		0.0066	0.008	$\Omega$
		$V_{GS} = 4.5 \text{ V}$ , $I_D = 15 \text{ A}$		0.0090	0.011	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15 \text{ V}$ , $I_D = 16 \text{ A}$		60		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 3 \text{ A}$ , $V_{GS} = 0 \text{ V}$		0.70	1.1	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 15 \text{ V}$ , $V_{GS} = 4.5 \text{ V}$ , $I_D = 16 \text{ A}$		13	18	nC
Gate-Source Charge	$Q_{gs}$			5		
Gate-Drain Charge	$Q_{gd}$			4.0		
Gate Resistance	$R_g$		1.0	1.7	2.9	$\Omega$
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = 15 \text{ V}$ , $R_L = 15 \Omega$ $I_D \geq 1 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ , $R_g = 6 \Omega$		18	27	ns
Rise Time	$t_r$			12	18	
Turn-Off Delay Time	$t_{d(\text{off})}$			46	70	
Fall Time	$t_f$			19	30	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 3 \text{ A}$ , $di/dt = 100 \text{ A}/\mu\text{s}$		40	70	

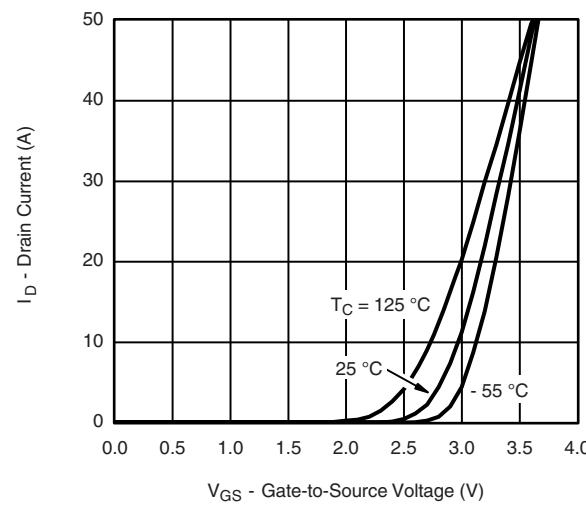
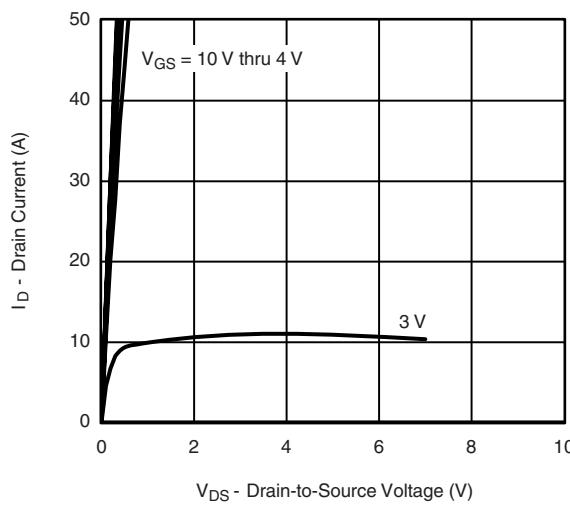
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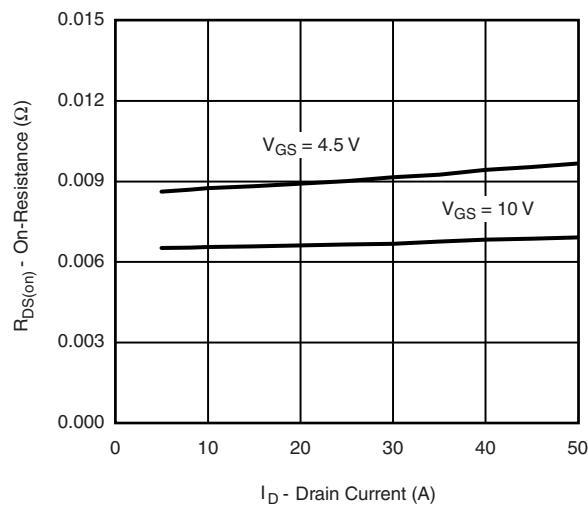
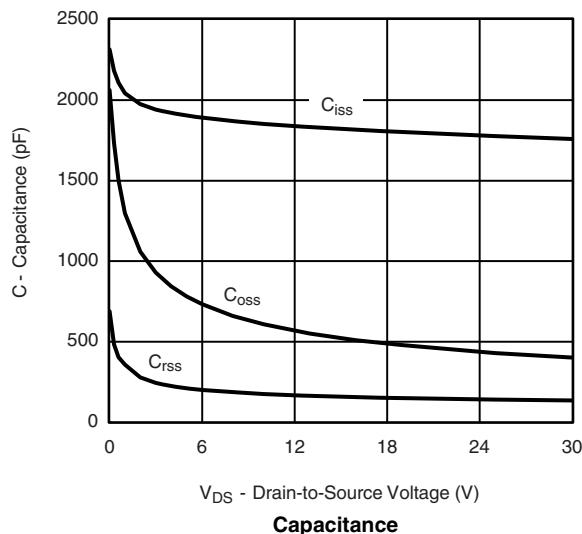
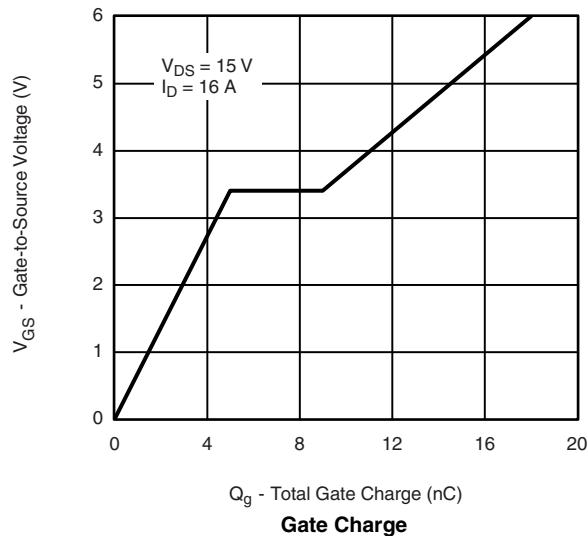
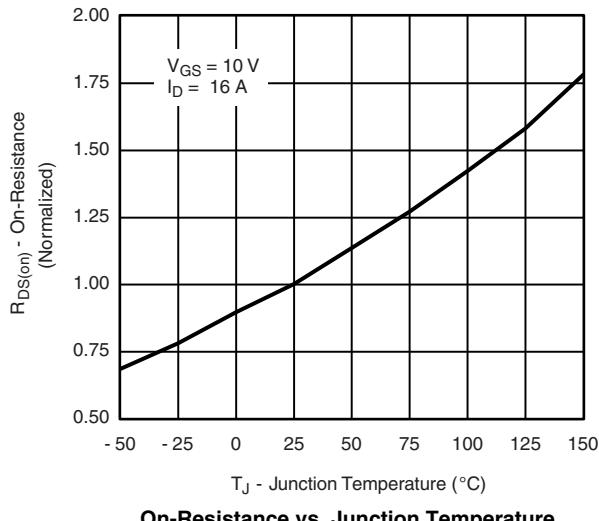
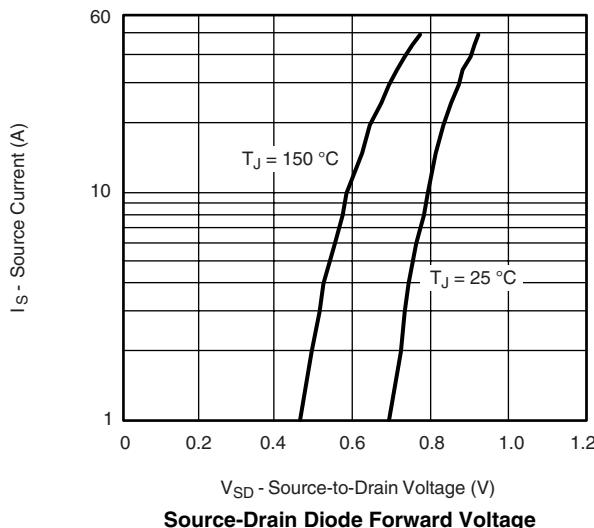
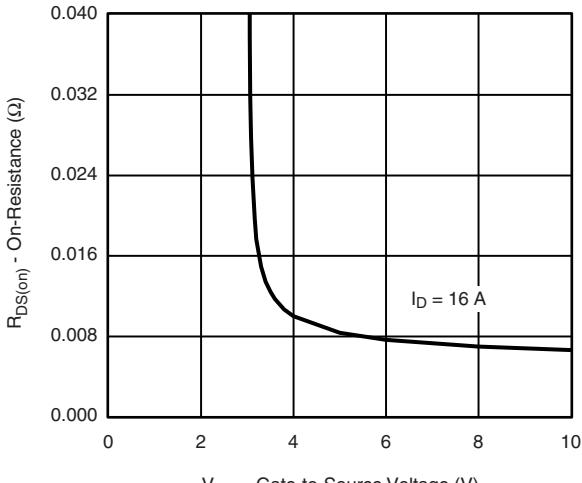
a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2 \%$ .

b. Guaranteed by design, not subject to production testing.

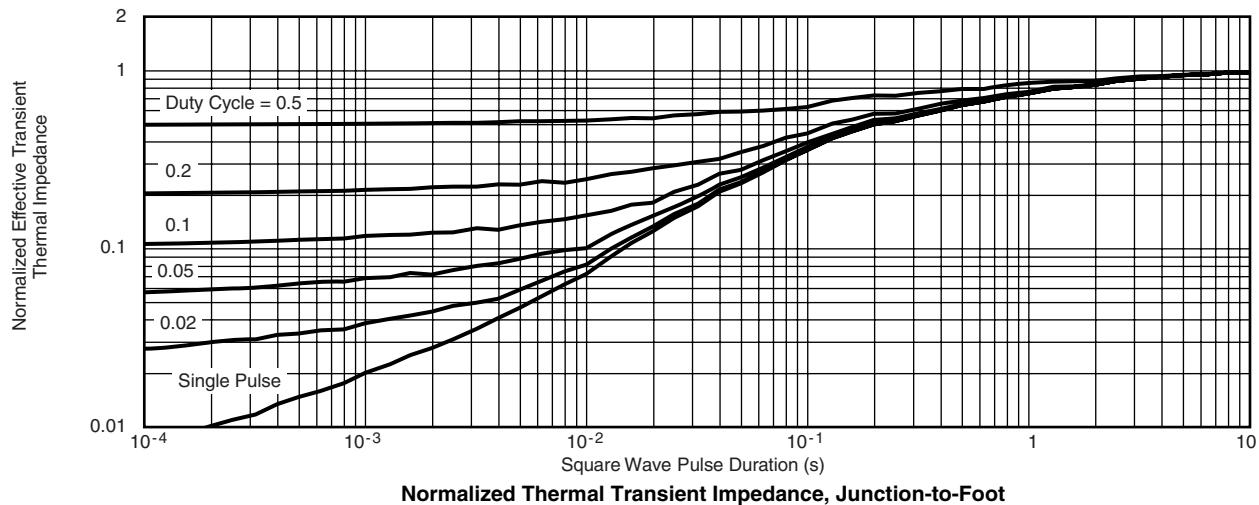
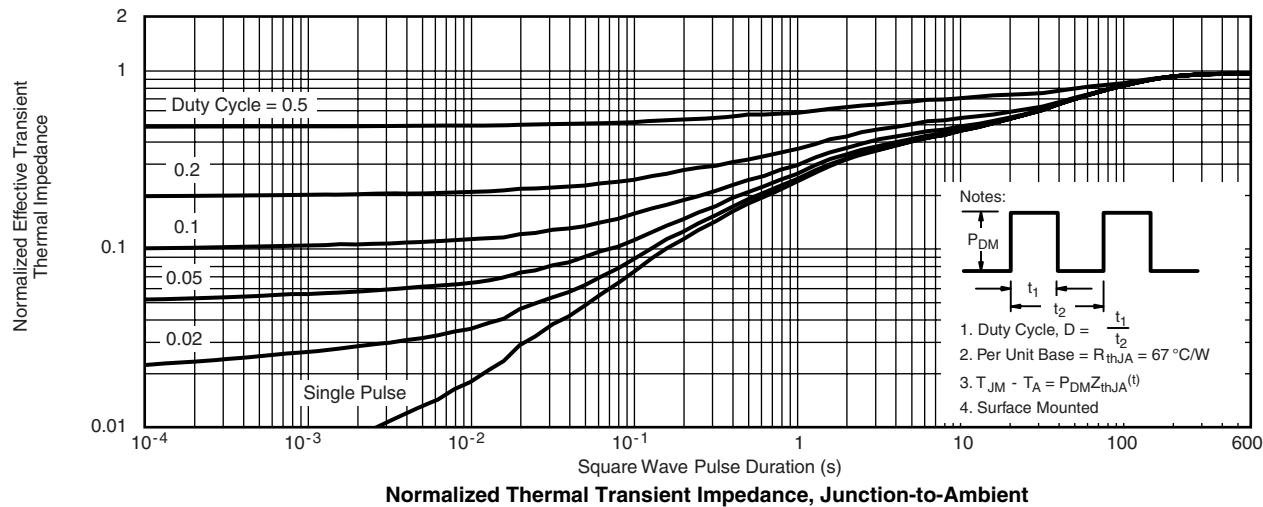
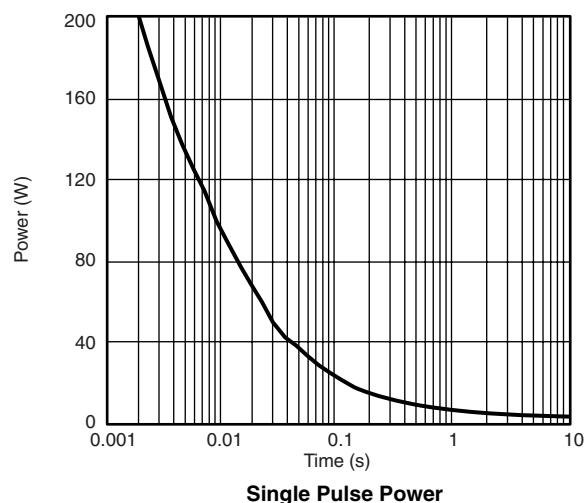
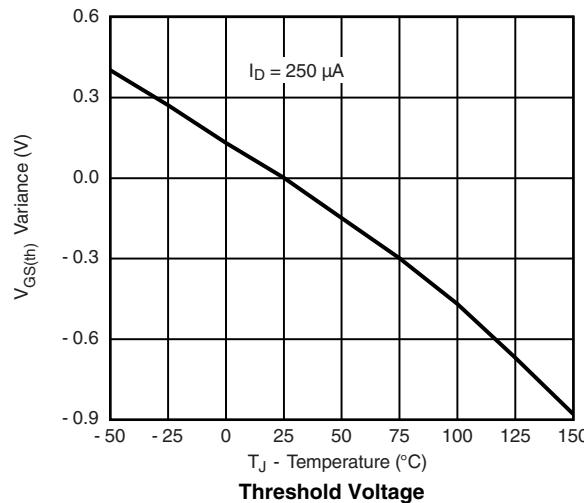
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS**  $25^\circ\text{C}$ , unless otherwise noted



**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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