ALUMINUM ELECTROLYTIC CAPACITORS









- Chip type, low impedance temperature range up to +105°C.
- Designed for surface mounting on high density PC board.
- Applicable to automatic mounting machine fed with carrier tape.
- Compliant to the RoHS directive (2011/65/EU).

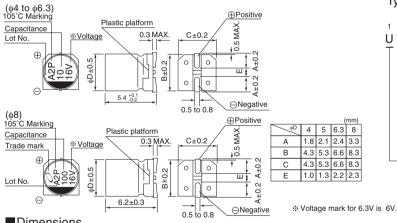




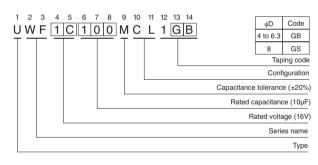
■Specifications

- opcomoations		,				,							
Item	Performance Characteristics												
Category Temperature Range	−55 to +105°C												
Rated Voltage Range	6.3 to 35V												
Rated Capacitance Range	1 to 220µF												
Capacitance Tolerance	±20% at 120Hz, 20°C												
Leakage Current	After 2 minutes' application of rated voltage, leakage current is not more than 0.01CV or 3 (µA), whichever is greater.												
Tangent of loss angle (tan δ)	Measurement frequency : 120Hz at 20°C												
	Rated voltage (V)	e (V) 6.3		10	16	2	25	35					
	tan δ (MAX.)	0.22	(0.19	0.16	0.	14	0	.12				
Stability at Low Temperature	Measurement frequency : 120Hz												
	Rated voltage (V)			6.3	10	16	25		35				
	Impedance ratio	Z-25°C / Z+2	20°C	2	2	2	2		2				
	ZT / Z20 (MAX.)	Z-55°C / Z+2	20°C	4	4	3	3		3				
							ance cha	nce change Within ±20% of the initial capacitance valu					
Endurance							tan δ 200% or			or less than the initial specified value			
	voltage is applied	05°C.		Leakage current Le			Less than	s than or equal to the initial specified value					
	After storing the capacitors under no load at 105°C for 1000 hours and then performing voltage treatment based on JIS C 5101-4 clause 4.1 at												
Shelf Life										ment based on JIS C 5101-4 clause 4.1 at			
	20°C, they shall meet the specified values for the endurance characteristics listed above.												
Burtala and the salah dari	The capacitors are kept on a hot plate for 30 seconds, which is							itanc	e change	Within ±10% of the initial capacitance value			
Resistance to soldering heat	maintained at 250°C. The capacitors shall meet the						tan δ			Less than or equal to the initial specified value			
neat	characteristic requirements listed at right when they are removed from the plate and restored to 20°C. Leakage current Leakage current Leakage current								Less than or equal to the initial specified value				
Marking	Black print on the ca		· ·										
Marking	Didok print off the ce	ioo iop.											

■Chip Type



Type numbering system (Example: 16V 10µF)



■ Dimensions

Dimonolo																
V 6.3		10			16			25			35					
Cap. (µF)	Code	de 0J		1A			1C			1E			1V			
1	010		!	!			!			!		!	!	4	5.0	50
1.5	1R5		i				İ		i i			i	i	4	5.0	50
2.2	2R2		i				İ		I I	i i		i i	İ	4	5.0	50
3.3	3R3		!	!		!	!			!		!	!	4	5.0	50
4.7	4R7						i		l		4	5.0	50	4	5.0	50
6.8	6R8		i i	i i			İ			i i	4	5.0	50	5	2.6	80
10	100		!				!	4	5.0	50	5	2.6	80	5	2.6	80
15	150						1	5	2.6	80	6.3	1.3	115	6.3	1.3	115
22	220	4	5.0	50	5	2.6	80	5	2.6	80	6.3	1.3	115	6.3	1.3	115
33	330	5	2.6	80	5	2.6	80	6.3	1.3	115	6.3	1.3	¦ 115	8	0.8	150
47	470	5	2.6	80	6.3	1.3	115	6.3	1.3	115	8	0.8	150	8	0.8	150
68	680	6.3	1.3	115	6.3	1.3	115	8	0.8	150	8	0.8	150			
100	101	6.3	1.3	115	8	0.8	150	8	0.8	150		l I	I I			
150	151	8	0.8	150	8	0.8	150		 	İ		1	!	Case size	Impedance	Rated
220	221	8	0.8	150			!			i		į	i	φD (mm)	impedance	Rated ripple

Frequency coefficient of rated ripple current

Frequency	50 Hz	120 Hz	300 Hz	1 kHz	10 kHz or more
Coefficient	0.35	0.50	0.64	0.83	1.00

Max. Impedance (Ω) at 20°C 100kHz Rated ripple current (mArms) at 105°C 100kHz

- Taping specifications are given in page 23.
- Recommended land size, soldering by reflow are given in page 18, 19.
- Please select UJ(p.160) series if high C/V products are reqired.
- Please refer to page 3 for the minimum order quantity.

CAT.8100D