



TN8, TS8 and TYNx08 Series

SENSITIVE & STANDARD

8A SCRs

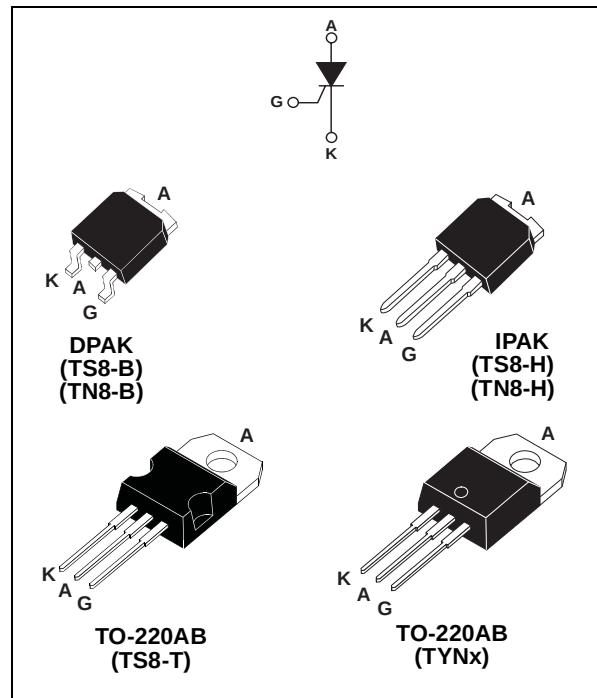
MAIN FEATURES:

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM}/V_{RRM}	600 to 1000	V
I_{GT}	0.2 to 15	mA

DESCRIPTION

Available either in sensitive (TS8) or standard (TN8 / TYN) gate triggering levels, the 8A SCR series is suitable to fit all modes of control, found in applications such as overvoltage crowbar protection, motor control circuits in power tools and kitchen aids, inrush current limiting circuits, capacitive discharge ignition and voltage regulation circuits...

Available in through-hole or surface-mount packages, they provide an optimized performance in a limited space area.



ABSOLUTE RATINGS (limiting values)

Symbol	Parameter			Value		Unit
I _{T(RMS)}	RMS on-state current (180° conduction angle)		T _c = 110°C	8		A
I _{T(AV)}	Average on-state current (180° conduction angle)		T _c = 110°C	5		A
				TS8/TN8	TYN	
I _{TSM}	Non repetitive surge peak on-state current	tp = 8.3 ms	T _j = 25°C	73	100	A
		tp = 10 ms		70	95	
I ² _t	I ² _t Value for fusing	tp = 10 ms	T _j = 25°C	24.5	45	A ² s
di/dt	Critical rate of rise of on-state current I _G = 2 x I _{GT} , tr ≤ 100 ns	F = 60 Hz	T _j = 125°C	50		A/μs
I _{GM}	Peak gate current	tp = 20 μs	T _j = 125°C	4		A
P _{G(AV)}	Average gate power dissipation		T _j = 125°C	1		W
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125		°C
V _{RGM}	Maximum peak reverse gate voltage (for TN8 & TYN only)			5		V

TN8, TS8 and TYNx08 Series

ELECTRICAL CHARACTERISTICS (T_j = 25°C, unless otherwise specified)

■ SENSITIVE

Symbol	Test Conditions			TS820	Unit
I _{GT}	V _D = 12 V R _L = 140 Ω		MAX.	200	μA
V _{GT}			MAX.	0.8	V
V _{GD}	V _D = V _{DRM} R _L = 3.3 kΩ R _{GK} = 220 Ω	T _j = 125°C	MIN.	0.1	V
V _{RG}	I _{RG} = 10 μA		MIN.	8	V
I _H	I _T = 50 mA R _{GK} = 1 kΩ		MAX.	5	mA
I _L	I _G = 1 mA R _{GK} = 1 kΩ		MAX.	6	mA
dV/dt	V _D = 65 % V _{DRM} R _{GK} = 220 Ω	T _j = 125°C	MIN.	5	V/μs
V _{TM}	I _{TM} = 16 A t _p = 380 μs	T _j = 25°C	MAX.	1.6	V
V _{t0}	Threshold voltage	T _j = 125°C	MAX.	0.85	V
R _d	Dynamic resistance	T _j = 125°C	MAX.	46	mΩ
I _{DRM} I _{RRM}	V _{DRM} = V _{RRM} R _{GK} = 220 Ω	T _j = 25°C	MAX.	5	μA
		T _j = 125°C		1	mA

■ STANDARD

Symbol	Test Conditions			TN805	TN815	TYNx08	Unit
I _{GT}	V _D = 12 V R _L = 33 Ω		MIN.	0.5	2	2	mA
			MAX.	5	15	15	
V _{GT}			MAX.	1.3			V
V _{GD}	V _D = V _{DRM} R _L = 3.3 kΩ	T _j = 125°C	MIN.	0.2			V
I _H	I _T = 100 mA Gate open		MAX.	25	40	30	mA
I _L	I _G = 1.2 I _{GT}		MAX.	30	50	70	mA
dV/dt	V _D = 67 % V _{DRM} Gate open	T _j = 125°C	MIN.	50	150	150	V/μs
V _{TM}	I _{TM} = 16 A t _p = 380 μs	T _j = 25°C	MAX.	1.6			V
V _{t0}	Threshold voltage	T _j = 125°C	MAX.	0.85			V
R _d	Dynamic resistance	T _j = 125°C	MAX.	46			mΩ
I _{DRM} I _{RRM}	V _{DRM} = V _{RRM}	T _j = 25°C	MAX.	5			μA
		T _j = 125°C		2			mA

THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
R _{th(j-c)}	Junction to case (DC)		20	°C/W
R _{th(j-a)}	Junction to ambient (DC)	TO-220AB	60	°C/W
		IPAK	100	
		S = 0.5 cm ² DPAK	70	

S= copper surface under tab

PRODUCT SELECTOR

Part Number	Voltage (xxx)				Sensitivity	Package
	600 V	700 V	800 V	1000 V		
TN805-xxxB	X		X		5 mA	DPAK
TN805-xxxH	X		X		5 mA	IPAK
TN815-xxxB	X		X		15 mA	DPAK
TN815-xxxH	X		X		15 mA	IPAK
TS820-xxxB	X	X			0.2 mA	DPAK
TS820-xxxH	X	X			0.2 mA	IPAK
TS820-xxxT	X	X			0.2 mA	TO-220AB
TYNx08	X		X	X	15 mA	TO-220AB

ORDERING INFORMATION

TN

8

05

-

600

B

(-TR)

STANDARD
SCR
SERIES

CURRENT: 8A

SENSITIVITY:
05: 5mA
15: 15mA

VOLTAGE:
600: 600V
800: 800V

PACKAGE:
B: DPAK
H: IPAK

PACKING MODE:
Blank: Tube
-TR: DPAK Tape & Reel

TS

8

20

-

600

B

(-TR)

SENSITIVE
SCR
SERIES

CURRENT: 8A

SENSITIVITY:
20: 200µA

VOLTAGE:
600: 600V
700: 700V

PACKAGE:
B: DPAK
H: IPAK
T: TO-220AB

PACKING MODE:
Blank: Tube
-TR: DPAK Tape & Reel

TYN

6

08

(RG)

STANDARD
SCR
SERIES

VOLTAGE:
6: 600V
8: 800V
10: 1000V

CURRENT: 8A

PACKING MODE:
Blank: Bulk
RG: Tube

TN8, TS8 and TYNx08 Series

OTHER INFORMATION

Part Number	Marking	Weight	Base Quantity	Packing mode
TN805-x00B	TN805x00	0.3 g	75	Tube
TN805-x00B-TR	TN805x00	0.3 g	2500	Tape & reel
TN805-x00H	TN805x00	0.4 g	75	Tube
TN815-x00B	TN815x00	0.3 g	75	Tube
TN815-x00B-TR	TN815x00	0.3 g	2500	Tape & reel
TN815-x00H	TN815x00	0.4 g	75	Tube
TS820-x00B	TS820x00	0.3 g	75	Tube
TS820-x00B-TR	TS820x00	0.3 g	2500	Tape & reel
TS820-x00H	TS820x00	0.4 g	75	Tube
TS820-x00T	TS820x00T	2.3 g	50	Tube
TYNx08	TYNx08	2.3 g	250	Bulk
TYNx08RG	TYNx08	2.3 g	50	Tube

Note: x = voltage

Fig. 1: Maximum average power dissipation versus average on-state current.

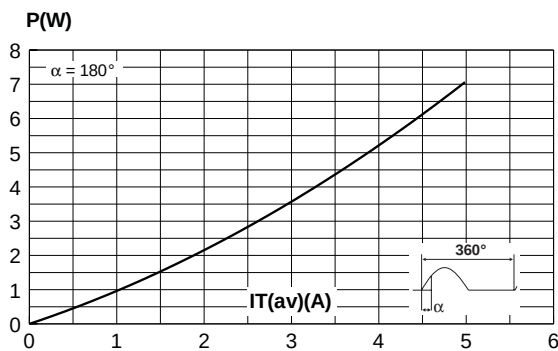


Fig. 2-1: Average and D.C. on-state current versus case temperature.

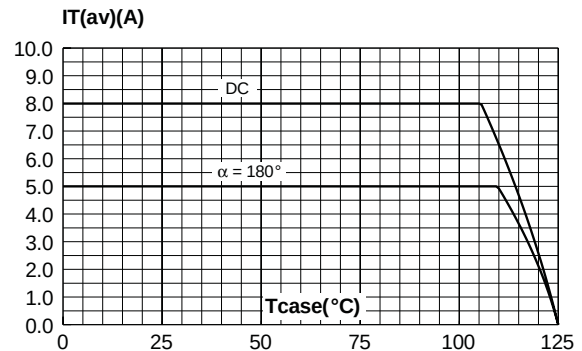


Fig. 2-2: Average and D.C. on-state current versus ambient temperature (device mounted on FR4 with recommended pad layout) (DPAK).

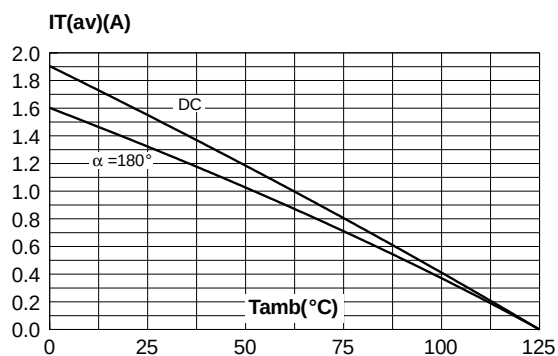


Fig. 3-1: Relative variation of thermal impedance junction to case versus pulse duration.

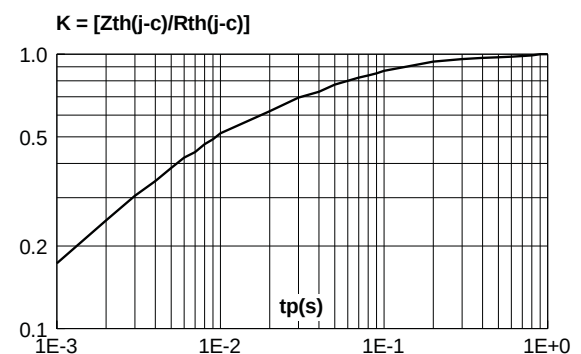


Fig. 3-2: Relative variation of thermal impedance junction to ambient versus pulse duration (recommended pad layout, FR4 PC board for DPAK).

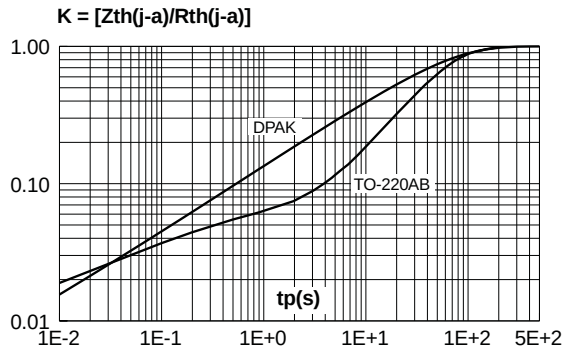


Fig. 4-2: Relative variation of gate trigger current and holding current versus junction temperature for TN8 & TYN series.

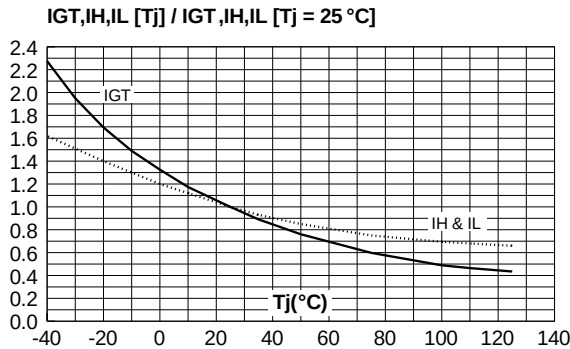


Fig. 6: Relative variation of dV/dt immunity versus gate-cathode resistance (typical values) for TS8 series.

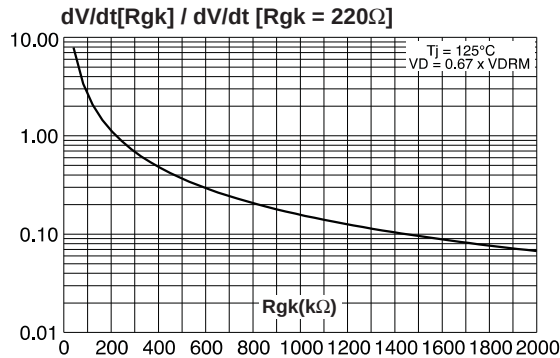


Fig. 4-1: Relative variation of gate trigger current and holding current versus junction temperature for TS8 series.

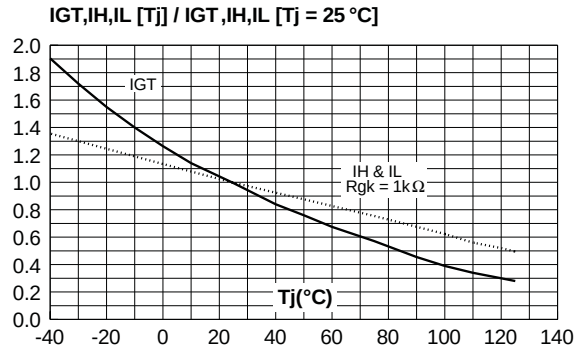


Fig. 5: Relative variation of holding current versus gate-cathode resistance (typical values) for TS8 series.

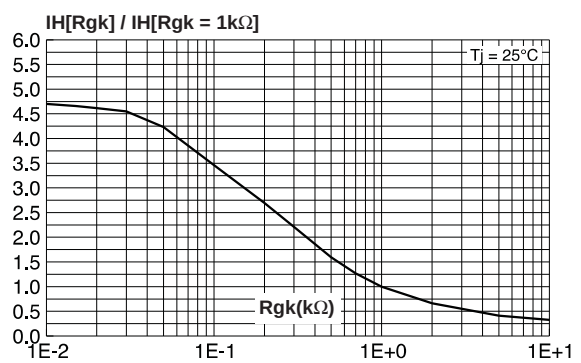


Fig. 7: Relative variation of dV/dt immunity versus gate-cathode capacitance (typical values) for TS8 series.

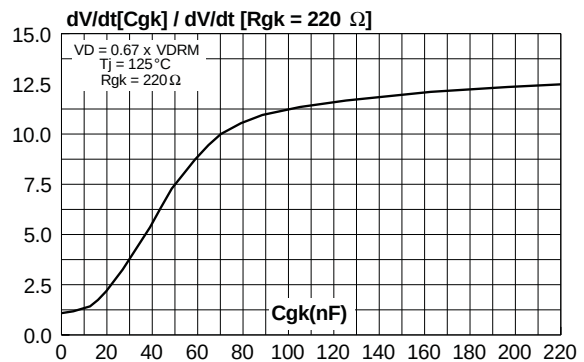


Fig. 8: Surge peak on-state current versus number of cycles. TS8/TN8/TYN.

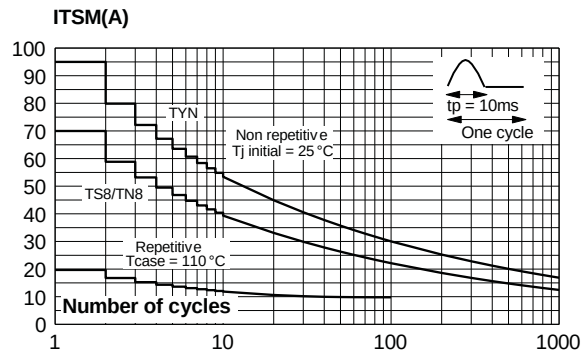


Fig. 10: On-state characteristics (maximum values).

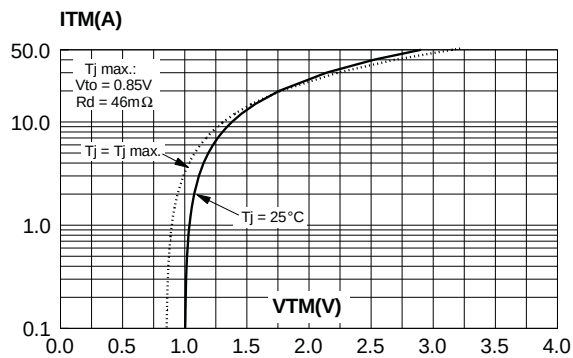


Fig. 9: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10$ ms, and corresponding values of I^2t .

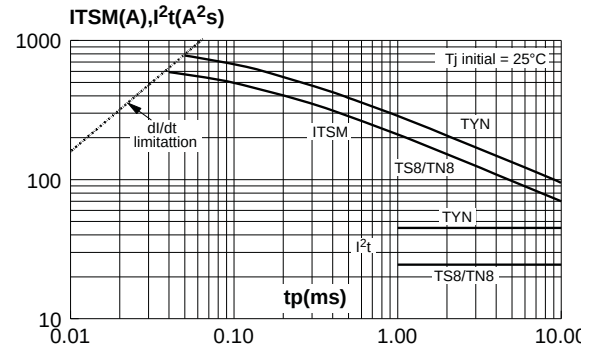
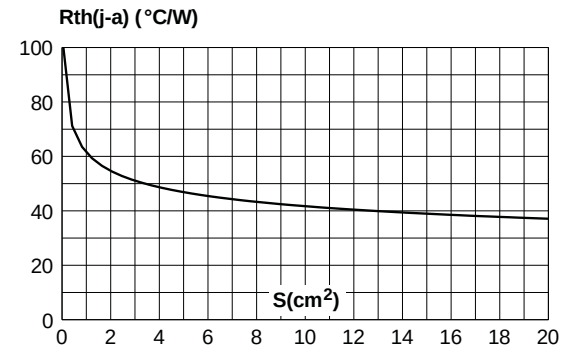
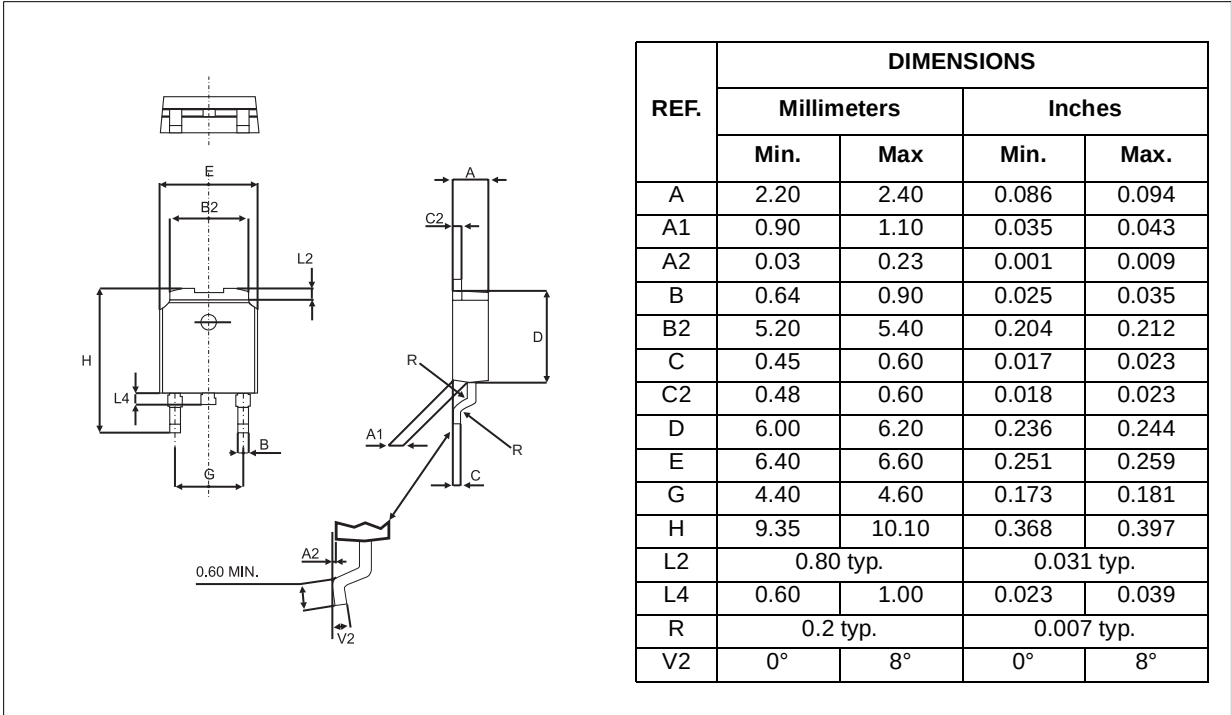


Fig. 11: Thermal resistance junction to ambient versus copper surface under tab (Epoxy printed circuit board FR4, copper thickness: 35 μm) (DPAK).



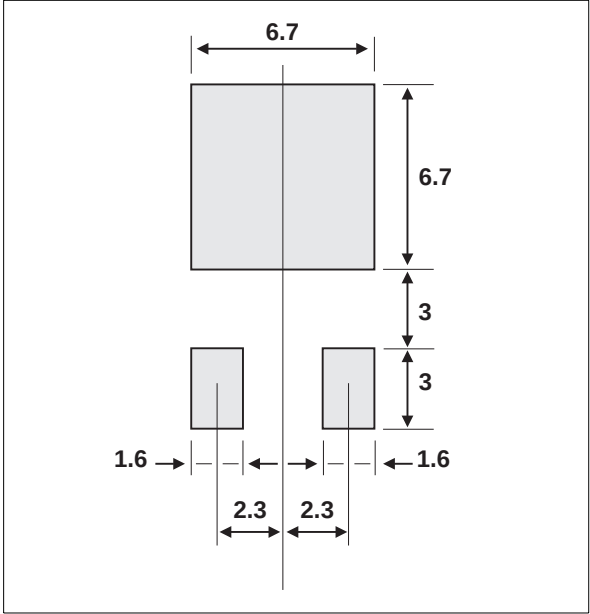
PACKAGE MECHANICAL DATA

DPAK (Plastic)



FOOTPRINT DIMENSIONS (in millimeters)

DPAK (Plastic)



PACKAGE MECHANICAL DATA

IPAK (Plastic)

REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A3	0.7		1.3	0.027		0.051
B	0.64		0.9	0.025		0.035
B2	5.2		5.4	0.204		0.212
B3			0.85			0.033
B5		0.3			0.035	
B6			0.95			0.037
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
E	6.4		6.6	0.252		0.260
G	4.4		4.6	0.173		0.181
H	15.9		16.3	0.626		0.641
L	9		9.4	0.354		0.370
L1	0.8		1.2	0.031		0.047
L2		0.8	1		0.031	0.039
V1		10°			10°	

TO-220AB (Plastic - with notches)

REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
C	1.23	1.32	0.048	0.051
D	2.40	2.72	0.094	0.107
E	0.49	0.70	0.019	0.027
F	0.61	0.88	0.024	0.034
F1	1.14	1.70	0.044	0.066
F2	1.14	1.70	0.044	0.066
G	4.95	5.15	0.194	0.202
G1	2.40	2.70	0.094	0.106
H2	10	10.40	0.393	0.409
L2	16.4 typ.		0.645 typ.	
L4	13	14	0.511	0.551
L5	2.65	2.95	0.104	0.116
L6	15.25	15.75	0.600	0.620
L7	6.20	6.60	0.244	0.259
L9	3.50	3.93	0.137	0.154
M	2.6 typ.		0.102 typ.	
Diam.	3.75	3.85	0.147	0.151

PACKAGE MECHANICAL DATA

TO-220AB (Without notches)

REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	15.20		15.90	0.598		0.625
a1		3.75			0.147	
a2	13.00		14.00	0.511		0.551
B	10.00		10.40	0.393		0.409
b1	0.61		0.88	0.024		0.034
b2	1.23		1.32	0.048		0.051
C	4.40		4.60	0.173		0.181
c1	0.49		0.70	0.019		0.027
c2	2.40		2.72	0.094		0.107
e	2.40		2.70	0.094		0.106
F	6.20		6.60	0.244		0.259
I	3.75		3.85	0.147		0.151
I4	15.80	16.40	16.80	0.622	0.646	0.661
L	2.65		2.95	0.104		0.116
I2	1.14		1.70	0.044		0.066
I3	1.14		1.70	0.044		0.066
M		2.60			0.102	

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