

Products > ADC1413D080W1 Demo board With FPGA

ADC1413D080W1 Obsolete

ADC1413D080W1 Demo board With FPGA

IDT's ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204A feature set.

Features

- SMA connector for clock and analog input signals
- On-board Data clock generator for ADC and FPGA
- On-board Virtex 5 FPGA for data-acquisition
- Single power supply (on-board regulators)

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Buy Sample
ADC1413D080W1-DB	Obsolete	BD0	BOARD	0	I	No	Availability

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ADC1413D080HN	Dual 14 bits ADC; 80 Msps; serial JESD204A

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Images

ADC1413D080W1 - Evaluation Board

