TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC74HC14AP, TC74HC14AF, TC74HC14AFN

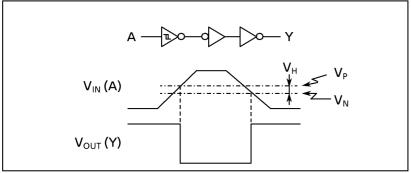
HEX SCHMITT INVERTER

The TC74HC14A is a high speed CMOS SCHMITT INVERTER fabricated with silicon gate C2MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation. Pin configuration and function are the same as the TC74HC04A but the inputs have 25% Vcc hysteresis and with its schmitt trigger function, the TC74HC14A can be used as a line receivers which will receive slow input signals. All inputs are equipped with protection circuits against static discharge or transient excess voltage.

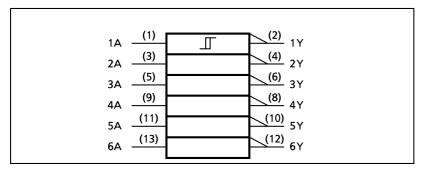
FEATURES:

- High Speed------ t_{pd} = 11ns(typ.) at V_{CC} = 5V
- Low Power Dissipation ············ $I_{CC} = 1\mu A(Max.)$ at Ta = 25°C
- High Noise Immunity $V_H = 1.1V$ at $V_{CC} = 5V$
- Output Drive Capability 10 LSTTL Loads
- Symmetrical Output Impedance... | I_{OH} | = I_{OL} = 4mA(Min.)
- Balanced Propagation Delays $\cdots t_{pLH} \simeq t_{pHL}$
- Wide Operating Voltage Range ···· V_{CC} (opr.) = 2V ~ 6V
- Pin and Function Compatible with 74LS14

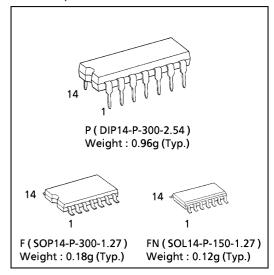
SYSTEM DIAGRAM, WAVEFORM



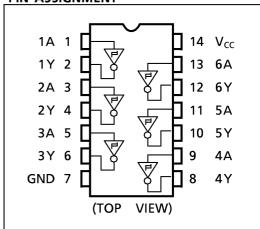
IEC LOGIC SYMBOL



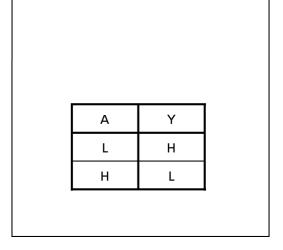
(Note) The JEDEC SOP (FN) is not available in Japan.



PIN ASSIGNMENT



TRUTH TABLE



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	V _{cc}	-0.5~7	V
DC Input Voltage	V _{IN}	$-0.5 \sim V_{CC} + 0.5$	V
DC Output Voltage	V _{OUT}	$-0.5 \sim V_{CC} + 0.5$	V
Input Diode Current	I _{IK}	± 20	mA
Output Diode Current	I _{OK}	± 20	mA
DC Output Current	I _{OUT}	± 25	mA
DC V _{CC} / Ground Current	I _{cc}	± 50	mA
Power Dissipation	P _D	500 (DIP)* / 180 (SOP)	mW
Storage Temperature	T _{stg}	−65~150	°C

*500mW in the range of Ta= $-40^{\circ}\text{C}\sim65^{\circ}\text{C}$. From Ta=65°C to 85°C a derating factor of $-10\text{mW}/^{\circ}\text{C}$ shall be applied until 300mW.

RECOMMENDED OPERTING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V _{CC}	2~6	V
Input Voltage	V _{IN}	0∼V _{cc}	V
Output Voltage	V _{OUT}	0~V _{cc}	V
Operating Temperature	T _{opr}	−40~85	°C

DC ELECTRICAL CHARACTERISITCS

PARAMETER SYMBOL TEST CON		NDITION V _{cc}		Ta = 25°C		$Ta = -40 \sim 85^{\circ}C$		UNIT		
		TEST CONDITION		(V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Positive Threshold Voltage	V _P			2.0 4.5 6.0	1.0 2.3 3.0	1.25 2.70 3.50	1.50 3.15 4.20	1.0 2.3 3.0	1.50 3.15 4.20	V
Negative Threshold Voltage	V _N			2.0 4.5 6.0	0.30 1.13 1.50	0.65 1.60 2.30	0.9 2.0 2.6	0.30 0.13 1.50	0.9 2.0 2.6	V
Hysteresis Voltage	V _H			2.0 4.5 6.0	0.3 0.6 0.8	0.6 1.1 1.2	1.0 1.4 1.7	0.3 0.6 0.8	1.0 1.4 1.7	V
High Level Output Voltage	V _{OH}	V _{I N} = V _{I L}	$I_{OH} = -20\mu A$	2.0 4.5 6.0	1.9 4.4 5.9	2.0 4.5 6.0	_ _ _	1.9 4.4 5.9	_ _ _	V
			$I_{OH} = -4 \text{ mA}$ $I_{OH} = -5.2 \text{ mA}$	4.5 6.0	4.18 5.68	4.31 5.80	_	4.13 5.63	_	
Low-Level Output Voltage	V _{OL}	V _{OL}	I _{OL} = 20μΑ	2.0 4.5 6.0	_ _ _	0.0 0.0 0.0	0.1 0.1 0.1		0.1 0.1 0.1	v
			$I_{OL} = 4 mA$ $I_{OL} = 5.2 mA$	4.5 6.0	1 1	0.17 0.18	0.26 0.26	_ _	0.33 0.33	_
Input Leakage Current	I _{IN}	$V_{IN} = V_{CC}$ or GND		6.0	_	_	± 0.1	_	± 1.0	
Quiescent Supply Current	I _{cc}	$V_{IN} = V_{CO}$	c or GND	6.0	-	_	1.0	_	10.0	μ A

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AC ELECTRICAL CHARACTERISTICS ($C_L = 15pF$, $V_{CC} = 5V$, $T_0 = 25^{\circ}C$, Input $t_r = t_f = 6ns$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Transition Time	t _{TLH} t _{THL}		_	4	8	ns
Propagation Delay Time	t _{pLH} t _{pHL}		_	11	21	

AC ELECTRICAL CHARACTERISTICS ($C_L = 50pF$, Input $t_r = t_f = 6ns$)

PARAMETER	SYMBOL	TEST CONDITION IV		Ta = 25°C			Ta = -40~85°C		ПИПТ
FARAIVIETER		TEST CONDITION	V _{cc} (V)	MIN.	TYP.	MAX.	MIN.	MAX.	OWIT
	t _{TLH}		2.0	_	30	75	_	95	
Output Transition Time			4.5	_	8	15	_	19	
· .	t _{THL}		6.0	1	7	13	_	16	ns
	+		2.0	-	42	125	_	155	
Propagation Delay Time	t _{pLH}		4.5	_	14	25	_	31	
	t _{pHL}		6.0	-	12	21	_	26	
Input Capacitance	C _{IN}			_	5	10	_	10	рF
Power Dissipation Capacitance	C _{PD} (1)			1	28	_	_	_	ρг

Note (1) C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

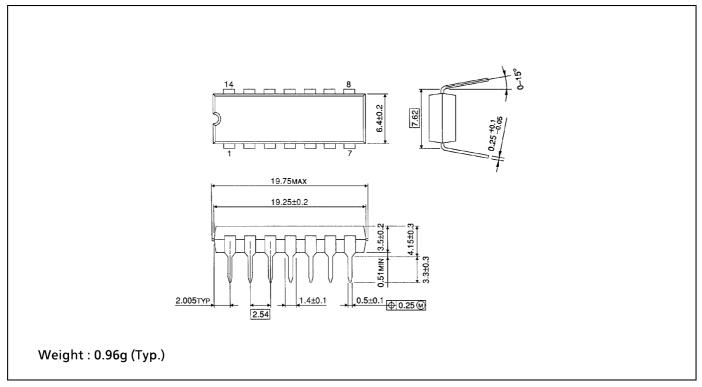
Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 6$ (per Gate)

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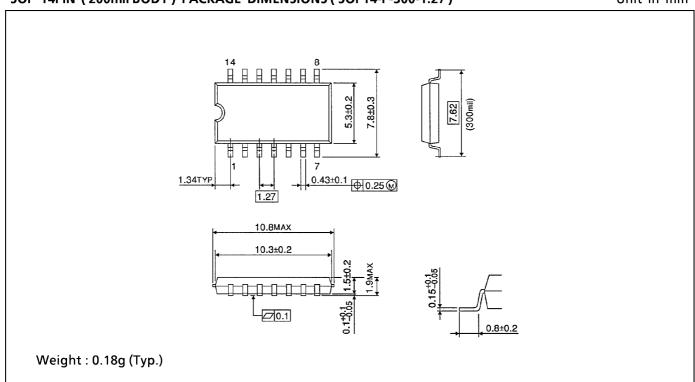
DIP 14PIN PACKAGE DIMENSIONS (DIP14-P-300-2.54)

Unit in mm



SOP 14PIN (200mil BODY) PACKAGE DIMENSIONS (SOP14-P-300-1.27)

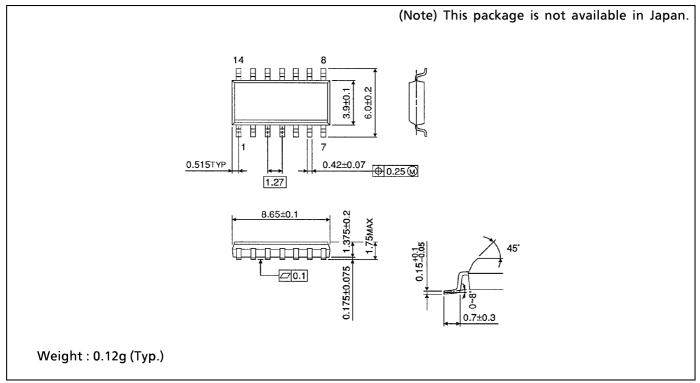
Unit in mm



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SOP 14PIN (150mil BODY) PACKAGE DIMENSIONS (SOL14-P-150 -1.27)

Unit in mm



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