

DuSLIC-SP

Single Package - Dual-Channel Subscriber Line Interface Controller
PEB 3268



The DuSLIC-SP is the newest member of Infineon's Dual-channel Subscriber Line Interface Controllers (DuSLIC) product family. The single package DuSLIC-SP implements a dual-channel telephone line interface to provide all the necessary voice interface functions from the high voltage subscriber line to the μ C/DSP digital interface. A single package solution only 256 mm² in size, DuSLIC-SP reduces the line interface unit footprint by up to 40% compared to multi-chip solutions currently on the market.

DuSLIC-SP targets POTS and the fast growing VoIP customer equipment market where competition and price erosion make time-to-market and system cost reduction critical issues. Fully programmable and featuring two integrated DC/DC converters, DuSLIC-SP incorporates all the advanced feature-set of Infineon's market leading SLIC/CODEC solutions into one highly integrated system-on-chip.

Building on Infineon's proven technological expertise, DuSLIC-SP makes the design of VoIP enabled devices much simpler, while reducing development time and efforts as well as the overall bill-of-material (BOM).

Analog Line Interface Applications

- Customer Premises Equipment (CPE)
- ISDN CPE
- XDSL CPE
- Analog Telephone Adaptors (ATA)
- VoIP Gateways and Routers
- Media Terminal Adapters (MTA)
- Integrated Access Devices (IAD)
- Cable modem

Features

- Specification according to ITU-T Q.552, G.712, LSSGR
- Fully programmable dual-channel CODEC
- Two programmable on-chip tone generators
- Programmable thresholds: on-hook, off-hook, ring trip detection
- Programmable DC feeding

- Constant current feed 0 - 50 mA
- Constant voltage zone
- Programmable resistive zone
- Internal balanced ringing with integrated DC/DC converter
- Up to 65 V_{rms} sinusoidal ringing
- Patented Ring mode allows significant power reduction
- Minimized external component count in order to enable high board density design
- Low cost BOM

Extended Features

- Power management capability
- Modem optimized mode (true 56 kbit/s)
- DTMF generation
- On-hook data transmission for always-on capability
- Message waiting indication

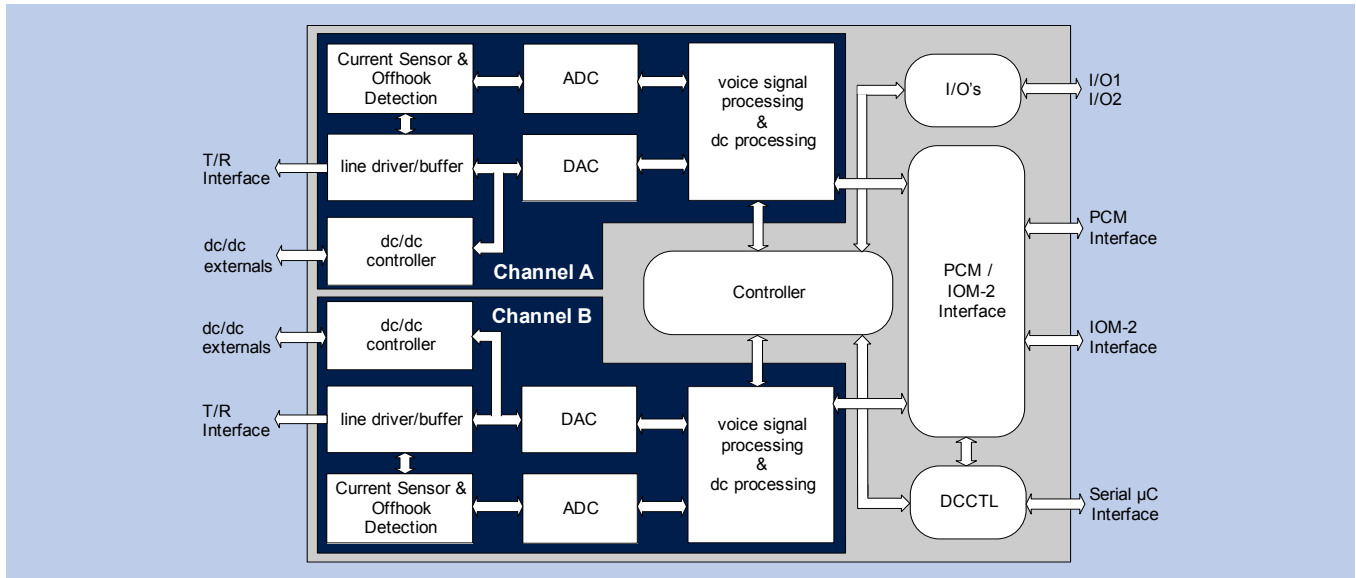
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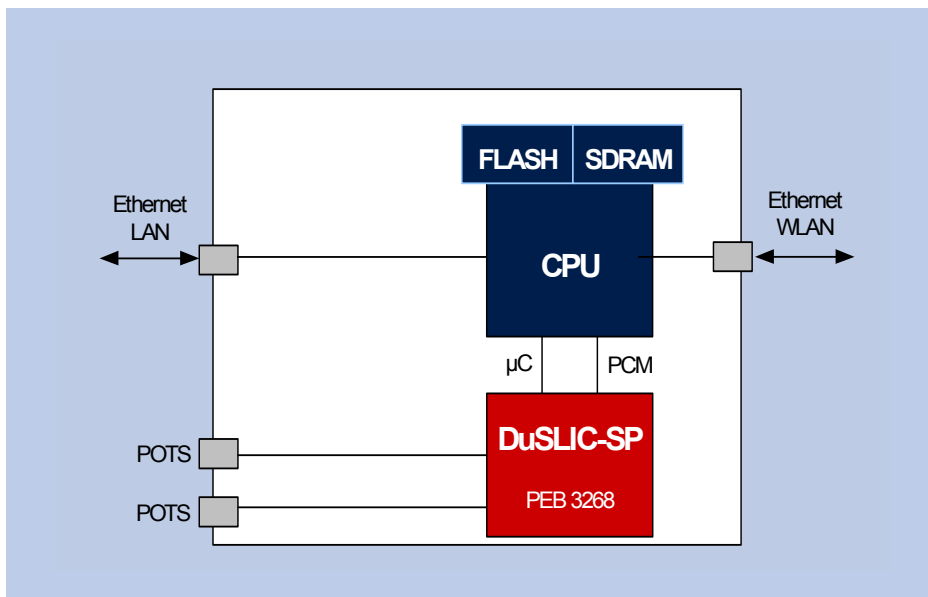
Never stop thinking

DuSLIC-SP Block Diagram



Product Summary

Sales Code	Description	Package
PEB3268F	DuSLIC-SP, Dual Channel Subscriber Line Interface Circuit in a Single Package	P/PG-TQFP-100-9
EASY 3268	DuSLIC-SP Tool Package including SMART 2000	
SMART 3268	DuSLIC-SP Evaluation Board	



Application Example: Analog Terminal Adapter

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