☐ MN101C86G

Type	MN101C86G (under development)
ROM (×8-bit)	128 K
RAM (×8-bit)	6 K
Package	QFP100-P-1818B *Lead-free*
Minimum Instruction Execution Time	0.1 µs (at 2.7 V to 3.6 V, 10 MHz) 62.5 µs (at 2.2 V to 3.6 V, 32 kHz) *

Interrupts

• RESET • Watchdog • External 0 • External 1 • External 2 • External 3 • External 4 • External 5
• External 6 (key interrupt common use) • Timer 0 • Timer 1-1 • Timer 1-2 • Timer 3-2 • Timer 4
• Timer 5 • Serial 0 • Serial 1 • Serial 2 • Serial 3 • Serial 4 • Serial 5 • DMA Transfer Completion (3Lines)
• PWM 1 • VBI 1 • VBI 2 • XDS 1 • XDS 2 • Vsync

Timer Counter

Timer counter 0 : 16-bit × 1
　　　Function ···························· Timer function, Clock function
Timer counter 1 : 16-bit × 1 or 8-bit × 2
　　　Function ···························· Timer function
Timer counter 2 : 22-bit × 1
　　　Function ···························· Watchdog function, Oscillation stabilization wait function
Timer counter 3 : 16-bit × 1 or 8-bit × 2
　　　Function ···························· Timer function, Remote control transmission function
Timer counter 4 : 16-bit × 1
　　　Function ···························· Timer function, Remote control reception function
Timer counter 5 : 16-bit × 1
　　　Function ···························· Timer function, Timer pulse output

Serial Interface

Serial interface 0 : 8-bit × 1
　　　Function ···························· Synchronous / Asynchronous switchable
Serial interface 1 : 8-bit × 1
　　　Function ···························· Synchronous / Asynchronous switchable
Serial interface 2 : 8-bit × 1
　　　Function ···························· Synchronous / Asynchronous switchable
Serial interface 3 : 8-bit × 1
　　　Function ···························· Synchronous / Asynchronous switchable, Remote control transmission interface
Serial interface 4 : 8-bit × 1
　　　Function ···························· Synchronous / Asynchronous switchable, Remote control reception interface
Serial interface 5 : 8-bit × 1
　　　Function ···························· I^{2}C (master transmission / master reception), Standard mode / Fast mode

Synchronization Signal Separation Function

Synchronization Judgement Function

VBI Data Slicer Function			Europe	TELETEXT, VPS, PDC, WSS
			North America	CC, XDS, XDS (× 2)
			Japan	EPG-J, VBID

I/O Pins	**I/O**	87	• Common use • Input/output selectable (each individual pin)

A/D Inputs	8-bit × 8-ch. (sample / hold function)

PWM	8-bit × 2 (interrupt function, no additional bit)

Special Ports	Output of remote control career, Large current capacity port (LED × 10)

Pin Assignment
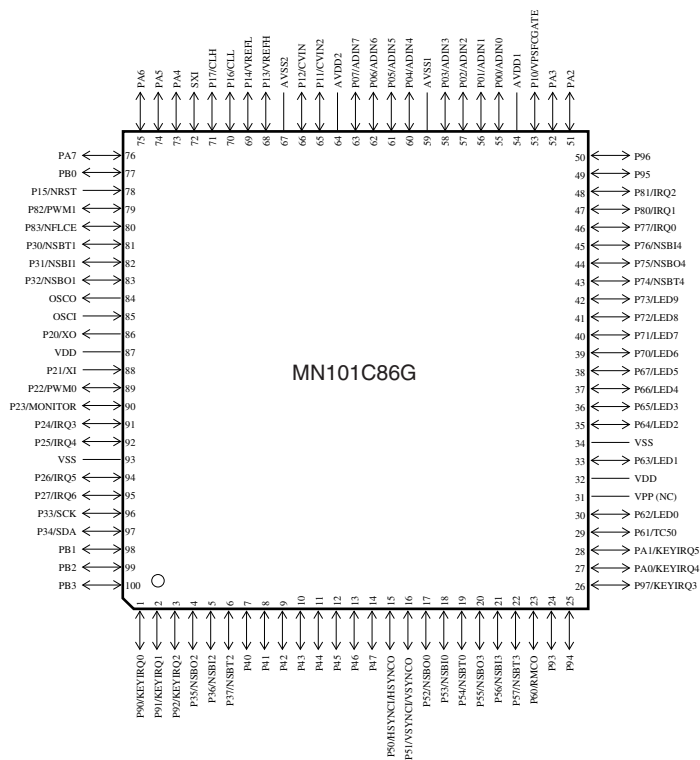
QFP100-P-1818B *Lead-free

Support Tool

In-circuit Emulator	PX-ICE101E + PX-PRB101C86-QFP100-P-1818B-M

Flash Memory Built-in Type	Type	MN101CF86G (under development)
	ROM (× 8-bit)	128 K
	RAM (× 8-bit)	6 K
	Minimum instruction execution time	0.1 µs (at 2.7 V to 3.6 V, 10 MHz)
		62.5 µs (at 2.2 V to 3.6 V, 32 kHz)
	Package	QFP100-P-1818F *Lead-free

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