

HD74LS157

Quadruple 2-line-to-1-line Data Selectors / Multiplexers (noninverted outputs)

REJ03D0442-0200 Rev.2.00 Feb.18.2005

This data selector / multiplexer contains inverters and drivers to supply full on-chip data selection to the four output gates. A separate strobe input is provided. A 4-bit word is selected from one of two sources and is routed to the four outputs. Then, outputs present true data to minimize propagation delay time.

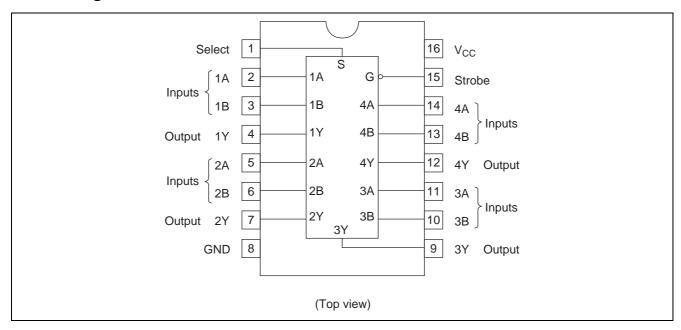
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS157P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74LS157FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74LS157RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement

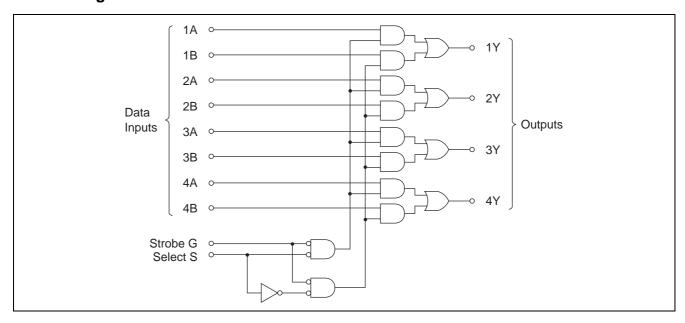


Function Table

	Output						
Strobe	trobe Select A B						
Н	Х	X	X	L			
L	L	L	X	L			
L	L	Н	X	Н			
L	Н	X	L	L			
L	Н	X	Н	Н			

H; high level, L; low level, X; irrelevant

Block Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	
Supply voltage	V _{CC}	7	V	
Input voltage	V _{IN}	7	V	
Power dissipation	P _T	400	mW	
Storage temperature	Tstg	-65 to +150	°C	

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Symbol Min		Max	Unit
Supply voltage	V_{CC}	4.75	5.00	5.25	V
Output ourrant	I _{OH}	_	_	-400	μΑ
Output current	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item		Symbol	min.	typ.*	max.	Unit	Condition		
Input voltage		V_{IH}	2.0	_	_	V			
		V_{IL}	_	_	0.8	V			
0		V _{OH}	2.7	_		V	$V_{CC} = 4.75 \; V, \; V_{IH} = 2 \; V, \; V_{IL} = 0.8 \; V, \\ I_{OH} = -400 \; \mu A$		
Output voltage		V_{OL}	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$		
		VOL	_	_	0.5	٧	$I_{OL} = 8 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$		
	S, G		_	_	40	^	V _{CC} = 5.25 V, V _I = 2.7 V		
	A, B	I _{IH}	_	_	20	μΑ	V _{CC} = 5.25 v, v ₁ = 2.7 v		
Innut ourrant	S, G		_	_	-0.8	mA	$V_{CC} = 5.25 \text{ V}, V_1 = 0.4 \text{ V}$		
Input current	A, B	I _{IL}	_	_	-0.4	IIIA			
	S, G	1	_	_	0.2	mA	V _{CC} = 5.25 V, V _I = 7 V		
	A, B	I _I	_	_	0.1	IIIA			
Short-circuit output current		los	-20	_	-100	mA	V _{CC} = 5.25 V		
Supply current**		Icc	_	9.7	16	mA	V _{CC} = 5.25 V		
Input clamp voltage		V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$		

Notes: $\overline{^* V_{CC} = 5 \text{ V, Ta} = 25^{\circ}\text{C}}$

Switching Characteristics

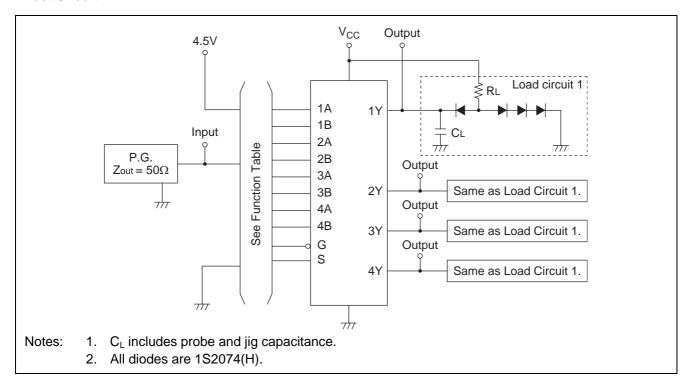
 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

Item	Symbol	Inputs	Output	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	Data	Υ	_	9	14	ns	$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$
	t _{PHL}			_	9	14	ns	
	t _{PLH}	Strobe	Υ	_	13	20	ns	
	t _{PHL}			_	14	21	ns	
	t _{PLH}	Select	Y	_	15	23	ns	
	t _{PHL}				18	27	ns	

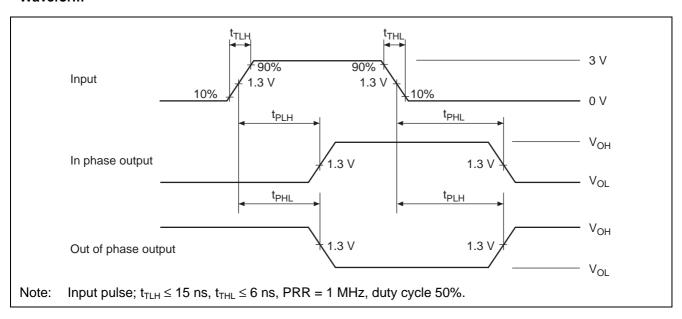
^{**} I_{CC} is measured with all outputs open and all inputs at 4.5 V.

Testing Method

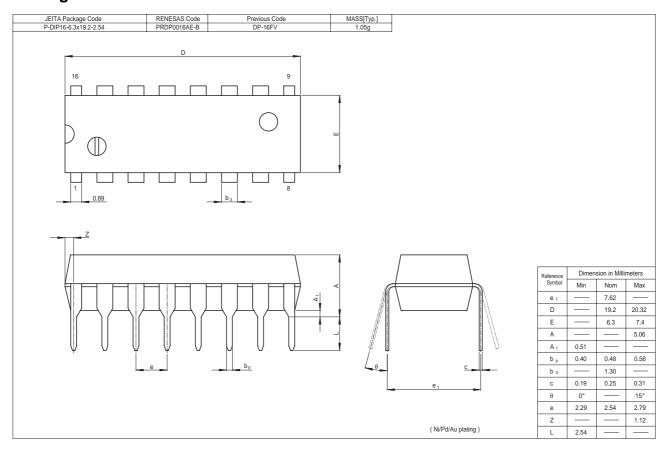
Test Circuit

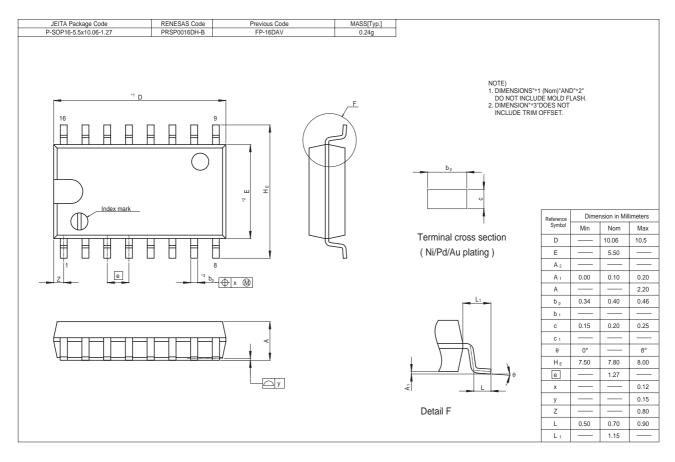


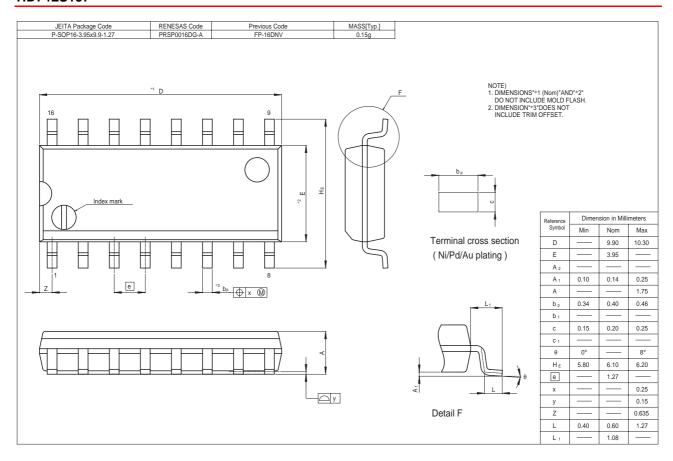
Waveform



Package Dimensions







Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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