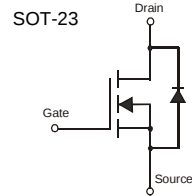


Features

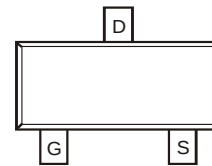
- Low On-Resistance:
35mΩ @ $V_{GS} = 10V$
50mΩ @ $V_{GS} = 4.5V$
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)**
- "Green" Device (Note 3)**
- Qualified to AEC-Q 101 Standards for High Reliability**



TOP VIEW



Equivalent Circuit



TOP VIEW

Maximum Ratings @ $T_A = 25^\circ C$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current (Note 1)	I_D	5.2 4.2	A
		$T_A = 25^\circ C$ $T_A = 70^\circ C$	
Drain Current (Note 1)	I_{DM}	20	A
		Pulsed	
Body-Diode Continuous Current (Note 1)	I_S	2.0	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P_D	1.4	W
Thermal Resistance, Junction to Ambient @ $T_A = 25^\circ C$ (Note 1)	$R_{\theta JA}$	90	$^\circ C/W$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ C$

Electrical Characteristics @ $T_A = 25^\circ C$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV_{DSS}	30	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Body Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	$V_{GS(th)}$	1	1.5	3	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	27 40	35 50	mΩ	$V_{GS} = 10V, I_D = 5.2A$ $V_{GS} = 4.5V, I_D = 4.2A$
Forward Transconductance	g_{fs}	—	6.5	—	S	$V_{DS} = 5V, I_D = 5.2A$
Source-Drain Diode Forward Voltage	V_{SD}	—	0.7	1	V	$V_{GS} = 0V, I_S = 1.0A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	390	—	pF	$V_{DS} = 15V, V_{GS} = 0V$ $f = 1.0MHz$
Output Capacitance	C_{oss}	—	55	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	45	—	pF	

- Notes:
- Device mounted on FR-4 PCB. $t \leq 5$ sec.
 - No purposefully added lead.
 - Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 - Short duration pulse test used to minimize self-heating effect.

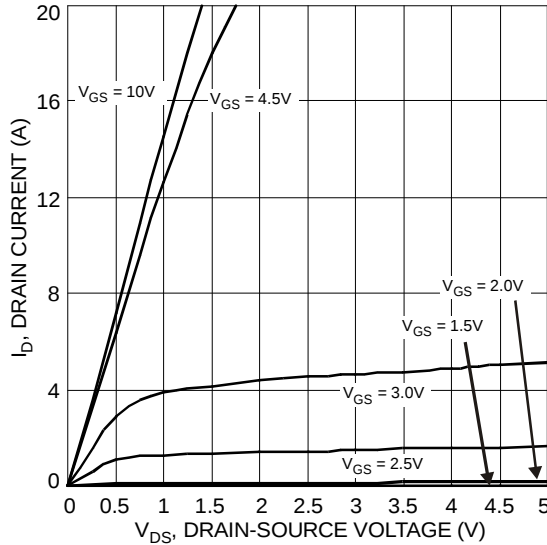


Fig. 1 Typical Output Characteristics

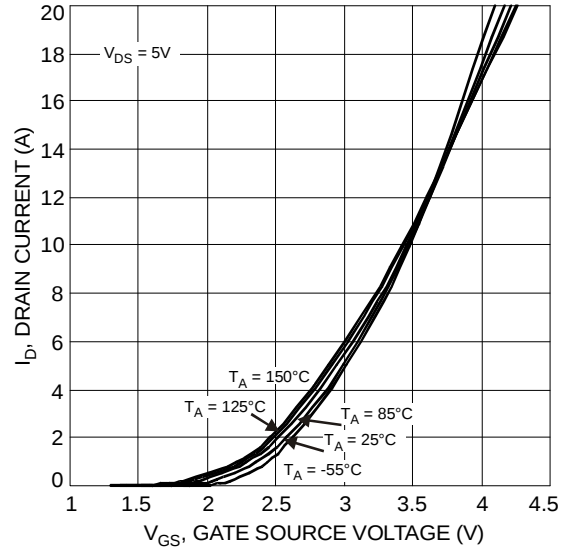


Fig. 2 Typical Transfer Characteristics

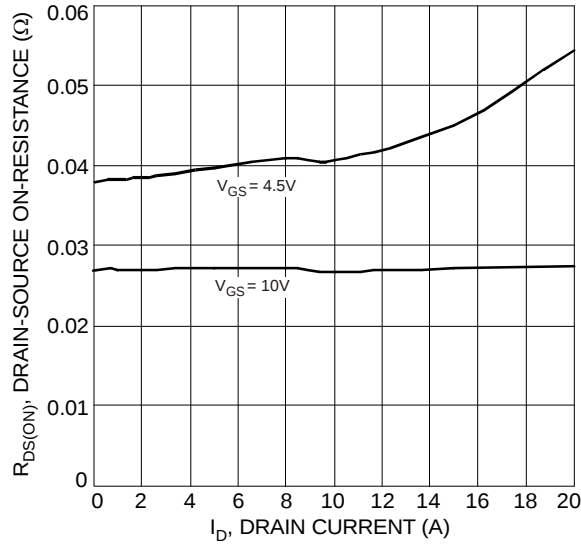


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

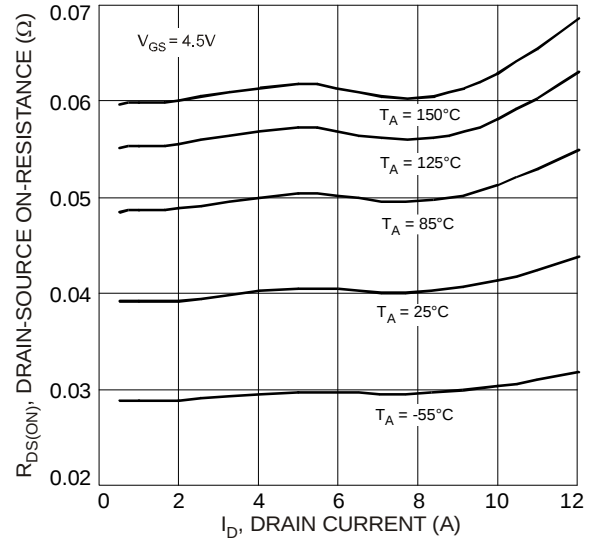


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

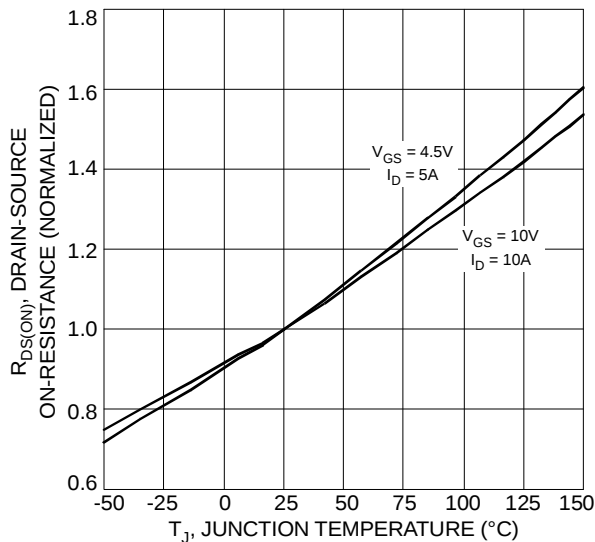


Fig. 5 On-Resistance Variation with Temperature

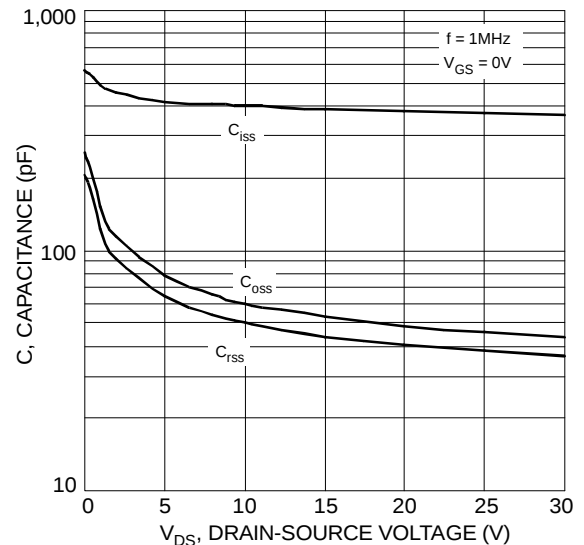
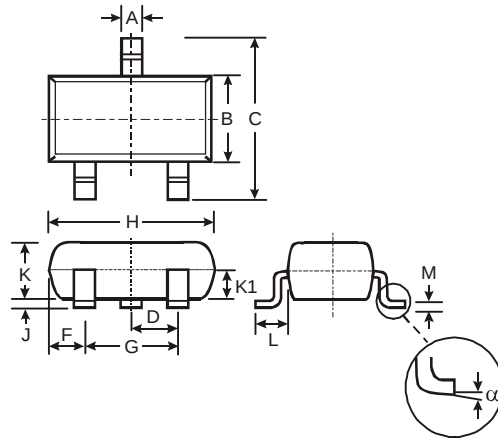


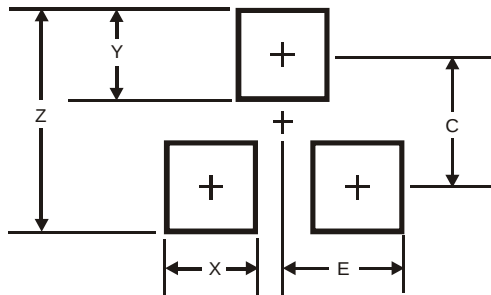
Fig. 6 Typical Capacitance

Package Outline Dimensions



SOT-23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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