

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

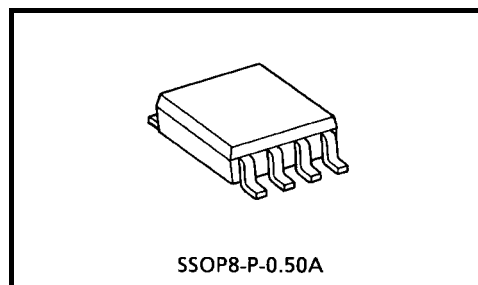
TAR8D03K

Dual Low-Dropout Regulator

TAR8D03K is a bipolar type 2-output power supply with a control pin. ON and OFF can be switched using the control pin.

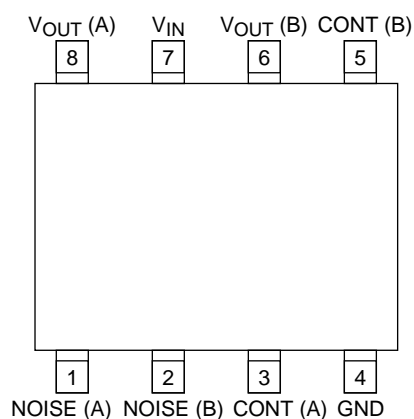
Features

- Include 2-LDO regulators. (2.8V, 3.0V)
- Output power supply can be independent control.
- Overtemperature/overcurrent protection.
- Very small 8-pin package.



Weight: 0.01 g (typ.)

Pin Assignment (top view)



$V_{OUT} (A) = 2.8 \text{ V}$

$V_{OUT} (B) = 3.0 \text{ V}$

Marking

8D03

Overtemperature protection and overcurrent protection function are not necessary of operating ratings below the maximum ratings.

Do not use device under conditions in which their maximum ratings will be exceeded.

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply Voltage	V _{IN}	15	V
Output Current (V _{OUT} (A))	I _{OUT} (A)	100	mA
Output Current (V _{OUT} (B))	I _{OUT} (B)		
Power Dissipation	P _D	200 (Note 1)	mW
		400 (Note 2)	
Operation Temp Range	T _{opr}	-40~85	°C
Storage Temp Range	T _{stg}	-55~150	°C

Note 1: Unit Rating

Note 2: Mounted on a glass epoxy circuit board of 30 mm × 30 mm Pad dimension of 70 mm²

Electrical Characteristics (unless otherwise specified, V_{IN} = V_{OUT} + 1 V, I_{OUT} = 30 mA, C_{IN} = 1 μF, C_{OUT} = 10 μF, C_{NOISE} = 0.01 μF, T_j = 25°C)

2.8 V output Characteristics (V_{OUT} (A))

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Output Voltage	V _{OUT}	I _{OUT} = 30 mA	2.73	2.8	2.87	V
Dropout Voltage	V _{IN} -V _{OUT}	I _{OUT} = 30 mA		120	180	
Input Voltatge	V _{IN}		V _{OUT} + 0.18 V	—	15	V
Load Regulation	Reg. load	I _{OUT} = 0 mA~30 mA	—	2	12	mV
Line Regulation	Reg. line	V _{IN} = 3.8 V~15.0 V	—	3	15	mV
Temp. Coefficient	T _{CV0}	Ta = -40°C~85°C	—	100	—	ppm/°C
Ripple Rejection	R.R	I _{OUT} = 10 mA, f = 1 kHz, V _{Ripple} = 500 mV _{p-p} , Ta = 25°C	—	70	—	dB
Output Noise Voltage	V _{NO}	I _{OUT} = 10 mA, 10 Hz ≤ f ≤ 10 kHz, Ta = 25°C	—	30	—	μV _{rms}

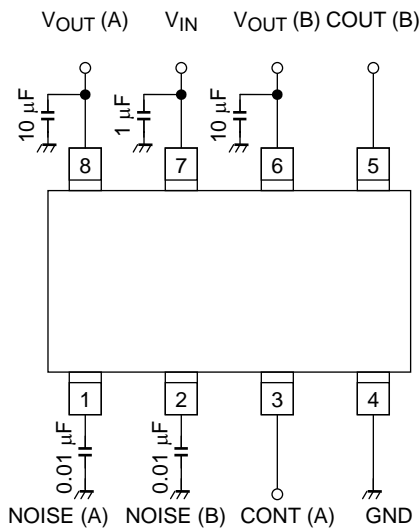
3.0 V output Characteristics (V_{OUT} (B))

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Output Voltage	V _{OUT}	I _{OUT} = 30 mA	2.92	3.0	3.08	V
Dropout Voltage	V _{IN} -V _{OUT}	I _{OUT} = 30 mA		120	180	
Input Voltatge	V _{IN}		V _{OUT} + 0.18 V	—	15	V
Load Regulation	Reg. load	I _{OUT} = 0 mA~30 mA	—	2	12	mV
Line Regulation	Reg. line	V _{IN} = 3.8 V~15.0 V	—	3	15	mV
Temp. Coefficient	T _{CV0}	Ta = -40°C~85°C	—	100	—	ppm/°C
Ripple Rejection	R.R	I _{OUT} = 10 mA, f = 1 kHz, V _{Ripple} = 500 mV _{p-p} , Ta = 25°C	—	70	—	dB
Output Noise Voltage	V _{NO}	I _{OUT} = 10 mA, 10 Hz ≤ f ≤ 10 kHz, Ta = 25°C	—	30	—	μV _{rms}

Common Characteristics

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Stand-by Current	I_B (OFF)	$V_{CT} = 0\text{ V}$	—	—	1	μA
Quiescent Current	I_{B1}	$I_{OUT} (A) = 0\text{ mA}$, $I_{OUT} (B) = 0\text{ mA}$	—	350	700	μA
	I_{B2}	$I_{OUT} (A) = 30\text{ mA}$, $I_{OUT} (B) = 30\text{ mA}$	—	850	1800	μA
Control Voltage (ON)	V_{CT} (ON)	—	1.5	—	V_{IN}	V
Control Voltage (OFF)	V_{CT} (OFF)	—	—	—	0.4	V
Control Input Current	I_{CT}	$V_{CT} = 1.5\text{ V}$ (output ON)	—	—	15	μA

Application Note • Recommended Application circuit



Control level	A-channel	B-channel
HIGH	ON	ON
LOW	OFF	OFF

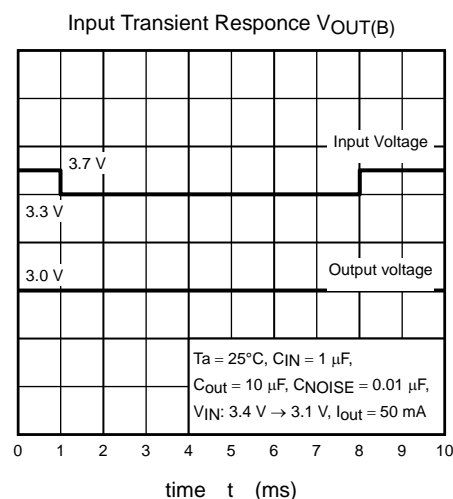
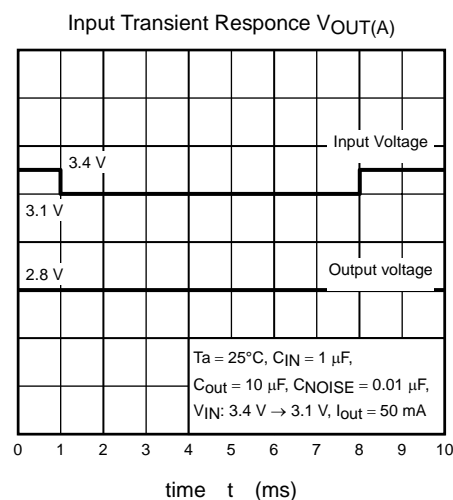
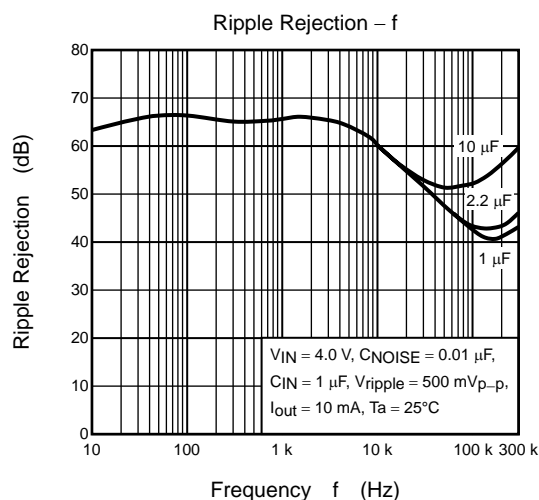
The noise capacitor should be connected to NOISE pin to GND for stable operation.
The recommended value is higher than 0.0047µF.

The figure above shows commended configuration for using a point regulator. Insert a capacitor for stable input/output operation.
If the control function is not to be used, Toshiba recommended that the control pin be connected to the V_{CC} pin.

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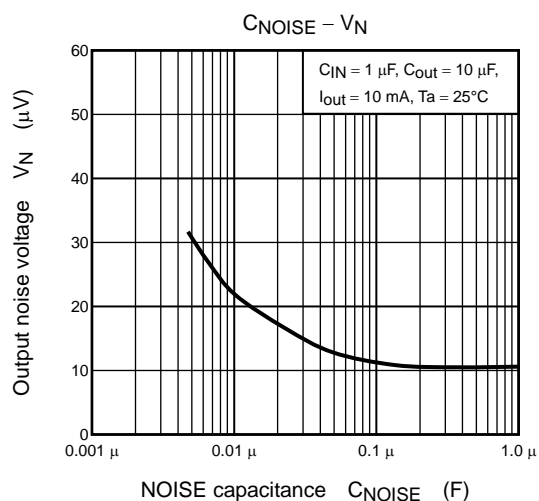
Ripple Rejection

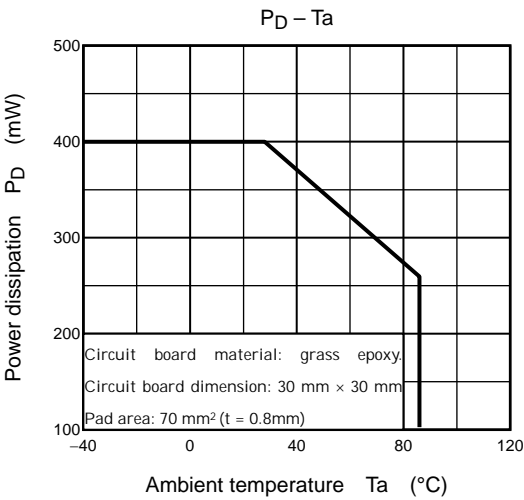
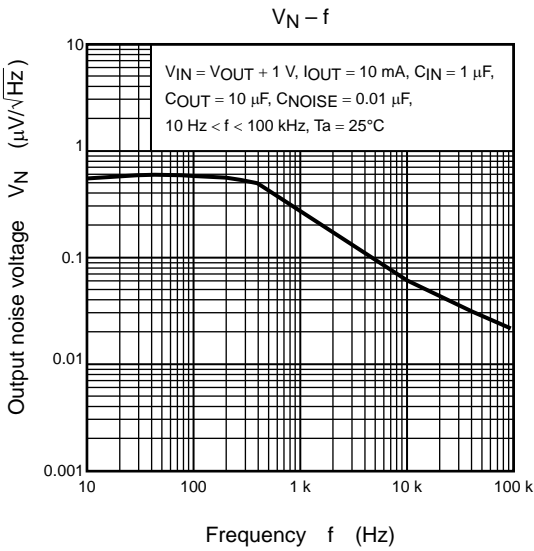
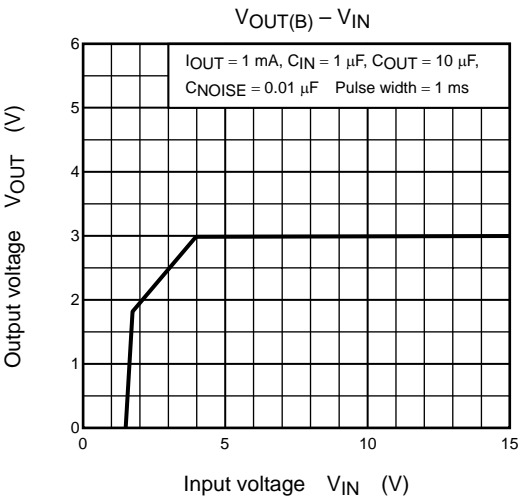
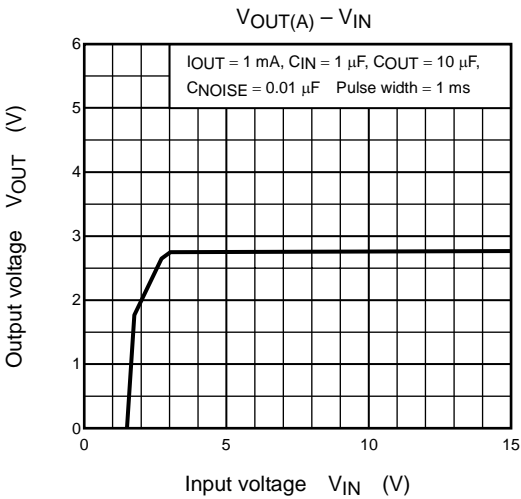
TAR8D03K feature a circuit with an excellent ripple rejection characteristic. Because the circuit also features an excellent output fluctuation characteristic for sudden supply voltage drops, the circuit is ideal for in the RF blocks incorporated in all mobile telephones.



NOISE Pin

TAR8D03K device incorporate a NOISE pin to reduce output noise voltage. Inserting a capacitor between the NOISE pin and GND reduces output noise. To ensure stable operation, insert a capacitor of $0.0047 \text{ } \mu\text{F}$ or more between the NOISE pin and GND.

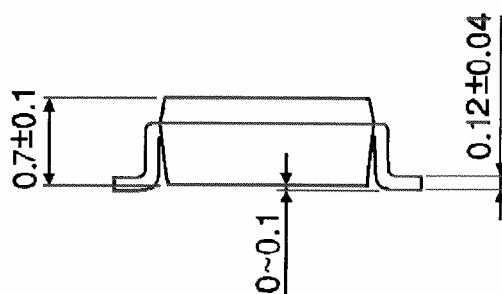
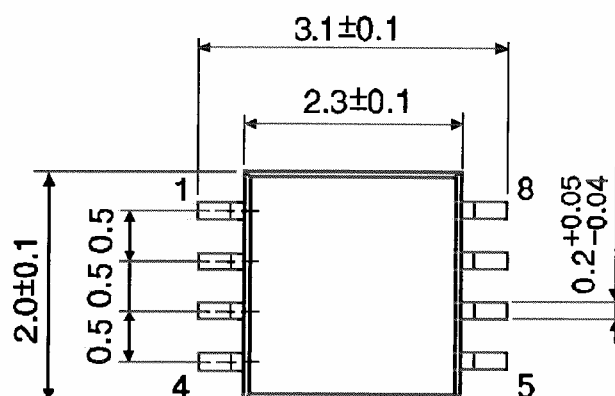




Package Dimensions

SSOP8-P-0.50A

Unit : mm



Weight: 0.01 g (Typ.)

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