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ADC1610S125F2 Obsolete

ADC1610S125F2 demo board, LVDS/DDR digital output

IDT's ADC1610S125F2 demo board is suitable for dynamic performance evaluations from low to high IF configuration with LVDS DDR output. A data-acquisition board can be used during design and prototype to analyze ADC performance.

Features

- Single power supply (on-board regulators)
- SMA connector for clock and analog input signals
- LVDS/DDR outputs

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ADC1610S125F2-DB	Obsolete	BD0	BOARD	0	I	No	Availability

Technical Documentation

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Demo Board Software ADC_1_r04	ZIP	2.33 MB	Jul 18, 2012

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ADC1610S125F2 - Evaluation Board

