

SINGLE CHIP PREAMPLIFIER FOR DOUBLE DECK RADIO CASSETTE RECORDER

PRODUCT PREVIEW

- HIGH OPEN-LOOP GAIN (80dB)
- VERY LOW INPUT NOISE
- LOW TURN ON/OFF POP
- LOW SUPPLY CURRENT
- PLAYBACK AMPLIFIER WITH HEAD INPUT SWITCH
- RECORDING AMPLIFIER WITH BUILT-IN ALC
- ALC TIME CONSTANT PIN CAN BE USED AS ALC SWITCH
- GOOD SUPPLY RIPPLE REJECTION

DESCRIPTION

The TDA7286 is a monolithic integrated circuit in DIP20 and SO20 packages designed for 6V, 9V and 12V AC/DC double deck cassette applications.



DIP20



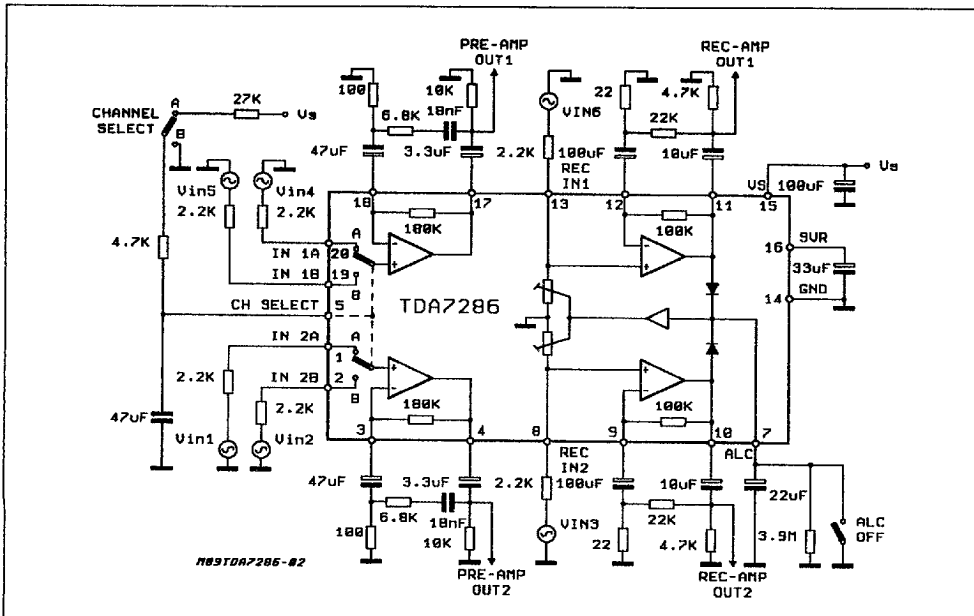
SO20

ORDERING NUMBERS:

TDA7286

TDA7286D

BLOCK DIAGRAM



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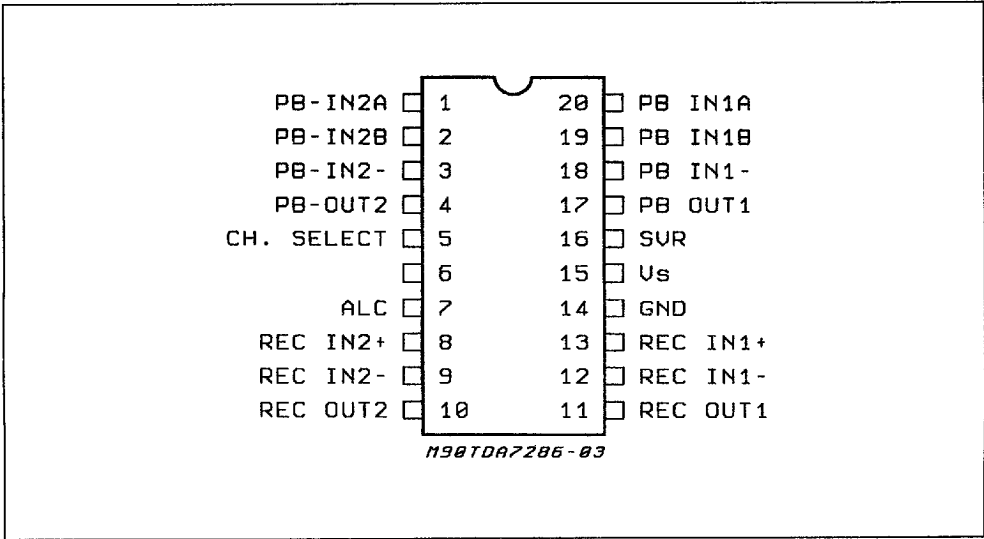
This is advanced information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

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ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_S	Supply Voltage	14	V
V_{IN}	Input DC Voltage	-0.4 to 0.5	V
T_{op}	Operating Temperature	-20 to 70	°C
T_{stg}, T_J	Storage & Junction Temperature	-40 to 150	°C

PIN CONNECTION (Top view)



THERMAL DATA

Symbol	Parameter	DIP20	SO20	Unit
R_{thJAmb}	Thermal Resistance Junction to Ambient Max.	100	200	°C/W

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ELECTRICAL CHARACTERISTICS ($V_S = 6V$, $T_{amb} = 25^{\circ}C$; $f = 1KHz$; $R_L = 4.7k\Omega$; $R_g = 2.2k\Omega$, unless otherwise specified.)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_S	Supply Voltage		4		12	V
I_d	Quiescent Current			7	15	mA
PLAYBACK AMPLIFIER						
G_{VO}	Open Loop Gain			80		dB
V_{CD}	Output DC Voltage		1.2	1.5	1.7	V
V_O	Output AC Voltage	THD = 1%	700	950		mVrms
SVR	Supply Voltage Rejection	$f = 100Hz$	35	45		dB
C_T	Cross Talk			60		dB
E_N	Total Output Noise	$R_n = 0$, BW = 22 to 22kHz		300		μV
R_E	Internal Feedback Res			180		k Ω
RECORDING AMPLIFIER						
G_{VO}	Open Loop Gain			80		dB
R_i	Input resistance		30	50	70	k Ω
V_{DC}	Output DC Voltage		2.0	2.4	2.8	V
V_O	Output AC Voltage	THD = 1% ALC = OFF ALC = ON	1 0.75	1.4 1	1.25	Vrms Vrms
THD	Total Harmonic Distortion	$V_O = 0.5V_{rms}$; ALC = OFF		0.2	0.5	%
	ALC Range	$\Delta V_O = 3dB$		48		dB
CB	Channel Balance	ALC = ON		0	2	dB
SVR	Supply Voltage Rejection	$f = 100Hz$	28	32		dB
C_T	Cross Talk	$f = 1KHz$		60		dB
HEAD SELECT INPUT						
	Input LOW Level	chB selected			0.6	V
	Input HIGH level	chA selected	1.4			V

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