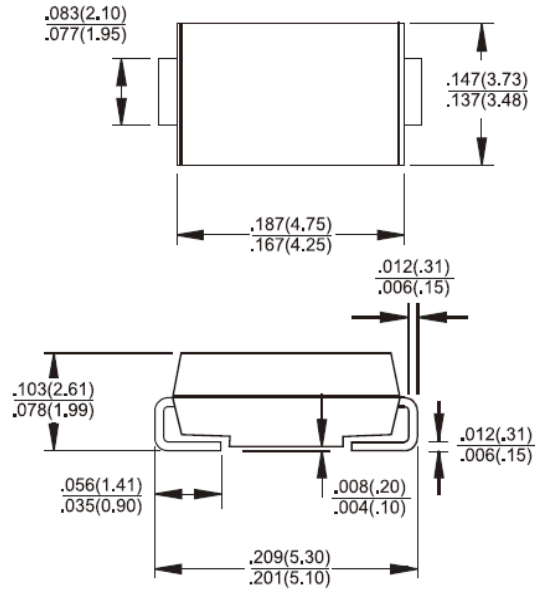



RoHS
COMPLIANCE


Features

- ✧ For surface mounted application
- ✧ Glass passivated junction chip
- ✧ Low forward voltage drop
- ✧ High current capability
- ✧ Easy pick and place
- ✧ High surge current capability
- ✧ Plastic material used carries Underwriters Laboratory Classification 94V-0
- ✧ High temperature soldering: 260°C/10 seconds at terminals
- ✧ Green compound with suffix "G" on packing code & prefix "G" on datecode



Mechanical Data

- ✧ Case: Molded plastic
- ✧ Terminal: Pure tin plated, lead free
- ✧ Polarity: Indicated by cathode band
- ✧ Packing: 12mm tape per EIA STD RS-481
- ✧ Weight: 0.093 grams

Dimensions in inches and (millimeters)



Marking Diagram

- S1XB = Specific Device Code
G = Green Compound
Y = Year
M = Work Month

Maximum Ratings and Electrical Characteristics

Rating at 25 °C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	Symbol	S1 AB	S1 BB	S1 DB	S1 GB	S1 JB	S1 KB	S1 MB	Unit
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current @TL=110°C	$I_{F(AV)}$	1							A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	30							A
Maximum Instantaneous Forward Voltage (Note 1) @ 1 A	V_F	1.1							V
Maximum Reverse Current @ Rated VR $T_A=25\text{ }^{\circ}\text{C}$ $T_A=125\text{ }^{\circ}\text{C}$	I_R	5 50							uA
Typical Junction Capacitance (Note 2)	C_j	12							pF
Typical Thermal Resistance	$R_{\theta JL}$	30							°C/W
Operating Temperature Range	T_J	- 55 to + 150							°C
Storage Temperature Range	T_{STG}	- 55 to + 150							°C

Note 1: Pulse Test with PW=300 usec, 1% Duty Cycle

Note 2: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

RATINGS AND CHARACTERISTIC CURVES (S1AB THRU S1MB)

FIG.1 FORWARD CURRENT DERATING CURVE

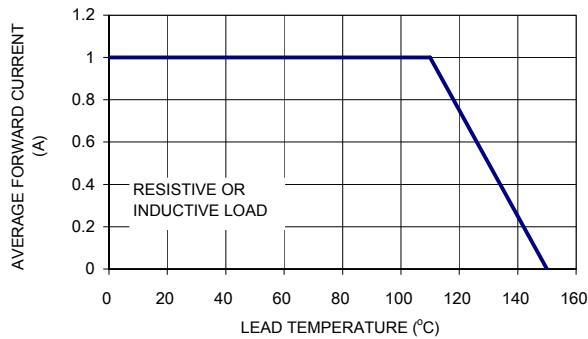


FIG. 2 TYPICAL REVERSE CHARACTERISTICS

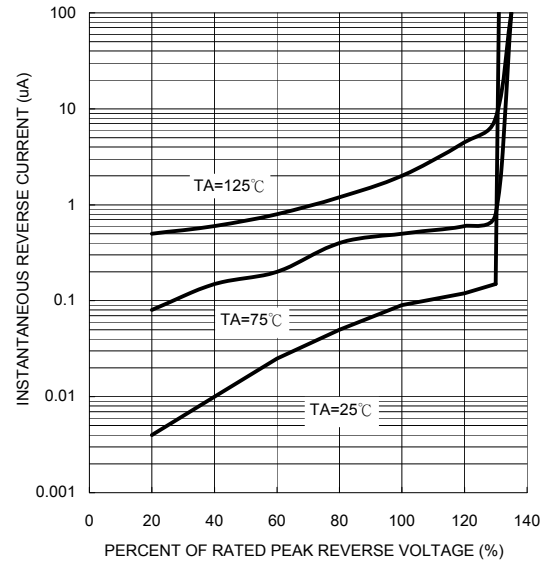


FIG. 3 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

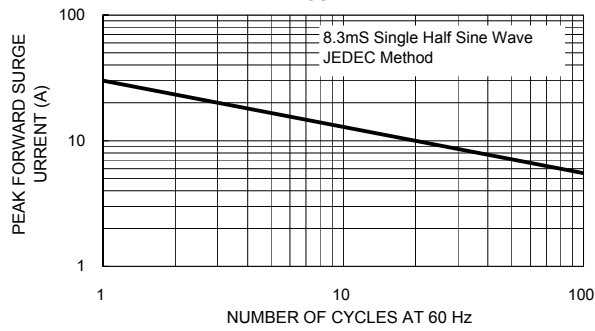


FIG. 4 TYPICAL JUNCTION CAPACITANCE

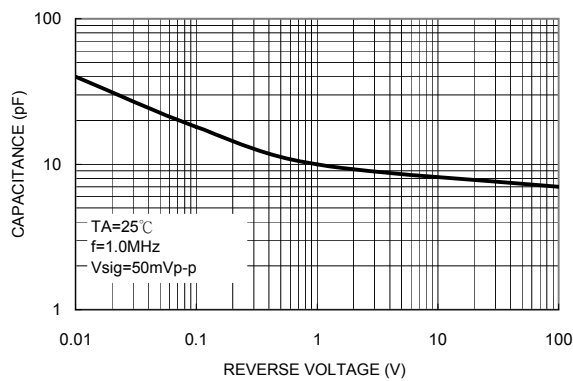


FIG. 5 TYPICAL FORWARD CHARACTERISTICS

