

T513 Series Commercial Off-The-Shelf (COTS) Multiple Anode Low ESR MnO₂

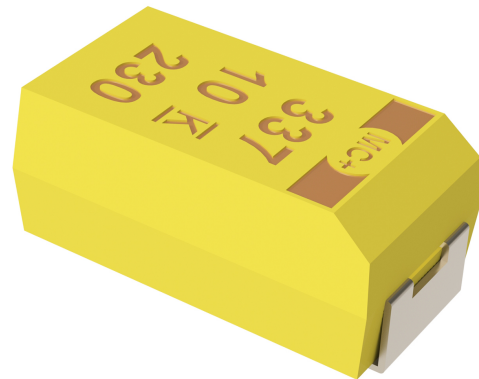
Overview

The KEMET T513 Series is suitable for the Commercial Off-The-Shelf (COTS) requirements of industrial, telecom, defense and aerospace markets. This surface mount series offers very low ESR and surge robustness designed for applications that require high surge current and high ripple current capability. These

benefits are achieved via a multiple anode construction similar to KEMET's T510 Series. The T513 Series COTS also offers various options including Weibull Grading, termination finish and surge current.

Benefits

- Meets or exceeds EIA Standard 535BAAC
- Taped and reeled per EIA 481
- High surge current capability
- Termination options B, C, H, K, T
- High ripple current capability
- Surge testing options
- 100% steady-state accelerated aging
- Capacitance values of 15 μ F to 1,000 μ F
- Tolerances of $\pm 10\%$ and $\pm 20\%$
- Voltage rating of 4 – 35 VDC
- Case sizes D, E, and X
- ESR as low as 10 m Ω
- RoHS compliant 100% Sn terminations available
- Operating temperature range of -55°C to +125°



Applications

The T513 Series is suitable for the industrial, telecom, defense and aerospace markets. Typical applications include decoupling and filtering in radar, sonar, power supply, guidance systems and other high reliability applications.

Environmental Compliance

RoHS Compliant (6/6) according to Directive 2002/95/EC when ordered with 100% Sn solder or gold plated.

SPICE

For a detailed analysis of specific part numbers, please visit www.kemet.com for a free download of KEMET's SPICE software. The KEMET SPICE program is freeware intended to aid design engineers in analyzing the performance of these capacitors over frequency, temperature, ripple, and DC bias conditions.

Ordering Information

T	513	X	108	K	004	B	H	61	10
Capacitor Class	Series	Case Size	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage (VDC)	Failure Rate/Design	Termination Finish	Surge	ESR
T = Tantalum	Multiple Anode COTS	D, E, X	First two digits represent significant figures. Third digit specifies number of zeros.	K = $\pm 10\%$ M = $\pm 20\%$	004 = 4 006 = 6.3 010 = 10 016 = 16 020 = 20 025 = 25 035 = 35	A = N/A B = 0.1%/1,000 hours	C = Hot Solder Dipped H = Standard Solder Coated (SnPb 5% Pb minimum) B = Gold Plated K = Solder Fused T = 100% Tin	61 = None 62 = 10 cycles, 25°C after Weibull 63 = 10 cycles, -55°C & 85°C after Weibull 64 = 10 cycles, -55°C & 85°C before Weibull	10 = Standard ESR 20 = Low ESR 30 = Ultra Low ESR

Performance Characteristics

Item	Performance Characteristics
Operating Temperature	-55°C to 125°C
Rated Capacitance Range	15 – 1,000 μ F @ 120 Hz/25°C
Capacitance Tolerance	K Tolerance (10%), M Tolerance (20%)
Rated Voltage Range	4 – 35 VDC
DF (120 Hz)	Refer to Part Number Electrical Specification Table
ESR (100 kHz)	Refer to Part Number Electrical Specification Table
Leakage Current	≤ 0.01 CV (μ A) at rated voltage after 5 minutes

Qualification

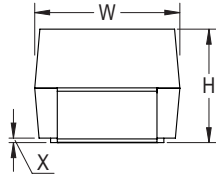
Test	Condition		Characteristics			
Endurance	85°C @ rated voltage, 2,000 hours 125°C @ 2/3 rated voltage, 2,000 hours		Δ C/C	Within ±10% of initial value		
			DF	Within initial limits		
			DCL	Within 1.25 x initial limit		
			ESR	Within initial limits		
Storage Life	125°C @ 0 volts, 2,000 hours		Δ C/C	Within ±10% of initial value		
			DF	Within initial limits		
			DCL	Within 1.25 x initial limit		
			ESR	Within initial limits		
Thermal Shock	MIL–STD–202, Method 107, Condition B, mounted, -55°C to 125°C, 1,000 cycles		Δ C/C	Within ±5% of initial value		
			DF	Within initial limits		
			DCL	Within 1.25 x initial limit		
			ESR	Within initial limits		
Temperature Stability	Extreme temperature exposure at a succession of continuous steps at +25°C, -55°C, +25°C, +85°C, +125°C, +25°C	Δ C/C	+25°C	-55°C	+85°C	+125°C
		IL*	±10%	±10%	±20%	
		DF	IL	IL	1.5 x IL	1.5 x IL
		DCL	IL	n/a	10 x IL	12 x IL
Surge Voltage	25°C and 85°C, 1.32 x rated voltage 1,000 cycles (125°C, 1.2 x rated voltage)		Δ C/C	Within ±5% of initial value		
			DF	Within initial limits		
			DCL	Within initial limits		
			ESR	Within initial limits		
Mechanical Shock/Vibration	MIL–STD–202, Method 213, Condition I, 100 G peak MIL–STD–202, Method 204, Condition D, 10 Hz to 2,000 Hz, 20 G peak		Δ C/C	Within ±10 of initial value		
			DF	Within initial limits		
			DCL	Within initial limits		
Additional Qualification Tests per MIL–PRF–55365/8	Please contact KEMET for more information.					

*IL = Initial limit

Dimensions – Millimeters (Inches)

Metric will govern

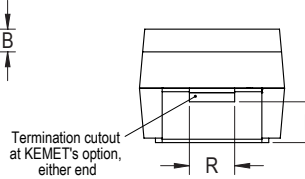
CATHODE (-) END VIEW



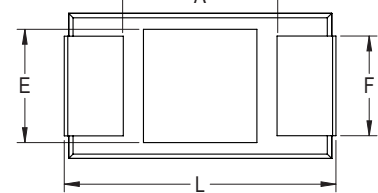
SIDE VIEW



ANODE (+) END VIEW



BOTTOM VIEW



Case Size		Component												
KEMET	EIA	L	W	H	F ±0.1 ±(.004)	S ±0.3 ±(.012)	B ±0.15 (Ref) ±.006	X (Ref)	P (Ref)	R (Ref)	T (Ref)	A (Min)	G (Ref)	E (Ref)
D	7343-31	7.3 ±0.3 (0.287 ±.012)	4.3 ±0.3 (0.169 ±.012)	2.8 ±0.3 (0.110 ±.012)	2.4 (.095)	1.3 (.051)	0.5 (.020)	0.10 ±0.10 (.004 ±.004)	0.9 (.035)	1.0 (.039)	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)
X	7343-43	7.3 ±0.3 (0.287 ±.012)	4.3 ±0.3 (0.169 ±.012)	4.0 ±0.3 (0.157 ±.012)	2.4 (.094)	1.3 (.051)	0.5 (.020)	0.10 ±0.10 (.004 ±.004)	1.7 (.067)	1.0 (.039)	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)
E	7360-38	7.3 ±0.3 (0.287 ±.012)	6.0 ±0.3 (0.236 ±.012)	3.6 ±0.2 (0.142 ±.008)	4.1 (.161)	1.3 (.051)	0.5 (.020)	0.10 ±0.10 (.004 ±.004)	0.9 (.035)	1.0 (.039)	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)

Notes: (Ref) – Dimensions provided for reference only.

Table 1 – Ratings & Part Number Reference

Rated Voltage	Rated Cap	Case Code/ Case Size	KEMET Part Number	DC Leakage	DF	Standard ESR	Low ESR	Ultra-low ESR	Maximum Operating Temp
VDC @ 85°C	µF	KEMET/EIA	(See below for part options)	µA @ +20°C Max/5 Min	% @ +20°C 120 Hz Max	mΩ @ +20°C 100 kHz Max	mΩ @ +20°C 100 kHz Max	mΩ @ +20°C 100 kHz Max	°C
4	680	X/7343-43	T513X687(1)004(2)(3)(4)(5)	27.2	6.0	30	N/A	N/A	125
4	1000	X/7343-43	T513X108(1)004(2)(3)(4)(5)	40.0	6.0	23	18	N/A	125
4	1000	E/7360-38	T513E108(1)004(2)(3)(4)(5)	40.0	6.0	18	10	N/A	125
6.3	470	X/7343-43	T513X477(1)006(2)(3)(4)(5)	29.6	6.0	30	N/A	N/A	125
6.3	680	X/7343-43	T513X687(1)006(2)(3)(4)(5)	42.8	6.0	45	23	N/A	125
6.3	680	E/7360-38	T513E687(1)006(2)(3)(4)(5)	42.8	6.0	23	12	N/A	125
10	330	X/7343-43	T513X337(1)010(2)(3)(4)(5)	33.0	6.0	35	N/A	N/A	125
16	100	D/7343-31	T513D107(1)016(2)(3)(4)(5)	16.0	6.0	75	N/A	N/A	125
16	150	X/7343-43	T513X157(1)016(2)(3)(4)(5)	24.0	6.0	40	30	N/A	125
16	220	X/7343-43	T513X227(1)016(2)(3)(4)(5)	35.2	10.0	40	25	N/A	125
20	100	X/7343-43	T513X107(1)020(2)(3)(4)(5)	20.0	8.0	45	40	35	125
25	68	X/7343-43	T513X686(1)025(2)(3)(4)(5)	17.0	8.0	45	N/A	N/A	125
25	100	E/7360-38	T513E107(1)025(2)(3)(4)(5)	25.0	8.0	50	N/A	N/A	125
35	15	D/7343-31	T513D156(1)035(2)(3)(4)(5)	5.3	6.0	100	N/A	N/A	125
35	33	X/7343-43	T513X336(1)035(2)(3)(4)(5)	11.6	6.0	65	55	N/A	125
35	47	X/7343-43	T513X476(1)035(2)(3)(4)(5)	16.5	8.0	65	55	N/A	125
VDC @ 85°C	µF	KEMET/EIA	(See below for part options)	µA @ +20°C Max/5 Min	% @ +20°C 120 Hz Max	mΩ @ +20°C 100 kHz Max	mΩ @ +20°C 100 kHz Max	mΩ @ +20°C 100 kHz Max	°C
Rated Voltage	Rated Cap	Case Code/ Case Size	KEMET Part Number	DC Leakage	DF	Standard ESR	Low ESR	Ultra-low ESR	Maximum Operating Temp

(1) To complete KEMET part number, insert M for ±20%, K for ±10%. Designates Capacitance tolerance.

(2) To complete KEMET part number, insert B (0.1%/1,000 hours), or A = N/A. Designates Reliability Level.

(3) To complete KEMET part number, insert B = Gold Plated, C = Hot solder dipped, H = Solder Plated, K = Solder Fused or T = 100% Tin (Sn). Designates Termination Finish.

(4) To complete KEMET part number, insert 61 = None, 62 = 10 cycles +25°C, 63 = 10 cycles -55°C +85°C after Weibull or 64 = 10 cycles -55°C +85°C before Weibull.

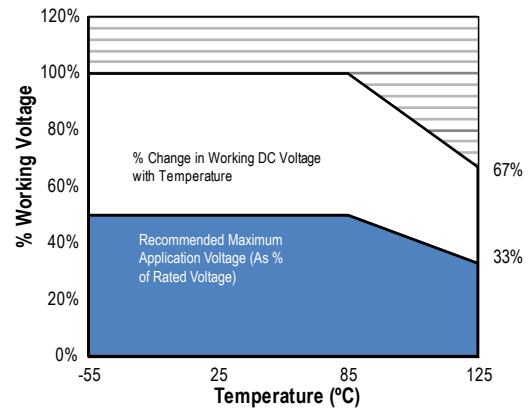
Designates Surge current option.

(5) To complete KEMET part number, insert 10 = Standard ESR, 20 = Low ESR or 30 = Ultra Low ESR. Designates ESR option.

Refer to Ordering Information for additional detail.

Recommended Voltage Derating Guidelines

	-55°C to 85°C	85°C to 125°C
% Change in Working DC Voltage with Temperature	V _R	67% of V _R
Recommended Maximum Application Voltage	50% of V _R	33% of V _R



Ripple Current/Ripple Voltage

Permissible AC ripple voltage and current are related to equivalent series resistance (ESR) and the power dissipation capabilities of the device. Permissible AC ripple voltage which may be applied is limited by two criteria:

1. The positive peak AC voltage plus the DC bias voltage, if any, must not exceed the DC voltage rating of the capacitor.
2. The negative peak AC voltage in combination with bias voltage, if any, must not exceed the allowable limits specified for reverse voltage. See the Reverse Voltage section for allowable limits.

The maximum power dissipation by case size can be determined using the table at right. The maximum power dissipation rating stated in the table must be reduced with increasing environmental operating temperatures. Refer to the table below for temperature compensation requirements.

KEMET Case Code	EIA Case Code	Maximum Power Dissipation (P max) mWatts @ 25°C w/+20°C Rise
A	3216-18	75
B	3528-21	85
C	6032-28	110
D	7343-31	150
X	7343-43	165
E	7360-38	200
S	3216-12	60
T	3528-12	70
U	6032-15	90
V	7343-20	125
T510X	7343-43	270
T510E	7360-38	285

The maximum power dissipation rating must be reduced with increasing environmental operating temperatures. Refer to the Temperature Compensation Multiplier table for details.

Temperature Compensation Multipliers for Maximum Ripple Current		
T ≤ 25°C	T ≤ 85°C	T ≤ 125°C
1.00	0.90	0.40

T = Environmental Temperature

Using the P max of the device, the maximum allowable rms ripple current or voltage may be determined.

$$I(max) = \sqrt{P_{max}/R}$$

$$E(max) = Z \sqrt{P_{max}/R}$$

I = rms ripple current (amperes)

E = rms ripple voltage (volts)

P max = maximum power dissipation (watts)

R = ESR at specified frequency (ohms)

Z = Impedance at specified frequency (ohms)

Reverse Voltage

Solid tantalum capacitors are polar devices and may be permanently damaged or destroyed if connected with the wrong polarity. The positive terminal is identified on the capacitor body by a stripe, plus in some cases a beveled edge. A small degree of transient reverse voltage is permissible for short periods per the below table. The capacitors should not be operated continuously in reverse mode, even within these limits.

Temperature	Permissible Transient Reverse Voltage
25°C	15% of Rated Voltage
85°C	5% of Rated Voltage
125°C	1% of Rated Voltage

Table 2 – Land Dimensions/Courtyard

KEMET	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)					Density Level B: Median (Nominal) Land Protrusion (mm)					Density Level C: Minimum (Least) Land Protrusion (mm)				
Case	EIA	W	L	S	V1	V2	W	L	S	V1	V2	W	L	S	V1	V2
D	7343–31	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84
E ¹	7360–38	4.25	2.77	3.67	10.22	7.30	4.13	2.37	3.87	9.12	6.80	4.03	1.99	4.03	8.26	6.54
X ¹	7343–43	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84

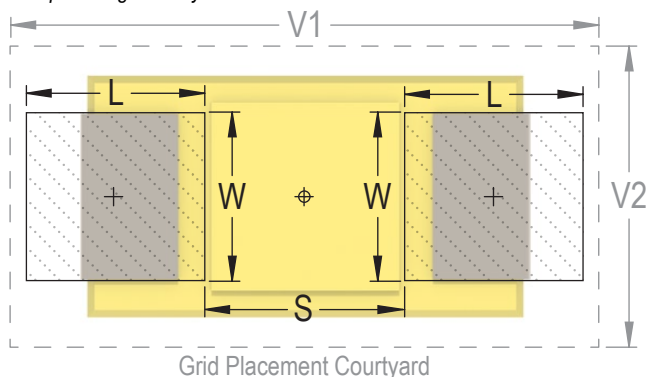
Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes.

Density Level C: For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC standard 7351 (IPC–7351).

¹ Height of these chips may create problems in wave soldering.

² Land pattern geometry is too small for silkscreen outline.



Soldering Process

KEMET's families of surface mount capacitors are compatible with wave (single or dual), convection, IR, or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020D standard for moisture sensitivity testing. The devices can safely withstand a maximum of three reflow passes at these conditions.

Please note that although the X/7343-43 case size can withstand wave soldering, the tall profile (4.3 mm maximum) dictates care in wave process development.

Hand soldering should be performed with care due to the difficulty in process control. If performed, care should be taken to avoid contact of the soldering iron to the molded case. The iron should be used to heat the solder pad, applying solder between the pad and the termination, until reflow occurs. Once reflow occurs, the iron should be removed immediately. "Wiping" the edges of a chip and heating the top surface is not recommended.

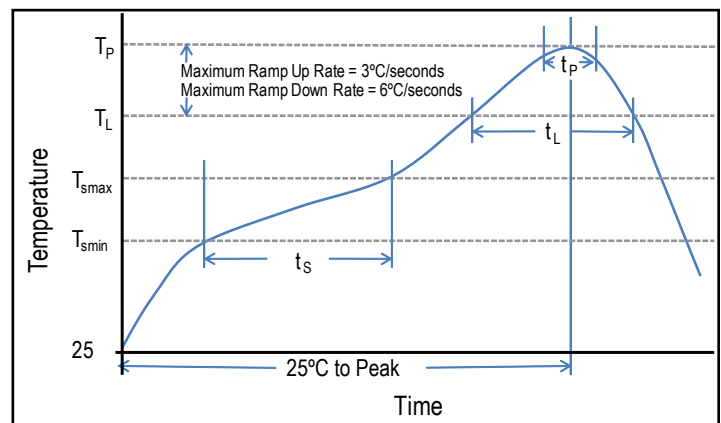
During typical reflow operations, a slight darkening of the gold-colored epoxy may be observed. This slight darkening is normal and not harmful to the product. Marking permanency is not affected by this change.

Profile Feature	SnPb Assembly	Pb-Free Assembly
Preheat/Soak		
Temperature Minimum (T_{Smin})	100°C	150°C
Temperature Maximum (T_{Smax})	150°C	200°C
Time (t_s) from T_{Smin} to T_{Smax}	60 – 120 seconds	60 – 120 seconds
Ramp-up Rate (T_L to T_P)	3°C/seconds maximum	3°C/seconds maximum
Liquidous Temperature (T_L)	183°C	217°C
Time Above Liquidous (t_L)	60 – 150 seconds	60 – 150 seconds
Peak Temperature (T_P)	220°C* 235°C**	250°C* 260°C**
Time within 5°C of Maximum Peak Temperature (t_p)	20 seconds maximum	30 seconds maximum
Ramp-down Rate (T_P to T_L)	6°C/seconds maximum	6°C/seconds maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

Note: All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow.

*Case Size D, E, P, Y, and X

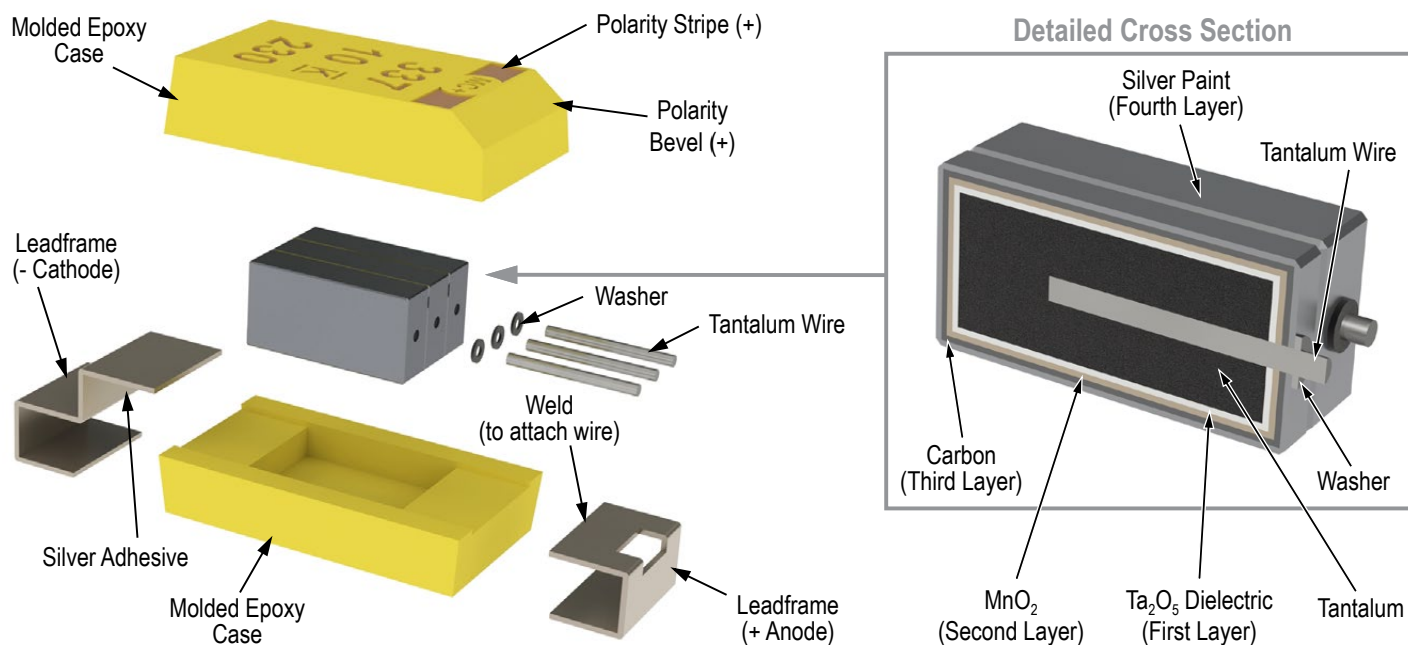
**Case Size A, B, C, H, I, K, M, R, S, T, U, V, W, and Z



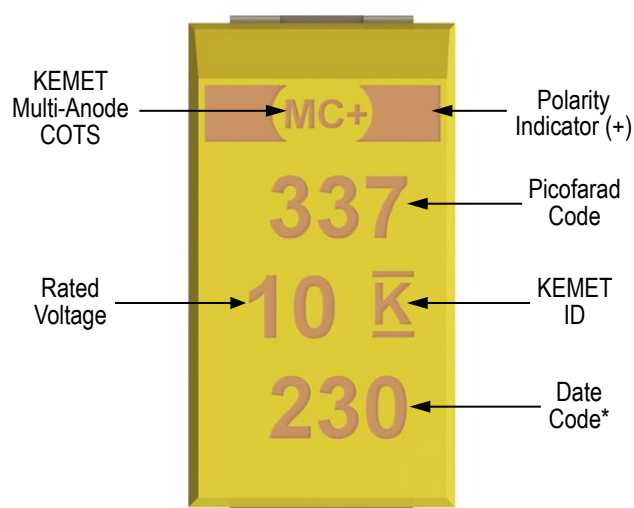
Storage

Tantalum chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature— reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 60% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulphur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within three years of receipt.

Construction



Capacitor Marking



* 230 = 30th week of 2012

Date Code *	
1 st digit = Last number of Year	9 = 2009 0 = 2010 1 = 2011 2 = 2012 3 = 2013 4 = 2014
2 nd and 3 rd digit = Week of the Year	01 = 1 st week of the Year to 52 = 52 nd week of the Year

Tape & Reel Packaging Information

KEMET's molded chip capacitor families are packaged in 8 and 12 mm plastic tape on 7" and 13" reels in accordance with *EIA Standard 481: Embossed Carrier Taping of Surface Mount Components for Automatic Handling*. This packaging system is compatible with all tape-fed automatic pick-and-place systems.

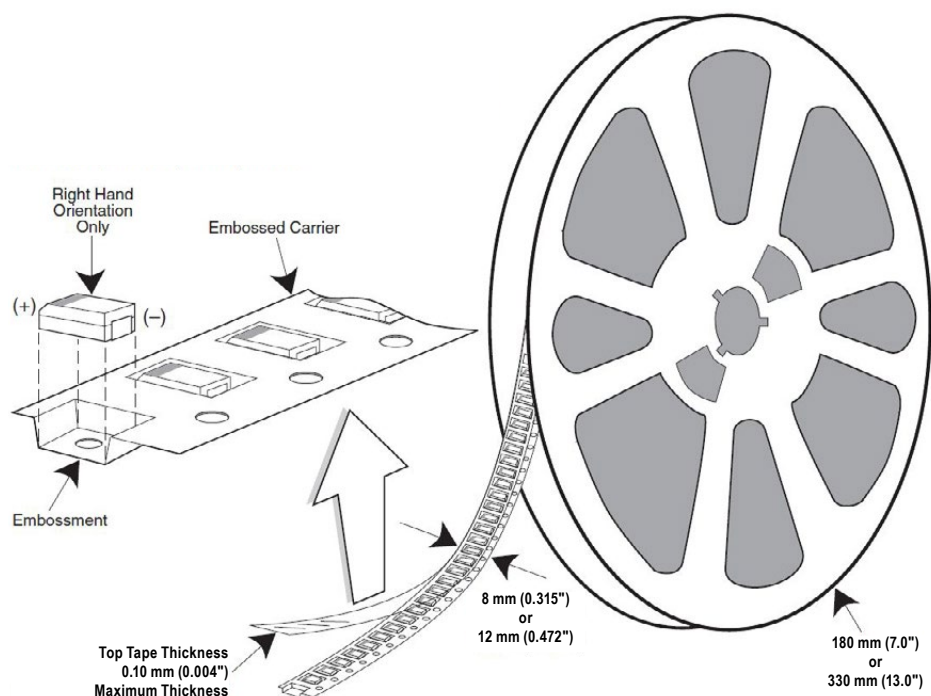


Table 3 – Packaging Quantity

Case Code		Tape Width (mm)	7" Reel*	13" Reel*
KEMET	EIA			
S	3216-12	8	2,500	10,000
T	3528-12	8	2,500	10,000
M	3528-15	8	2,000	8,000
U	6032-15	12	1,000	5,000
L	6032-19	12	1,000	3,000
W	7343-15	12	1,000	3,000
Z	7343-17	12	1,000	3,000
V	7343-20	12	1,000	3,000
A	3216-18	8	2,000	9,000
B	3528-21	8	2,000	8,000
C	6032-28	12	500	3,000
D	7343-31	12	500	2,500
Q	7343-12	12	1,000	3,000
Y	7343-40	12	500	2,000
X	7343-43	12	500	2,000
E/T428P	7360-38	12	500	2,000
H	7360-20	12	1,000	2,500

* No C-Spec required for 7" reel packaging. C-7280 required for 13" reel packaging.

Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

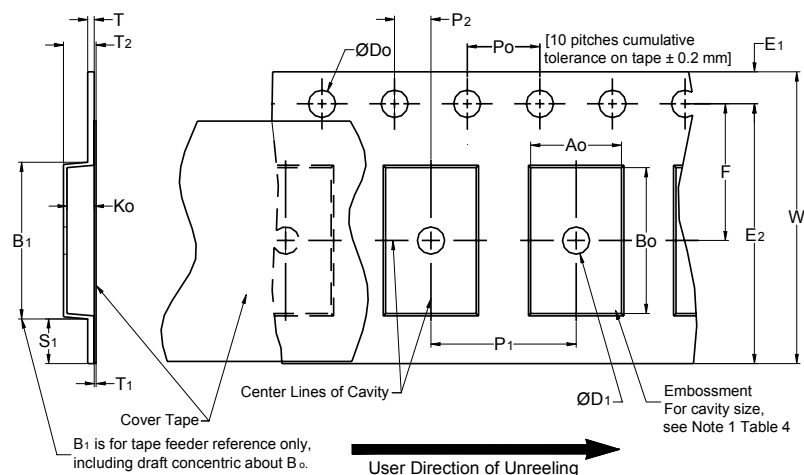


Table 4 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)									
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.0 (0.039)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	25.0 (0.984)	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
12 mm		1.5 (0.059)			2.0 ±0.1 (0.079 ±0.059)	30 (1.181)			
16 mm									
Variable Dimensions — Millimeters (Inches)									
Tape Size	Pitch	B ₁ Maximum Note 4	E ₂ Minimum	F	P ₁	T ₂ Maximum	W Maximum	A ₀ , B ₀ & K ₀	
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	2.0 ±0.05 or 4.0 ±0.10 (0.079 ±0.002 or 0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)	Note 5	
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	2.0 ±0.05 (0.079 ±0.002) or 4.0 ±0.10 (0.157 ±0.004) or 8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)		
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5±0.10 (0.295 ±0.004)	4.0 ±0.10 (0.157 ±0.004) to 12.0 ±0.10 (0.472 ±0.004)	8.0 (0.315)	16.3 (0.642)		

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
2. The tape, with or without components, shall pass around R without damage (see Figure 4).
3. If S₁ < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481–D, paragraph 4.3, section b).
4. B₁ dimension is a reference dimension for tape feeder clearance only.
5. The cavity defined by A₀, B₀ and K₀ shall surround the component with sufficient clearance that:
 - (a) the component does not protrude above the top surface of the carrier tape.
 - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 2).
 - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 mm and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 3).
 - (e) see Addendum in EIA Standard 481–D for standards relating to more precise taping requirements.

Packaging Information Performance Notes

- Cover Tape Break Force:** 1.0 Kg minimum.
- Cover Tape Peel Strength:** The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 Newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

- Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. Refer to EIA Standards 556 and 624.

Figure 2 – Maximum Component Rotation

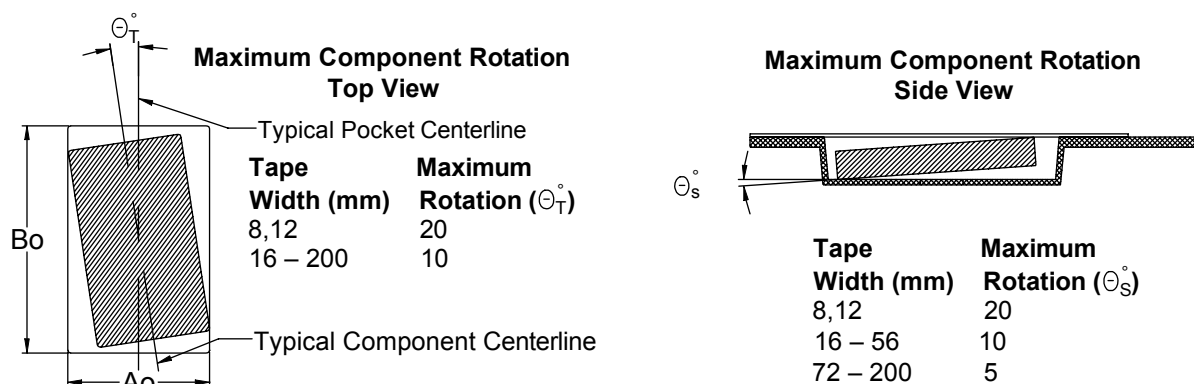


Figure 3 – Maximum Lateral Movement

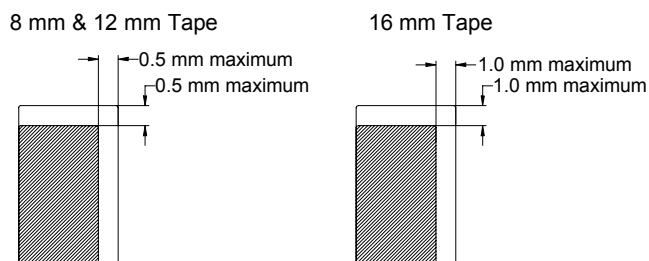


Figure 4 – Bending Radius

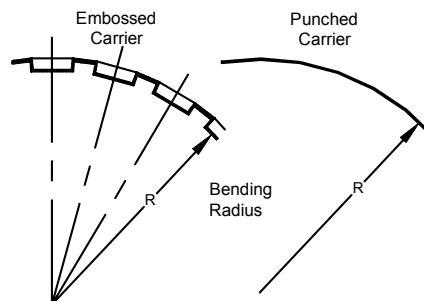
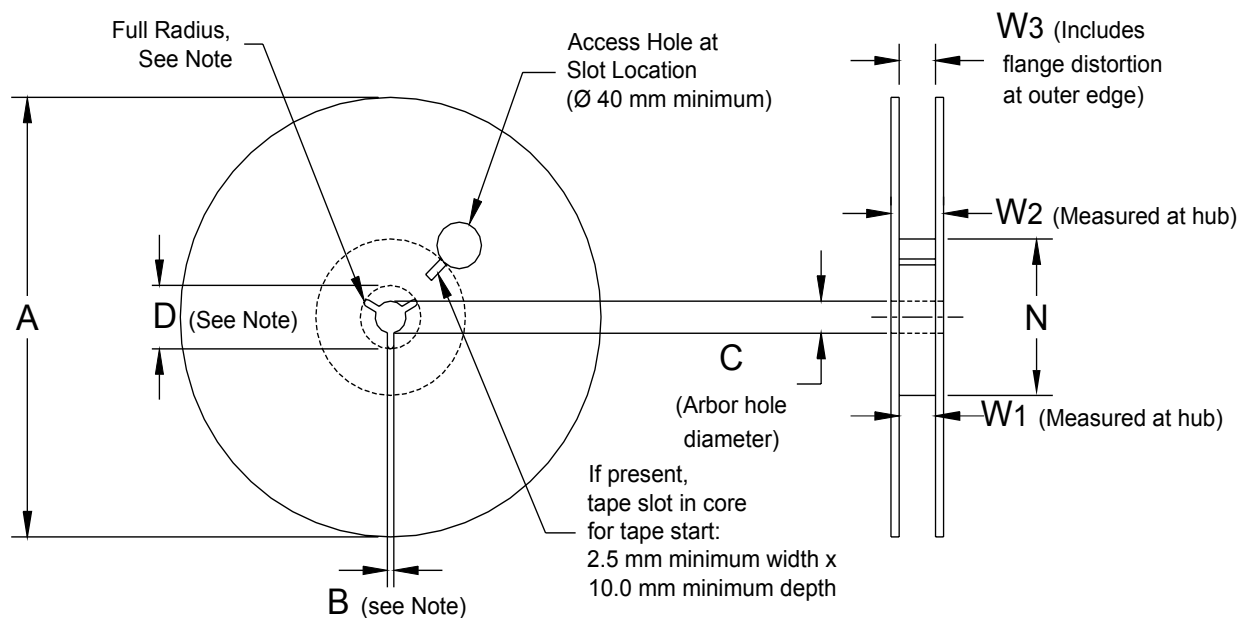


Figure 5 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 5 – Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)				
Tape Size	A	B Minimum	C	D Minimum
8 mm	178 ±0.20 (7.008 ±0.008) or 330 ±0.20 (13.000 ±0.008)	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)
12 mm				
16 mm				
Variable Dimensions — Millimeters (Inches)				
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃
8 mm	50 (1.969)	8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)	Shall accommodate tape width without interference
12 mm		12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)	

Figure 6 – Tape Leader & Trailer Dimensions

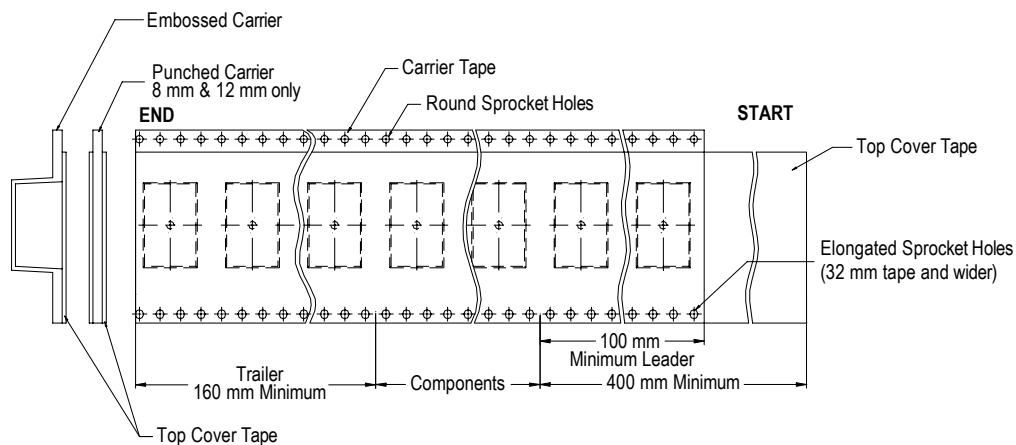
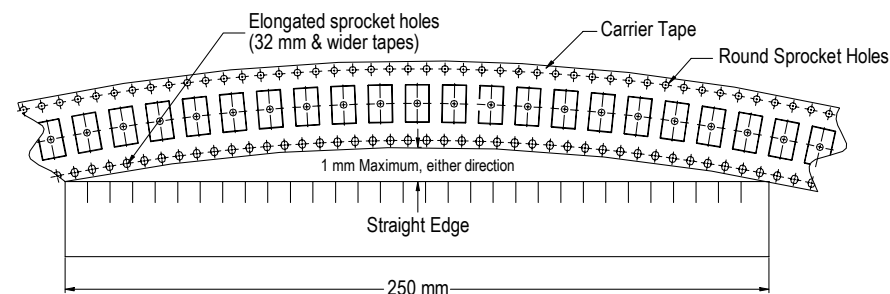


Figure 7 – Maximum Camber



KEMET Corporation World Headquarters

2835 KEMET Way
Simpsonville, SC 29681

Mailing Address:
P.O. Box 5928
Greenville, SC 29606

www.kemet.com
Tel: 864-963-6300
Fax: 864-963-6521

Corporate Offices
Fort Lauderdale, FL
Tel: 954-766-2800

North America

Southeast

Lake Mary, FL
Tel: 407-855-8886

Northeast

Wilmington, MA
Tel: 978-658-1663

Central

Novi, MI
Tel: 248-994-1030

West

Milpitas, CA
Tel: 408-433-9950

Mexico

Guadalajara, Jalisco
Tel: 52-33-3123-2141

Europe

Southern Europe

Sasso Marconi, Italy
Tel: 39-051-939111

Skopje, Macedonia
Tel: 389-2-55-14-623

Central Europe

Landsberg, Germany
Tel: 49-8191-3350800

Kamen, Germany
Tel: 49-2307-438110

Northern Europe

Wyboston, United Kingdom
Tel: 44-1480-273082

Espoo, Finland
Tel: 358-9-5406-5000

Asia

Northeast Asia

Hong Kong
Tel: 852-2305-1168

Shenzhen, China
Tel: 86-755-2518-1306

Beijing, China
Tel: 86-10-5877-1075

Shanghai, China
Tel: 86-21-6447-0707

Seoul, South Korea
Tel: 82-2-6294-0550

Taipei, Taiwan
Tel: 886-2-27528585

Southeast Asia

Singapore
Tel: 65-6701-8033

Penang, Malaysia
Tel: 60-4-6430200

Bangalore, India
Tel: 91-806-53-76817

Note: KEMET reserves the right to modify minor details of internal and external construction at any time in the interest of product improvement. KEMET does not assume any responsibility for infringement that might result from the use of KEMET Capacitors in potential circuit designs. KEMET is a registered trademark of KEMET Electronics Corporation.

Disclaimer

All product specifications, statements, information and data (collectively, the "Information") in this datasheet are subject to change. The customer is responsible for checking and verifying the extent to which the Information contained in this publication is applicable to an order at the time the order is placed.

All Information given herein is believed to be accurate and reliable, but it is presented without guarantee, warranty, or responsibility of any kind, expressed or implied.

Statements of suitability for certain applications are based on KEMET Electronics Corporation's ("KEMET") knowledge of typical operating conditions for such applications, but are not intended to constitute – and KEMET specifically disclaims – any warranty concerning suitability for a specific customer application or use. The Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by KEMET with reference to the use of KEMET's products is given gratis, and KEMET assumes no obligation or liability for the advice given or results obtained.

Although KEMET designs and manufactures its products to the most stringent quality and safety standards, given the current state of the art, isolated component failures may still occur. Accordingly, customer applications which require a high degree of reliability or safety should employ suitable designs or other safeguards (such as installation of protective circuitry or redundancies) in order to ensure that the failure of an electrical component does not result in a risk of personal injury or property damage.

Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicated or that other measures may not be required.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Kemet:

T513X477K006AH6110	T513X337K010AH6110	T513E687K006AH6110	T513D107K016BH6110
T513D156K035BH6110	T513E107K025AK6210	T513X686M025BT6330	T513X337M010BC6130
T513X686K025BH6310	T513D107M016BK6210	T513X108M004AH6430	T513E108M004AH6110
T513X227K016BH6310	T513E687K006AC6330	T513X687K006BT6320	T513X108M004BH6430
T513D156M035BB6310	T513E107K025BH6330	T513X477K006BB6330	T513X687M006AB6430
T513X476M035AH6420	T513X687K004BH6230	T513E687K006BH6320	T513D107M016AK6410
T513X687M006BC6130	T513X687K006AT6220	T513X227K016BB6110	T513D156K035AB6410
T513X107M020AC6120	T513X157M016BT6410	T513X686M025AH6410	T513D107K016BT6220
T513X687M004AT6110	T513X157K016BB6330	T513E687M006BC6230	T513X477K006BB6120
T513X687M006BC6410	T513X477K006AB6320	T513D107K016AC6230	T513X108M004BT6420
T513E687M006BK6430	T513X477M006BK6330	T513X476K035BB6420	T513D107K016AK6220
T513X336K035AK6430	T513D156K035BC6110	T513X157M016AT6310	T513E108K004AB6330
T513E108K004BT6310	T513X477K006AT6110	T513D107M016BH6220	T513E108M004AH6420
T513X108K004AT6120	T513X107K020BC6330	T513X687K004BT6430	T513X476K035BK6230
T513X476K035BC6230	T513X227M016AB6110	T513D107K016BK6310	T513X477M006BB6220
T513X336M035AC6410	T513X337M010AC6410	T513X336K035BT6210	T513X687M004BC6310
T513X476M035AB6430	T513D156M035AK6110	T513E107M025AH6430	T513X477M006BT6410
T513X476K035AB6320	T513X687K004AH6130	T513X227K016BB6320	T513X337K010BT6210
T513X157K016BB6120	T513X157M016AB6430	T513E108K004AH6220	T513X157M016AH6320
T513E108K004AT6210	T513X476K035AK6430	T513X477K006BH6320	T513X687M004BT6110
T513X336M035BK6330	T513E107K025BB6130	T513X157M016BH6420	T513X687K006AC6420
T513X686K025BB6320	T513X686M025BH6410	T513X476M035BK6330	T513D107M016AB6230
T513E108K004AC6410	T513X337M010AK6330	T513X477M006AB6430	T513X336M035AK6330
T513X336K035BH6220	T513X107M020BK6420	T513X157K016BH6320	T513D156K035BK6320
T513X157K016AK6130	T513X107M020BC6220	T513E108M004BC6230	T513X336M035BH6110