TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA76432FT,TA76432FC,TA76432F,TA76432FR,TA76432S

1.26-V Adjustable High-Precision Shunt Regulators

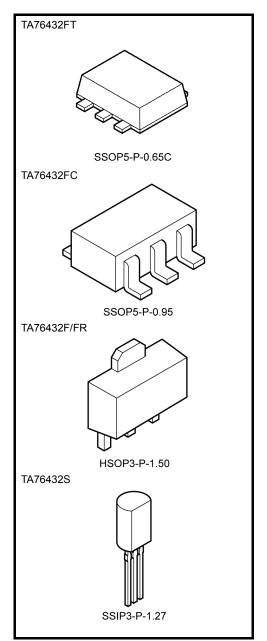
The TA76432 series consists of adjustable high-precision shunt regulators whose output voltage (V_{KA}) can be set arbitrarily using two external resistors.

These devices have a precise internal reference voltage of 1.26 V, enabling them to operate at low voltage.

The devices are ideal for use as error amplifiers in 3-V switching-regulator systems. In addition, they can be used as zener diodes to perform temperature compensation.

Features

- Precision reference voltage: $V_{REF} = 1.26 \text{ V} \pm 1.4\% \text{ (Ta} = 25^{\circ}\text{C)}$
- Small temperature coefficient: | aVREF | = 30 ppm/°C (typ.)
- Adjustable output voltage: $V_{REF} \le V_{OUT} \le 19 \text{ V}$
- Minimum cathode current for regulation: I_{kmin} = 0.5 mA (max.)
- Operating temperature: $Ta = -40 \sim 85$ °C
- Packages: UFV (TA76432FT), SMV (TA76432FC), PW-MINI (TA76432F/FR) and TO-92MOD (TA76432S)
- The TA76432FT is housed in an ultra-thin UFV package. (thickness: 0.7 mm typ.)



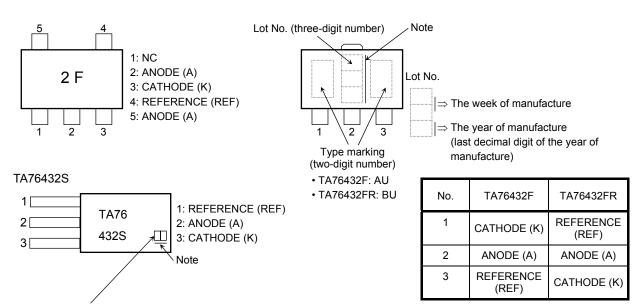
Weight

SSOP5-P-0.65C : 0.007 g (typ.) SSOP5-P-0.95 : 0.014 g (typ.) HSOP3-P-1.50 : 0.05 g (typ.) SSIP3-P-1.27 : 0.36 g (typ.)

Pin Assignment/Marking



TA76432F/FR



Lot No: The last decimal digit of the year of manufacture followed by the month as letters A to L of the alphabet. For example: Jan-2001 is coded as "1A"

Note: TA76432F vs. TA76432FR: reverse pin connection.

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

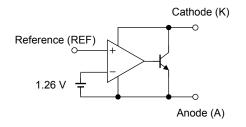
How to Order

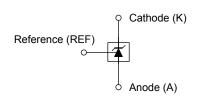
Product No.	Package Type	Packing Type and Capacity	Minimum Order
TA76432FT (TE85L)	UFV (surface-mount type)	Embossed tape: 3000/tape	1 tape
TA76432FC (TE85L)	SMV (surface-mount type)	Embossed tape: 3000/tape	1 tape
TA76432F/R	PW-MINI (SOT-89)	On cut tape (TE12L): 100/tape section	100
TA76432F/R (TE12L)	(surface-mount type)	Embossed tape: 1000/tape	1 tape
TA76432S	TO-92MOD	Loose in bag: 200/bag	1 bag
TA76432S (TPE6)	(lead type)	Radial tape: 2000/tape	1 tape

Note: The lead pitch for the TA76432S and TA76432S (TPE6) may vary.

Functional Block Diagram

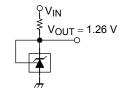
Circuit Symbol

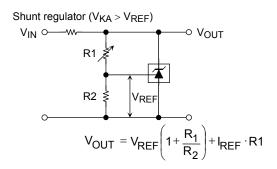




Typical Application Circuits

1.26 V Reference ($V_{KA} = V_{REF}$)





Precautions during Use

(1) TA76432FT, TA76432FC, TA76432F/FR, TA76432S

These products contain MOS elements. Please take care to avoid generating static electricity when handling these devices.

(2) TA76432FT, TA76432FC, TA76432F/FR, TA76432S

The oscillation frequency of these devices is determined by the value of the capacitor connected between

the anode and the cathode. When establishing maximum operating condition parameters, please derate the maximum rating values specified in these datasheets so as to allow an operational safety margin.

Use of a laminated ceramic capacitor is recommended.

Precautions when handling anode pins of TA76432FT/TA76432FC
Pin 2 and pin 5 should normally be shorted together. If only pin 5 is used, pin 2 should either be left open or always kept at a lower potential than pin 5. Do not leave pin 5 open and use pin 2 only.



Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Cathode voltage		V_{KA}	20	V	
Cathode current		ΙK	20	mA	
Cathode-anode reverse current		-IK	10	mA	
Reference voltage		V _{REF}	7	V	
Reference current		I _{REF}	50	μА	
Reference-anode reverse current		-I _{REF}	10	mA	
	TA76432FT		0.45 (Note 1)	w	
	TA76432FC		0.2		
Power dissipation		P_{D}	0.38 (Note 2)		
	TA76432F/FR		0.5		
	TA76432S		0.8		
	TA76432FT		277 (Note 1)	°C/W	
Thermal resistance	TA76432FC		625		
		R _{th}	328 (Note 2)		
	TA76432F/FR		250		
	TA76432S		156		
Operating temperature		T _{opr}	-40~85	°C	
Junction temperature		Tj	150	°C	
Storage temperature		T _{stg}	-55~150	°C	

Note 1: Glass epoxy board mounting: 30 mm \times 30 mm \times 0.8 mmt (Cu pad area 35 mm 2)

Note 2: Glass epoxy board mounting: $30 \text{ mm} \times 30 \text{ mm} \times 0.8 \text{ mmt}$ (Cu pad area 50 mm^2)

Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Cathode voltage	V_{KA}	V_{REF}	_	19	V
Cathode current	ΙK	0.5	_	15	mA
Operating temperature	T _{opr}	-40		85	°C

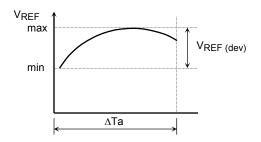


Electrical Characteristics (Unless otherwise specified, Ta = 25°C, $I_K = 5$ mA)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reference voltage	V_{REF}	$V_{KA} = V_{REF}$	1.242	1.26	1.278	V
Deviation of reference input voltage over temperature	V _{REF} (dev)	0°C ≦ Ta ≦ 85°C, V _{KA} = V _{REF}	_	3	15	mV
Ratio of change in reference input	ΔV _{REF} /ΔV	$V_{REF} \le V_{KA} \le 5 V$	_	0.5	2.5	mV/V
voltage to the change in cathode voltage		5 V ≤ V _{KA} ≤ 19 V	_	0.3	2.0	
Reference input current	I _{REF}	V _{KA} = V _{REF}	_	2	4	μА
Deviation of reference input current over temperature	I _{REF (dev)}	$\begin{array}{l} 0^{\circ}C \leq Ta \leq 85^{\circ}C, V_{KA} = V_{REF}, \\ R_1 = 10 \; k\Omega, \; R_2 = \infty \end{array}$	_	0.3	1.2	μА
Minimum cathode current for regulation	I _{Kmin}	V _{KA} = V _{REF}	_	0.2	0.5	mA
Off-State cathode current	I _{Koff}	V _{KA} = 19 V, V _{REF} = 0 V	_	_	1.0	μА
Dynamic impedance	Z _{KA}	$V_{KA} = V_{REF}, f \le 1 \text{ kHz},$ 0.5 mA $\le I_K \le 15 \text{ mA}$	_	0.2	0.5	Ω

The deviation parameters $V_{REF\,(dev)}$ and $I_{REF\,(dev)}$ are defined as the maximum variation of the V_{REF} and I_{REF} over the rated temperature range.

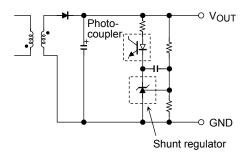
The average temperature coefficient of the VREF is defined as:



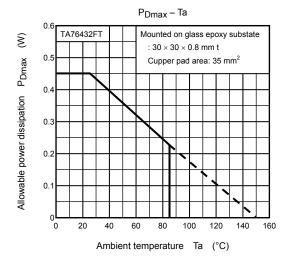
$$\left| \alpha V_{REF} \right| = \frac{\left(\frac{V_{REF (dev)} \times 10^6}{V_{REF @25^{\circ}C}} \right)}{\Delta Ta} (ppm/^{\circ}C)$$

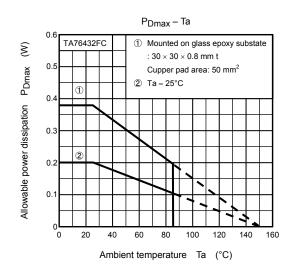
Application Circuit Example

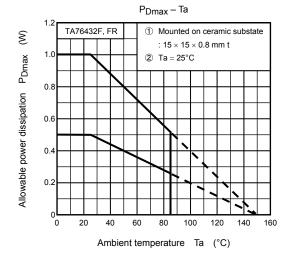
Error amplification circuit for the switching power supply

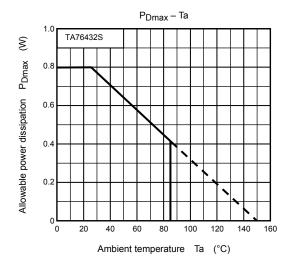


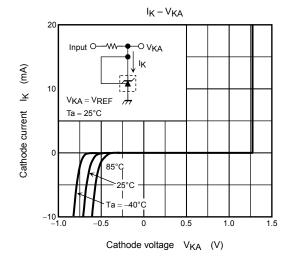
This circuit amplifies the difference between the switching power supply's secondary output voltage and the shunt regulator's reference voltage. It then feeds the amplified voltage back to the primary input voltage via the photocoupler.

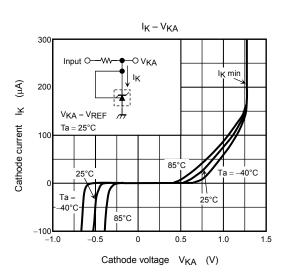


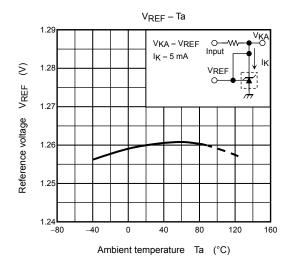


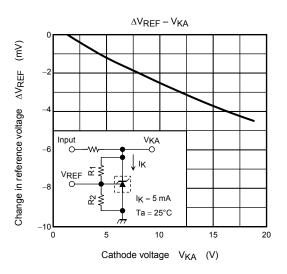


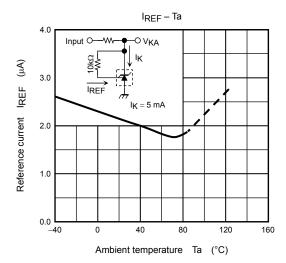


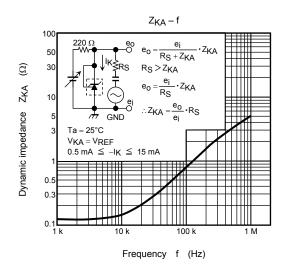


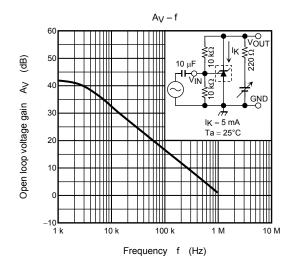


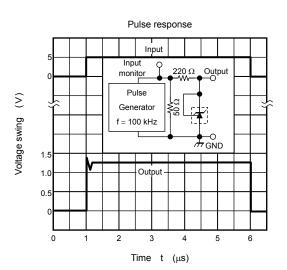


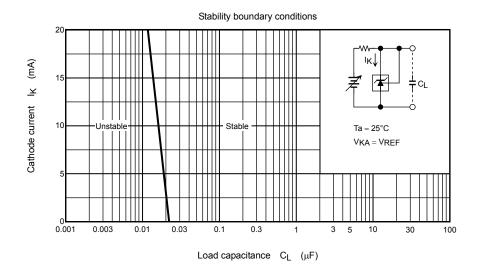




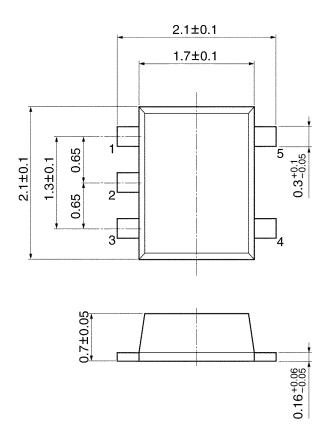








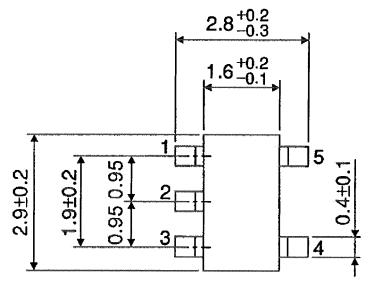
SSOP5-P-0.65C Unit: mm

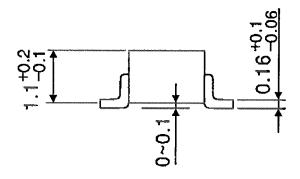


TA76432FT (UFV)

Weight: 0.007 g (typ.)

SSOP5-P-0.95 Unit: mm



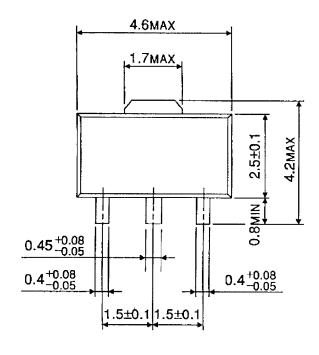


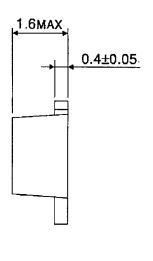
TA76432FC (SMV)

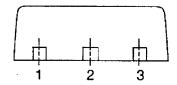
Weight: 0.014 g (typ.)

HSOP3-P-1.50

Unit : mm



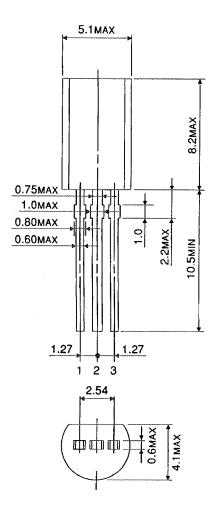




TA76432F/FR (PW-MINI)

Weight: 0.05 g (typ.)

SSIP3-P-1.27



TA76432S (TO-92MOD)

Weight: 0.36 g (typ.)



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