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Part Number : TPDCPLD1 - Bartek CPLD Development Kit

The CPLD Development Kit from Bartek is a platform for developing applications with Complex Programmable Logic Devices (CPLDs). It connects to an Explorer 16 development board or can be used in standalone mode. The kit includes an Altera MAX EPM712AETC100-10 device with 76 programmable logic channels. This device provides 2500 usable gates, 128 Macrocells, and eight Logic Array blocks. All PICTail I/O signals and CPLD pins are brought out to headers which can be interconnected using the included jumper wires.

The CPLD device can be programmed using the included USB Byte Blaster JTAG programmer, and free tools that are available from Altera.

- I/O pins are compatible with 5V, 3.3V, or 2.5V logic through a selectable jumper
- USB JTAG programmer and cables included

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