

Small Signal Fast Switching Diode



FEATURES

 These diodes are also available in other case styles including the DO-35 case with the type designation 1N4148, the MiniMELF case with the type designation LL4148, and the SOT-23 case with the type designation IMBD4148-V.





- · Silicon epitaxial planar diode
- Fast switching diodes
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

MECHANICAL DATA

Case: SOD-123

Weight: approx. 10.3 mg
Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS	
1N4148W-V	1N4148W-V-GS18 or 1N4148W-V-GS08	A2	Single diode	Tape and reel	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Reverse voltage		V _R	75	V		
Repetitive peak reverse voltage		V_{RRM}	100	V		
Average rectified current half wave rectification with resistive load (1)	f ≥ 50 Hz	I _{F(AV)}	150	mA		
Surge forward current	t < 1 s and T _j = 25 °C	I _{FSM}	500	mA		
Power dissipation (1)		P _{tot}	350	mW		

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R _{thJA}	375	K/W	
Junction temperature		Tj	150	°C	
Storage temperature		T _{stg}	- 65 to + 150	°C	

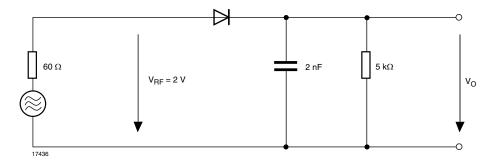
Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature.

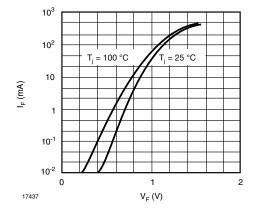


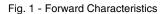
ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 10 mA	V _F			1000	mV
Forward voltage	$I_F = 100 \text{ mA}$	V _F			1200	mV
	V _R = 20 V	I _R			25	nA
Lookaga ayyyant	V _R = 75 V	I _R			5	μΑ
Leakage current	V _R = 100 V	I _R			100	μΑ
	$V_R = 20 \text{ V}, T_J = 150 ^{\circ}\text{C}$	I _R			50	μΑ
Diode capacitance	$V_F = V_R = 0 V$	C _D			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \mu s$, rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V _{fr}			2.5	V
Reverse recovery time	I_F = 10 mA, I_R = 1 mA, V_R = 6 V, R_L = 100 Ω	t _{rr}			4	ns
Rectification efficiency	f = 100 MHz, V _{RF} = 2 V	ην	0.45			

RECTIFICATION EFFICIENCY MEASUREMENT CIRCUIT



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)





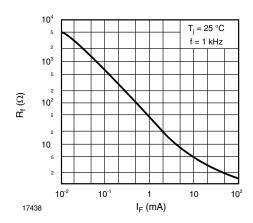
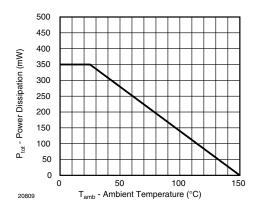


Fig. 2 - Dynamic Forward Resistance vs. Forward Current







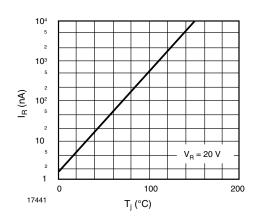


Fig. 5 - Leakage Current vs. Junction Temperature

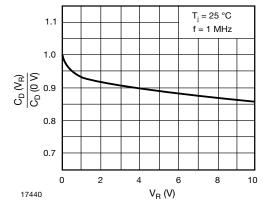


Fig. 4 - Relative Capacitance vs. Reverse Voltage

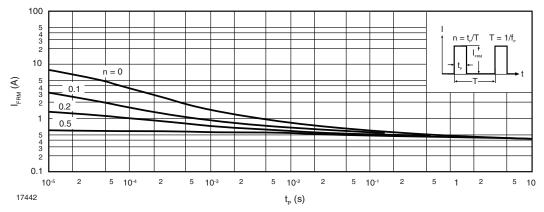
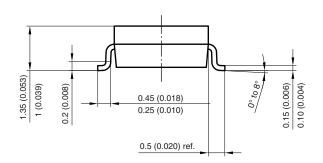
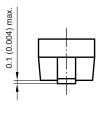


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

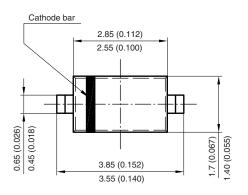


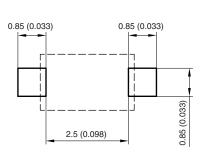
PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Mounting Pad Layout





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