

TOSHIBA HIGH EFFICIENCY DIODE STACK (HED) SILICON EPITAXIAL TYPE

16DL2C41A

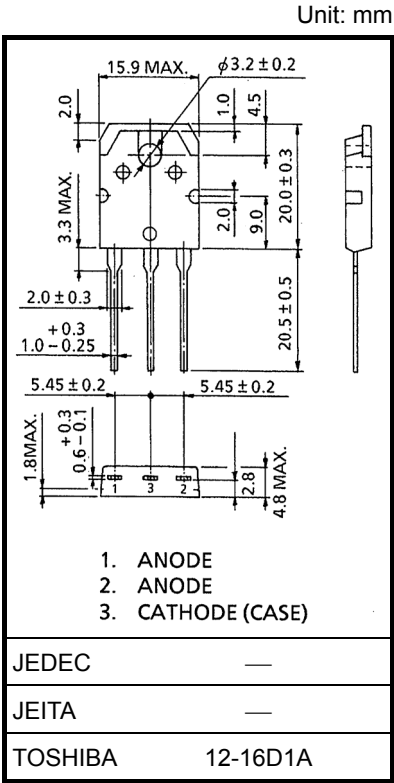
SWITCHING MODE POWER SUPPLY APPLICATION
CONVERTER & CHOPPER APPLICATION

- Repetitive Peak Reverse Voltage : $V_{RRM}=200V$
- Average Output Rectified Current : $I_O=16A$
- Ultra Fast Reverse-Recovery Time : $t_{rr}=35ns$ (Max)
- Low Forward Voltage : $V_{FM}=0.98V$ (Max)
- Low Switching Losses and Output Noise

ABSOLUTE MAXIMUM RATINGS

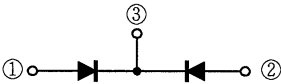
CHARACTERISTIC	SYMBOL	RATING	UNIT
Repetitive Peak Reverse Voltage	V_{RRM}	200	V
Average Output Rectified Current (Full Sine Waveform)	I_O	16	A
Peak One Cycle Surge Forward Current (Non-Repetitive)	I_{FSM}	80 (50Hz) 88 (60Hz)	A
Junction Temperature	T_j	-40~150	°C
Storage Temperature Range	T_{stg}	-40~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

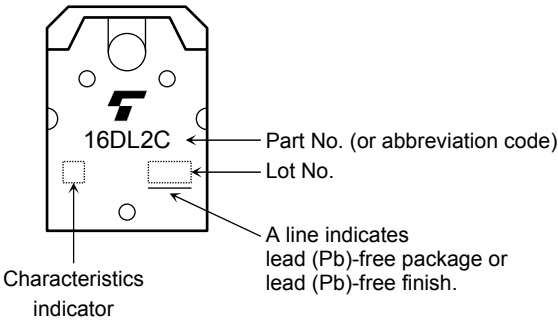


Weight: 4.85 g (typ.)

POLARITY



MARKING



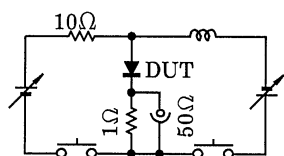
Abbreviation Code	Part No.
16DL2C	16DL2C41A

ELECTRICAL CHARACTERISTICS (Ta=25°C) (Note 1)

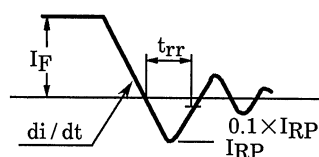
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	MAX.	UNIT
Peak Forward Voltage	V_{FM}	$I_{FM}=8A$	—	0.98	V
Repetitive Peak Reverse Current	I_{RRM}	$V_{RRM}=\text{Rated}$	—	50	μA
Reverse Recovery Time	t_{rr}	$I_F=2.0A$ $di/dt=-50A/\mu s$ (Note 2)	—	35	ns
Forward Recovery Time	t_{fr}	$I_F=1.0A$ (Note 3)	—	100	ns
Thermal Resistance	$R_{th(j-c)}$	Total DC, Junction to Case	—	1.9	$^{\circ}C/W$

Note 1: A value applied to one cell.

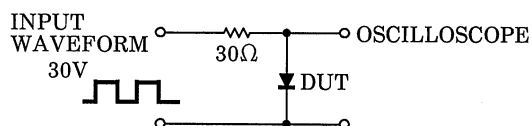
Note 2: t_{rr} Test circuit



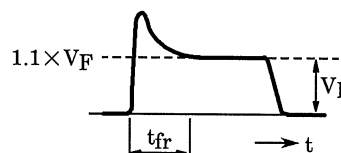
t_{rr} Waveform



Note 3: t_{fr} Test circuit



t_{fr} Waveform



Handling Precaution

The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

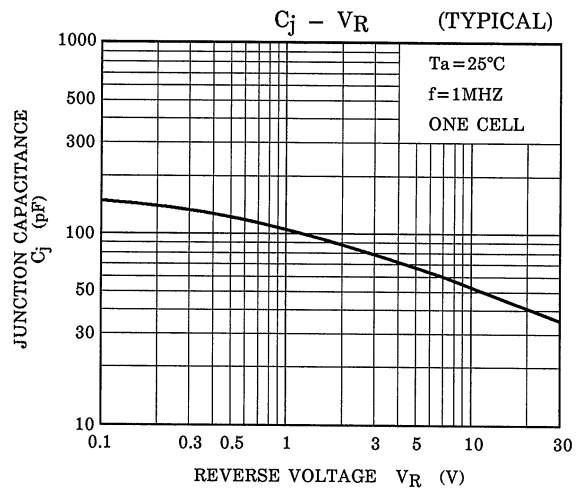
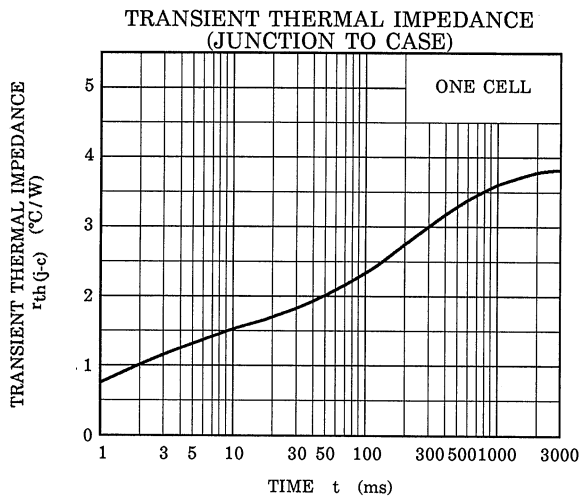
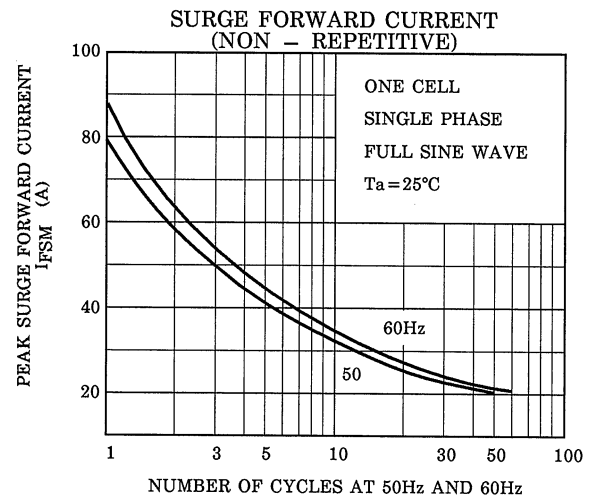
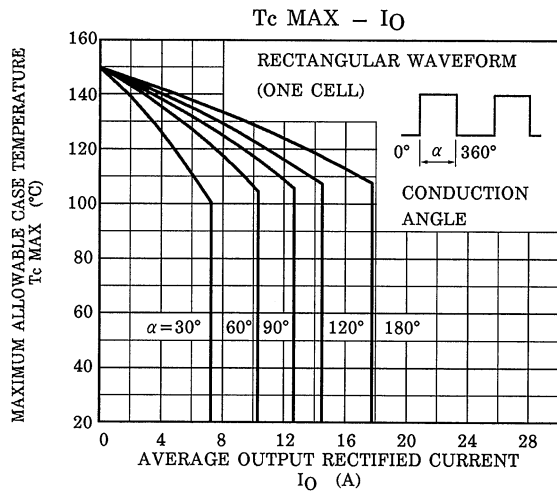
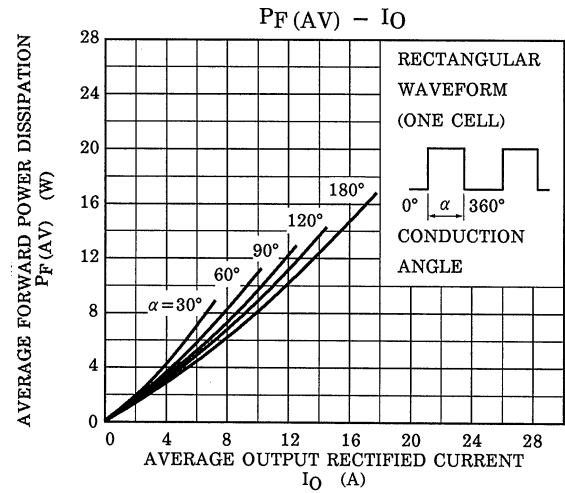
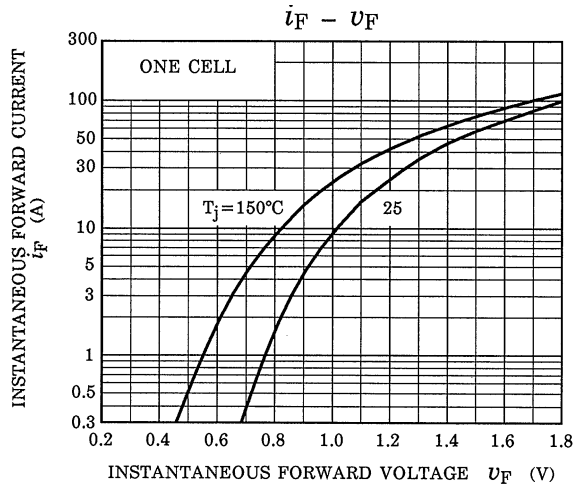
VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the absolute maximum rating of VRRM for a DC circuit and be no greater than 50% of that of VRRM for an AC circuit. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

I_O: We recommend that the worst case current be no greater than 80% of the absolute maximum rating of I_O. Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Tamax-I_O curve.

This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle 180. Therefore, this is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

We recommend that a device be used at a T_j of below 120°C under the worst load and heat radiation conditions.

Please refer to the Rectifiers databook for further information.



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20070701-EN

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In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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