

REV. Status

PRELIMINARY
04/26/00 JCPRELIMINARY 1
DEL. INSERTION LOSS
08/11/00 LLADSL transformer designed for use with Texas Instrument ADSL IC chip sets
TNETD2000R, TNETD3000R, TNETD300D, TNETD4000R (CPE side)

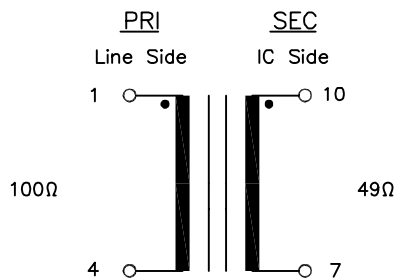
A. Electrical Specifications (@ 25°C)

1. Primary (Line side) Impedance; 100 Ω
2. Secondary (IC side) Impedance; 49 Ω
3. Frequency Response; $\pm 0.5\text{dB}$ MAX @ 100KHz to 1.1MHz 0dBm
4. Longitudinal Balance; 50dBm MIN @ 1.1MHz
(Per ITU method pin 10 grounded)
5. Primary Open Circuit Inductance; 1.5mH $\pm 5\%$ @ 100KHz, 0.1Vrms
Measured (1-4)
6. Leakage Inductance; 18.0 μH MAX @ 100KHz, 0.01Vrms
Measured (1-4) with 7 & 10 shorted
7. Interwinding Capacitance; 35pF MAX
Measured (1-10)
8. DC Resistance;
4.0 Ω MAX measured (1-4)
0.7 Ω MAX measured (10-7)
9. Turns Ratio; (1-4):(7-10)=1.42:1 $\pm 2\%$
10. Dielectric Strength; 1875V, 1 second @ Pri to Sec
11. Total Harmonic Distortion; -80dB MAX @ 100KHz, 15Vpp
Measured (1-4)

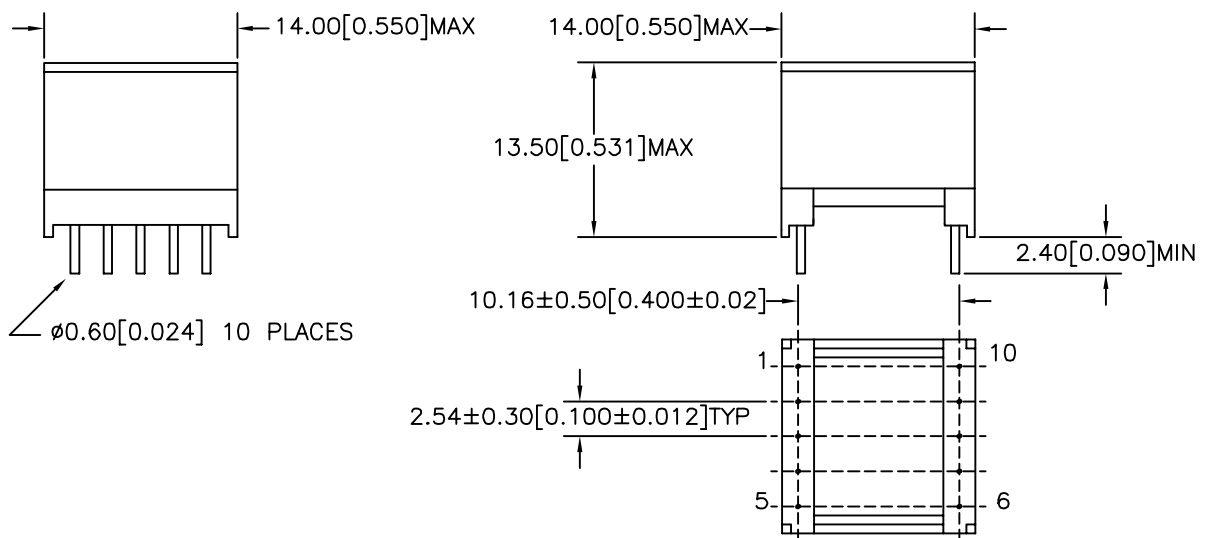
B. Marking; TTC-4054, TAMURA, date code and country of origin

C. Safety: Complies with UL1950

D. Schematic



E. Mechanical Specifications



PREPARED BY:

D. Rund

ENGINEER:

L. Le

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO.
P-A1-12479
ACAD\TTC\A1124791.DWGREV
PR1ADSL THROUGH HOLE
TRANSFORMERCONTENTS OF THIS DRAWING ARE
SUBJECT TO CHANGE WITHOUT
PRIOR NOTICE**TAMURA CORPORATION OF AMERICA**
43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624
(909) 699-1270 FAX 9096769482**TTC-4054**

MODEL SPECIFICATION

DIM: mm(In) SCL: 2/1 SH: 1 OF 1

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MODEL NUMBER

TTC-4054