TOSHIBA Field Effect Transistor Silicon P Channel MOS Type ( $L^2-\pi$ -MOSV)

# 2SJ512

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance : RDS (ON) = 1.0  $\Omega$  (typ.)

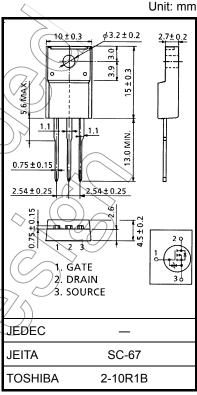
• High forward transfer admittance :  $|Y_{fs}| = 3.7 \text{ S (typ.)}$ 

• Low leakage current :  $I_{DSS} = -100 \mu A \text{ (max) (V}_{DS} = -250 \text{ V)}$ 

• Enhancement mode :  $V_{th} = -1.5 \text{ to } -3.5 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA})$ 

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	-250	(v)
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	-250	V
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	-5	У A
	Pulse (Note 1)	I <sub>DP</sub>	-20	Α
Drain power dissipation (Tc = 25°C)		PD	30	W
Single pulse avalanche energy (Note 2)		EAS	155	(mJ
Avalanche current		IAR	)) -5	A
Repetitive avalenche energy (Note 3)		EAR	3.0 m.	
Channel temperature		T <sub>ch</sub>	150 °C	
Storage temperature range		Tstg	-55 to 150 ℃	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

	110		
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th</sub> (ch-c)	4.16	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = -50 V,  $T_{ch}$  = 25°C (initial), L = 10.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = -5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

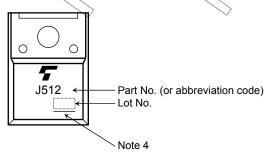
### **Electrical Characteristics (Ta = 25°C)**

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = -250 V, V <sub>GS</sub> = 0 V	_	_	-100	μΑ
Drain-source br voltage	eakdown	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-250	_	_	V
Gate threshold v	oltage	$V_{th}$	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	(1.5	5	-3.5	V
Drain-source Ol	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -2.5 A		1.0	1.25	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -2.5 \text{ A}$	1.8	3.7		S
Input capacitano	e	C <sub>iss</sub>			800		
Reverse transfer	r capacitance	C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	· —	80	-	pF
Output capacitance		Coss		_	250	_	
Switching time	Rise time	t <sub>r</sub>	V <sub>GS</sub> <sup>0V</sup> I <sub>D</sub> = -2.5A V <sub>OUT</sub>	-	16	/	
	Turn-on time	t <sub>on</sub>	$-10V$ $R_L = 40\Omega$		35	) —	- ns
	Fall time	t <sub>f</sub>	v <sub>DD</sub> = -100V	7	> 9	1	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\rm W} = 10 \mu {\rm s}$		70		
Total gate char plus gate-drain)	ge (Gate-source	Qg	V <sub>DD</sub> ≈ -200 V, V <sub>GS</sub> = -10 V,	_	22		
Gate-source charge		Q <sub>gs</sub>	D = -5 A	_	14	_	nC
Gate-drain ("mil	ler") charge	Q <sub>gd</sub>			8	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	<b>J</b> DR	<u> </u>	_	_	-5	Α
Pulse drain reverse current (Note 1)	) I <sub>DRP</sub>	_	-	_	-20	Α
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = -5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	2.0	٧
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = -5 A, V <sub>GS</sub> = 0 V		205	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs	_	2.1	_	μC





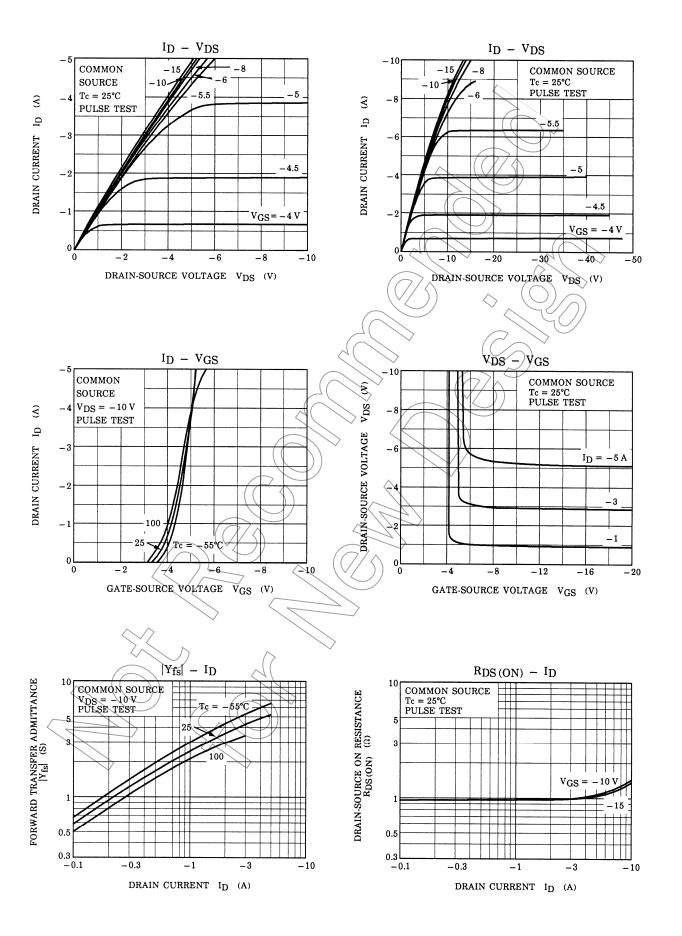
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

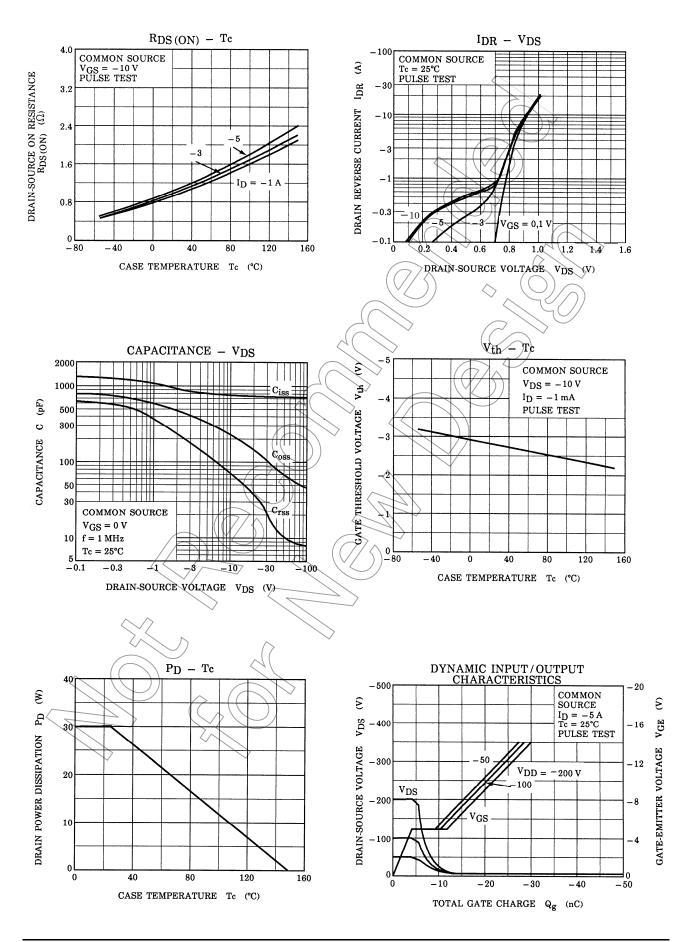
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

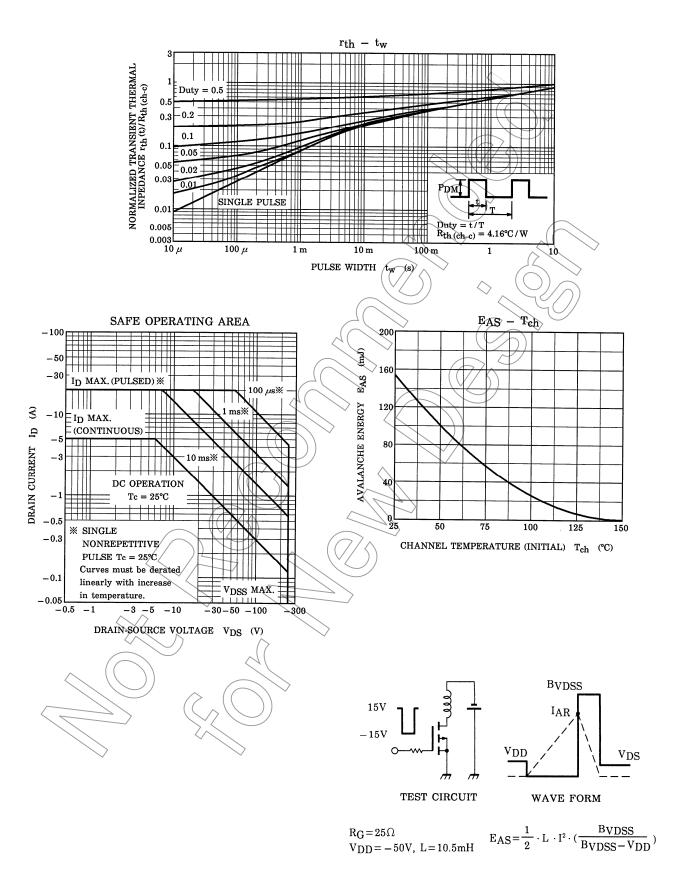
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