

TIP29, 30

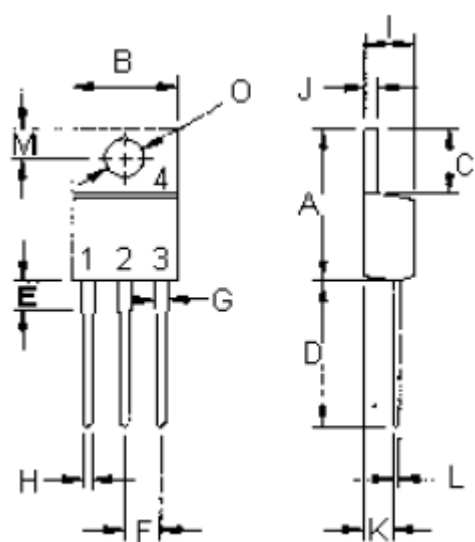
High Power Bipolar Transistors



TO - 220

Features:

- Collector - emitter sustaining voltage -
 $V_{CEO(sus)} = 60\text{ V (minimum) - TIP29A, TIP30A}$
 $= 100\text{ V (minimum) - TIP29C, TIP30C}$
- Collector - emitter saturation voltage -
 $V_{CE(sat)} = 0.7\text{ V (maximum) at } I_C = 1\text{ A}$
- Current gain-bandwidth product $f_T = 3\text{ MHz (Minimum) at } I_C = 200\text{ mA}$



Dimensions	Minimum	Maximum
A	14.68	15.31
B	9.78	10.42
C	5.01	6.52
D	13.06	14.62
E	3.57	4.07
F	2.42	3.66
G	1.12	1.36
H	0.72	0.96
I	4.22	4.98
J	1.14	1.38
K	2.2	2.97
L	0.33	0.55
M	2.48	2.98
O	3.7	3.9

Dimensions : Millimetres

Pin

1. Base
2. Collector
3. Emitter
4. Collector (Case)

Maximum Ratings

Characteristic	Symbol	TIP29A TIP30A	TIP29A TIP30A	Unit
Collector - emitter voltage	V _{CEO}	60	100	V
Collector - base voltage	V _{CBO}			V
Emitter - base voltage	V _{EBO}	5		V
Collector current - continuous -peak	I _C	1 3		A
Base current	I _B	0.4		A
Total power dissipation at T _c = 25°C derate above 25°C	P _D	30 0.24		W W/°C
Operating and storage Junction temperature range	T _J , T _{STG}	-65 to +150		°C

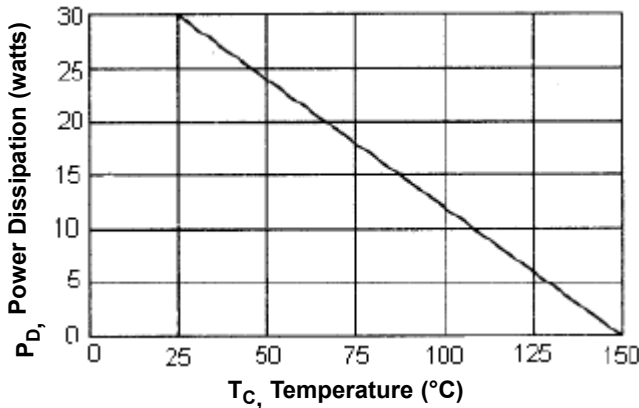
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Thermal Characteristics

Characteristic	Symbol	Maximum	Unit
Thermal resistance junction to case	$R_{\theta jc}$	4.167	$^{\circ}\text{C/W}$

Power Derating



Electrical Characteristics ($T_C = 25^{\circ}\text{C}$ Unless Otherwise noted)

Characteristics	Symbol	Minimum	Maximum	Units
Off Characteristics				
Collector - emitter sustaining voltage (1) ($I_C = 30\text{ mA}$, $I_B = 0$)	$V_{CEO(SUS)}$	60 100	-	V
Collector cut off current ($V_{CE} = 30\text{ V}$, $I_B = 0$) ($V_{CE} = 60\text{ V}$, $I_B = 0$)	I_{CEO}	-	0.3	mA
Collector cut off current ($V_{CE} = 60\text{ V}$, $V_{EB} = 0$) ($V_{CE} = 80\text{ V}$, $V_{EB} = 0$)	I_{CES}	-	0.2	mA
Emitter cut off current ($V_{EB} = 5\text{ V}$, $I_C = 0$)	I_{EBO}	-	1	mA
On Characteristics (1)				
DC current gain ($I_C = 0.2\text{ A}$; $V_{CE} = 4\text{ V}$) ($I_C = 1\text{ A}$; $V_{CE} = 4\text{ V}$)	h_{FE}	40 15	- 75	-
Collector - emitter saturation voltage ($I_C = 1\text{ A}$; $I_B = 125\text{ mA}$)	$V_{CE(sat)}$	-	0.7	V
Base-emitter on voltage ($I_C = 1\text{ A}$; $V_{CE} = 4\text{ V}$)	$V_{BE(on)}$	-	1.3	V
Dynamic characteristics				
Current gain-bandwidth Product (2) ($I_C = 200\text{ mA}$; $V_{CE} = 10\text{ V}$, $f = 1\text{ MHz}$)	f_T	3	-	MHz
Small signal current gain ($I_C = 200\text{ mA}$; $V_{CE} = 10\text{ V}$, $f = 1\text{ kHz}$)	h_{fe}	20	-	-

(1) Pulse test: Pulse width = 300 μs , duty cycle $\leq 2\%$

(2) $f_T = |h_{FE}| \cdot f_{TEST}$

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Figure-2 Turn on Time

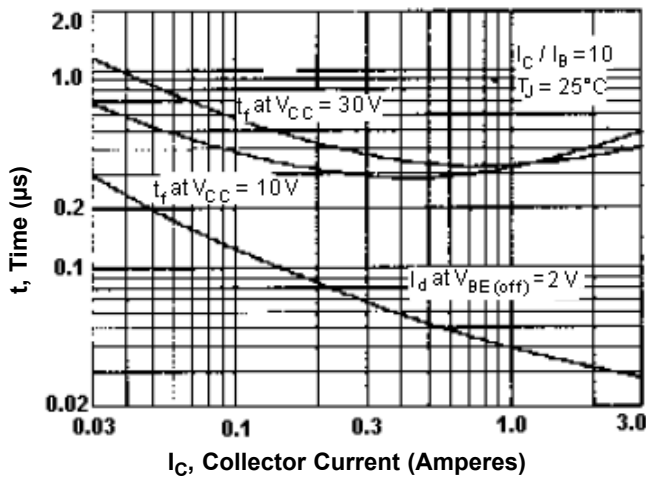
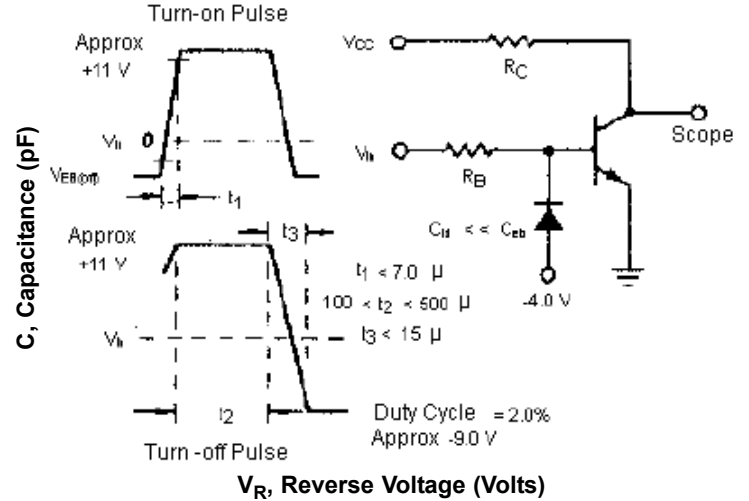


Figure - 3 Switching Time Equivalent Circuit



R_B and R_C varied to obtain desired current levels

Figure-4 DC Current Gain

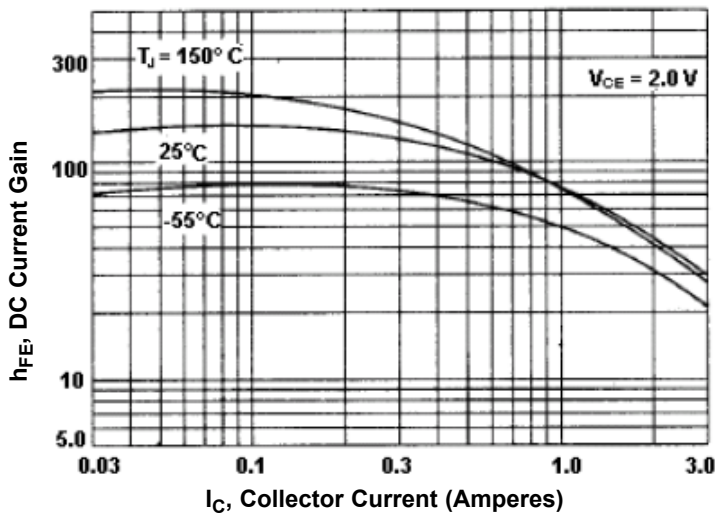
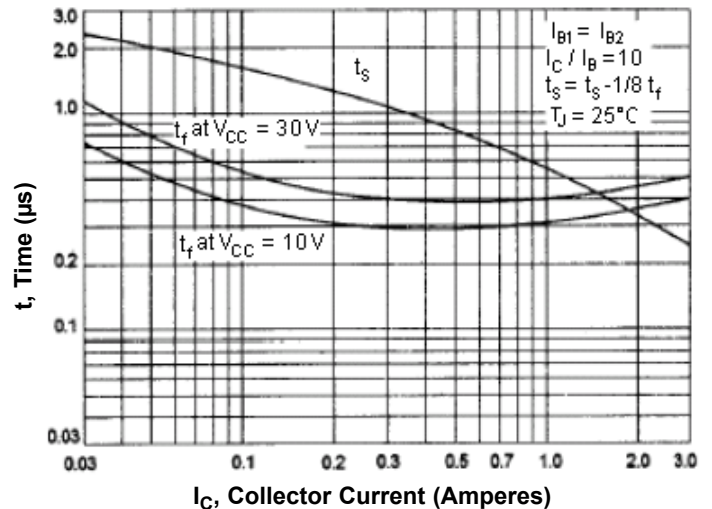


Figure-5 Turn-off Time

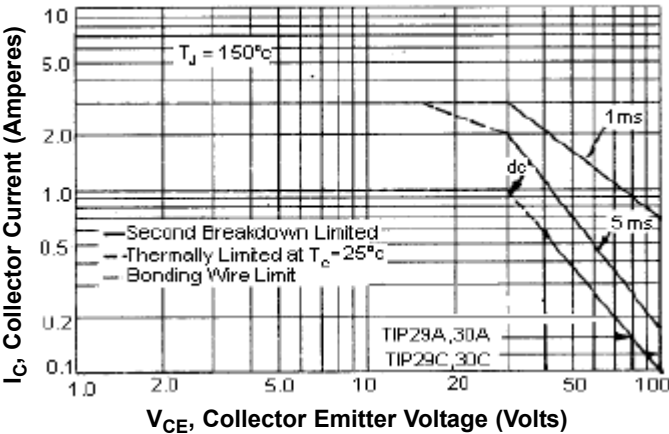


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Figure-6 Active Region Safe Operating Area



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown safe operating area curves indicate IC-VCE limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate

The data of Figure - 6 curve is based on $T_{J(PK)} = 150^{\circ}\text{C}$; T_C is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)} = 150^{\circ}\text{C}$ At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown

Part Number Table

Description	Type	Part Number
High Power Bipolar Transistor	NPN	TIP29A
High Power Bipolar Transistor	NPN	TIP29C

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