

# FQPF7N65C

## N-Channel QFET® MOSFET 650 V, 7 A, 1.4 Ω

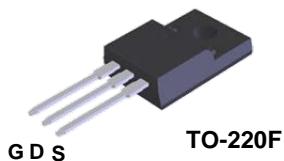


### Description

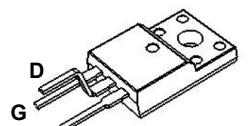
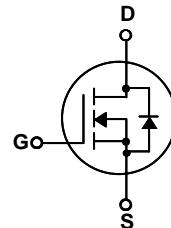
This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

### Features

- 7 A, 650 V,  $R_{DS(on)}$  = 1.4 Ω (Max.) @  $V_{GS} = 10$  V,  $I_D = 3.5$  A
- Low Gate Charge (Typ. 28 nC)
- Low  $C_{rss}$  (Typ. 12 pF)
- 100% Avalanche Tested



TO-220F

TO-220F  
Y-formed

### Absolute Maximum Ratings

$T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	FQPF7N65C / FQPF7N65CYDTU	Unit
$V_{DSS}$	Drain-Source Voltage	650	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ )	7 *	A
	- Continuous ( $T_C = 100^\circ\text{C}$ )	4.2 *	A
$I_{DM}$	Drain Current - Pulsed	(Note 1)	A
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy	(Note 2)	mJ
$I_{AR}$	Avalanche Current	(Note 1)	A
$E_{AR}$	Repetitive Avalanche Energy	(Note 1)	mJ
$dv/dt$	Peak Diode Recovery $dv/dt$	(Note 3)	V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ )	52	W
	- Derate above $25^\circ\text{C}$	0.42	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
$T_L$	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

\* Drain current limited by maximum junction temperature.

### Thermal Characteristics

Symbol	Parameter	FQPF7N65C / FQPF7N65CYDTU	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	2.4	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	$^\circ\text{C}/\text{W}$

## Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FQPF7N65C	FQPF7N65C	TO-220F	-	-	50
FQPF7N65C	FQPF7N65CYDTU	TO-220F (Y-formed)	-	-	50

## Electrical Characteristics

$T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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### Off Characteristics

$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}$ , $I_D = 250 \mu\text{A}$	650	--	--	V
$\Delta \text{BV}_{\text{DSS}} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to $25^\circ\text{C}$	--	0.8	--	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 650 \text{ V}$ , $V_{\text{GS}} = 0 \text{ V}$	--	--	1	$\mu\text{A}$
		$V_{\text{DS}} = 520 \text{ V}$ , $T_C = 125^\circ\text{C}$	--	--	10	$\mu\text{A}$
$I_{\text{GSSF}}$	Gate-Body Leakage Current, Forward	$V_{\text{GS}} = 30 \text{ V}$ , $V_{\text{DS}} = 0 \text{ V}$	--	--	100	nA
$I_{\text{GSSR}}$	Gate-Body Leakage Current, Reverse	$V_{\text{GS}} = -30 \text{ V}$ , $V_{\text{DS}} = 0 \text{ V}$	--	--	-100	nA

### On Characteristics

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = 250 \mu\text{A}$	2.0	--	4.0	V
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = 10 \text{ V}$ , $I_D = 3.5 \text{ A}$	--	1.2	1.4	$\Omega$
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 40 \text{ V}$ , $I_D = 3.5 \text{ A}$	--	8	--	S

### Dynamic Characteristics

$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = 25 \text{ V}$ , $V_{\text{GS}} = 0 \text{ V}$ , $f = 1.0 \text{ MHz}$	--	955	1245	pF
$C_{\text{oss}}$	Output Capacitance		--	100	130	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		--	12	16	pF

### Switching Characteristics

$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}} = 325 \text{ V}$ , $I_D = 7 \text{ A}$ , $R_G = 25 \Omega$ (Note 4)	--	20	50	ns
$t_r$	Turn-On Rise Time		--	50	110	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	90	190	ns
$t_f$	Turn-Off Fall Time		--	55	120	ns
$Q_g$	Total Gate Charge	$V_{\text{DS}} = 520 \text{ V}$ , $I_D = 7 \text{ A}$ , $V_{\text{GS}} = 10 \text{ V}$ (Note 4)	--	28	36	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	4.5	--	nC
$Q_{\text{gd}}$	Gate-Drain Charge		--	12	--	nC

### Drain-Source Diode Characteristics and Maximum Ratings

$I_S$	Maximum Continuous Drain-Source Diode Forward Current	--	--	7	A	
$I_{\text{SM}}$	Maximum Pulsed Drain-Source Diode Forward Current	--	--	28	A	
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$V_{\text{GS}} = 0 \text{ V}$ , $I_S = 7 \text{ A}$	--	--	1.4	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}} = 0 \text{ V}$ , $I_S = 7 \text{ A}$	--	400	--	ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$dI_F / dt = 100 \text{ A}/\mu\text{s}$	--	3.3	--	$\mu\text{C}$

#### Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2.  $L = 8\text{mH}$ ,  $I_{\text{AS}} = 7\text{A}$ ,  $V_{\text{DD}} = 50\text{V}$ ,  $R_G = 25 \Omega$ , Starting  $T_J = 25^\circ\text{C}$
3.  $I_{\text{SD}} \leq 7\text{A}$ ,  $dI/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{\text{DD}} \leq \text{BV}_{\text{DSS}}$ , Starting  $T_J = 25^\circ\text{C}$
4. Essentially independent of operating temperature

## Typical Characteristics

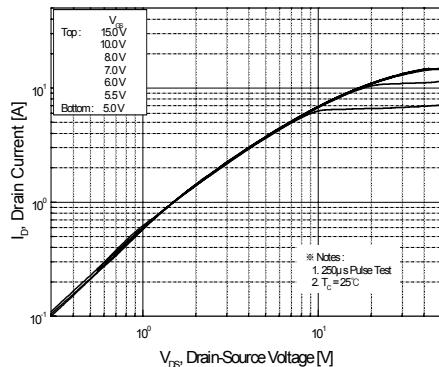


Figure 1. On-Region Characteristics

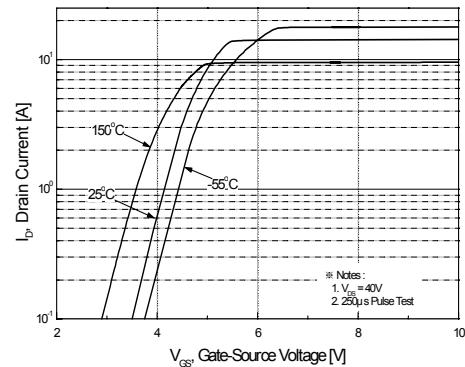


Figure 2. Transfer Characteristics

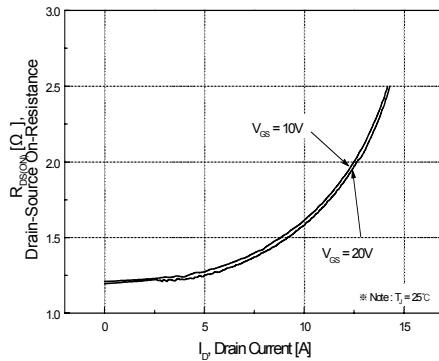


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

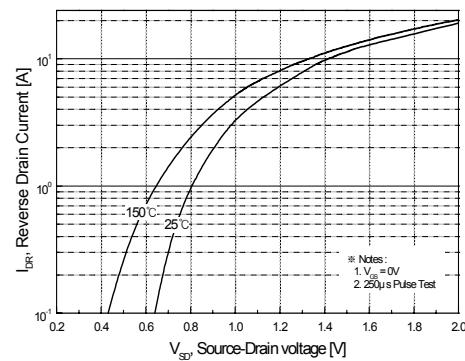


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

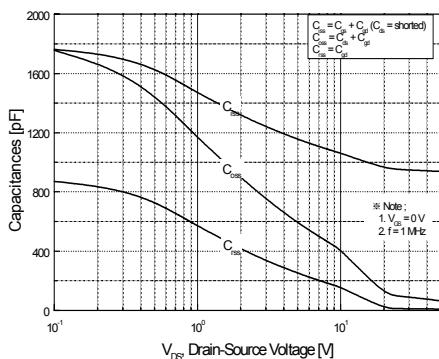


Figure 5. Capacitance Characteristics

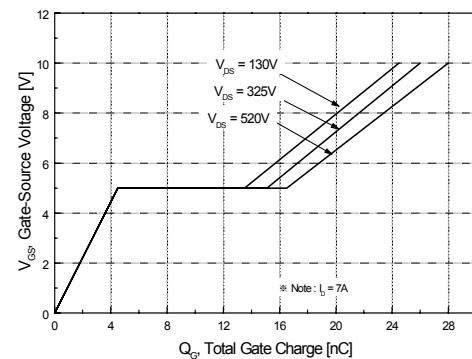
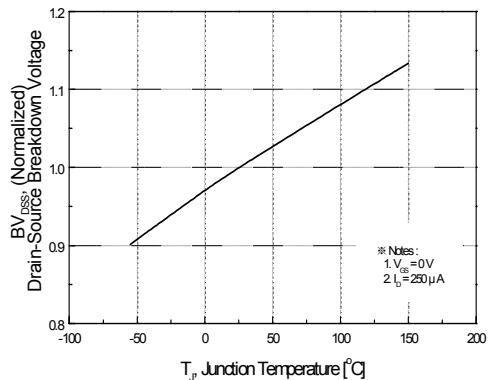
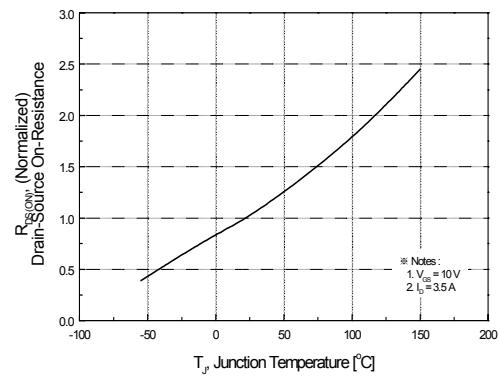


Figure 6. Gate Charge Characteristics

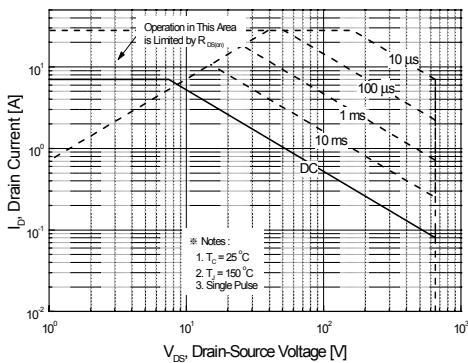
**Typical Characteristics** (Continued)



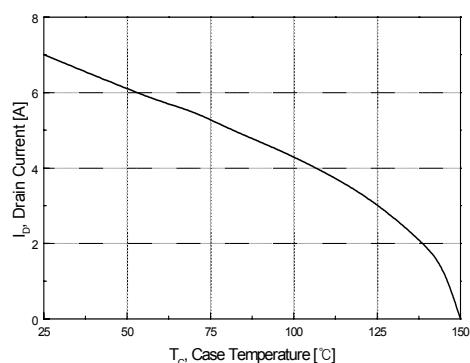
**Figure 7. Breakdown Voltage Variation vs Temperature**



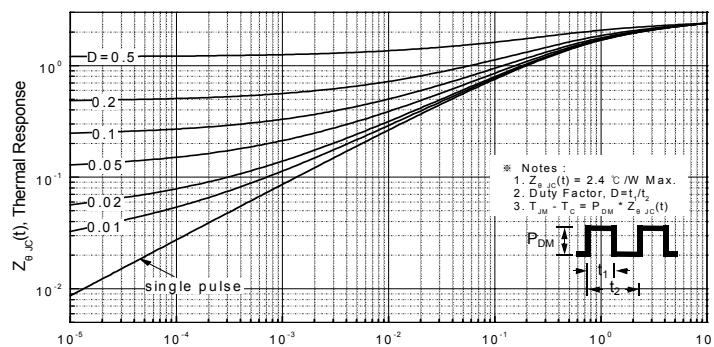
**Figure 8. On-Resistance Variation vs Temperature**



**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs Case Temperature**



**Figure 11 . Transient Thermal Response Curve**

Figure 12. Gate Charge Test Circuit & Waveform

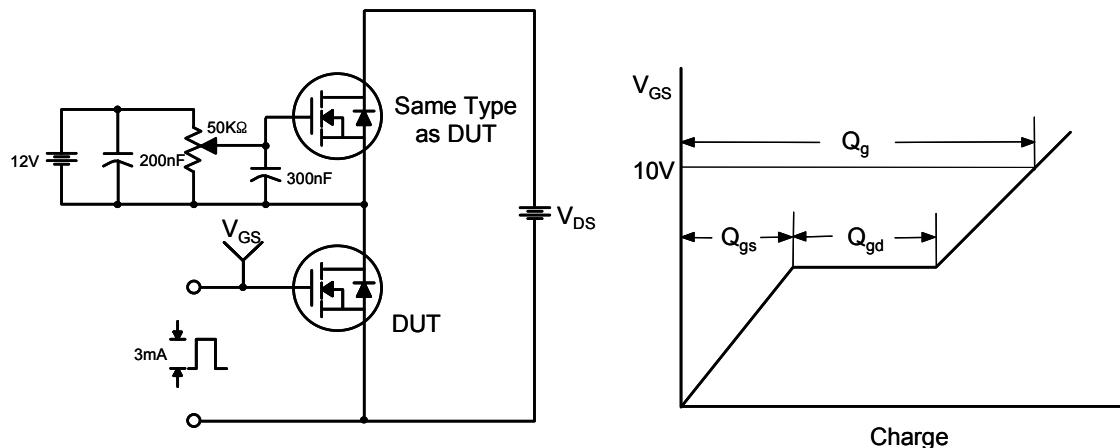


Figure 13. Resistive Switching Test Circuit & Waveforms

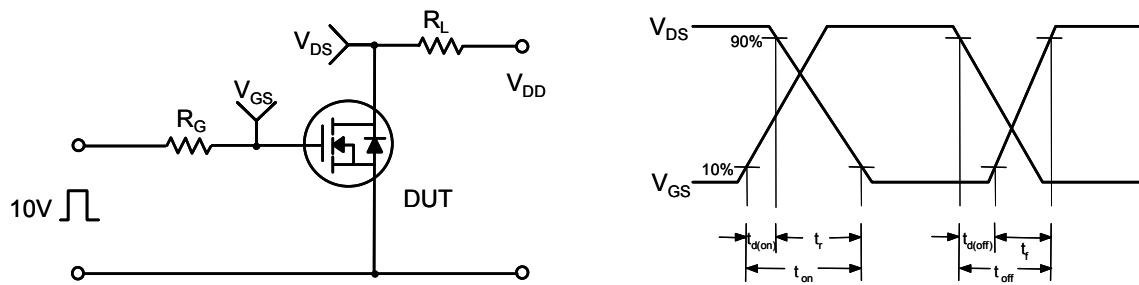


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

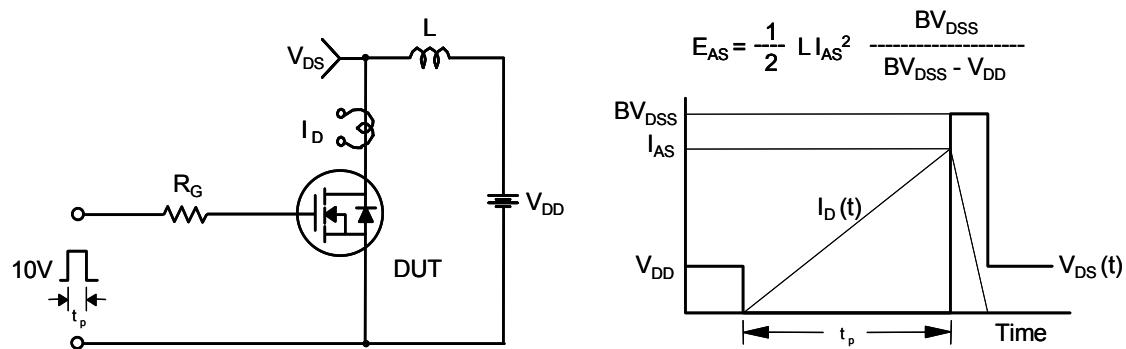
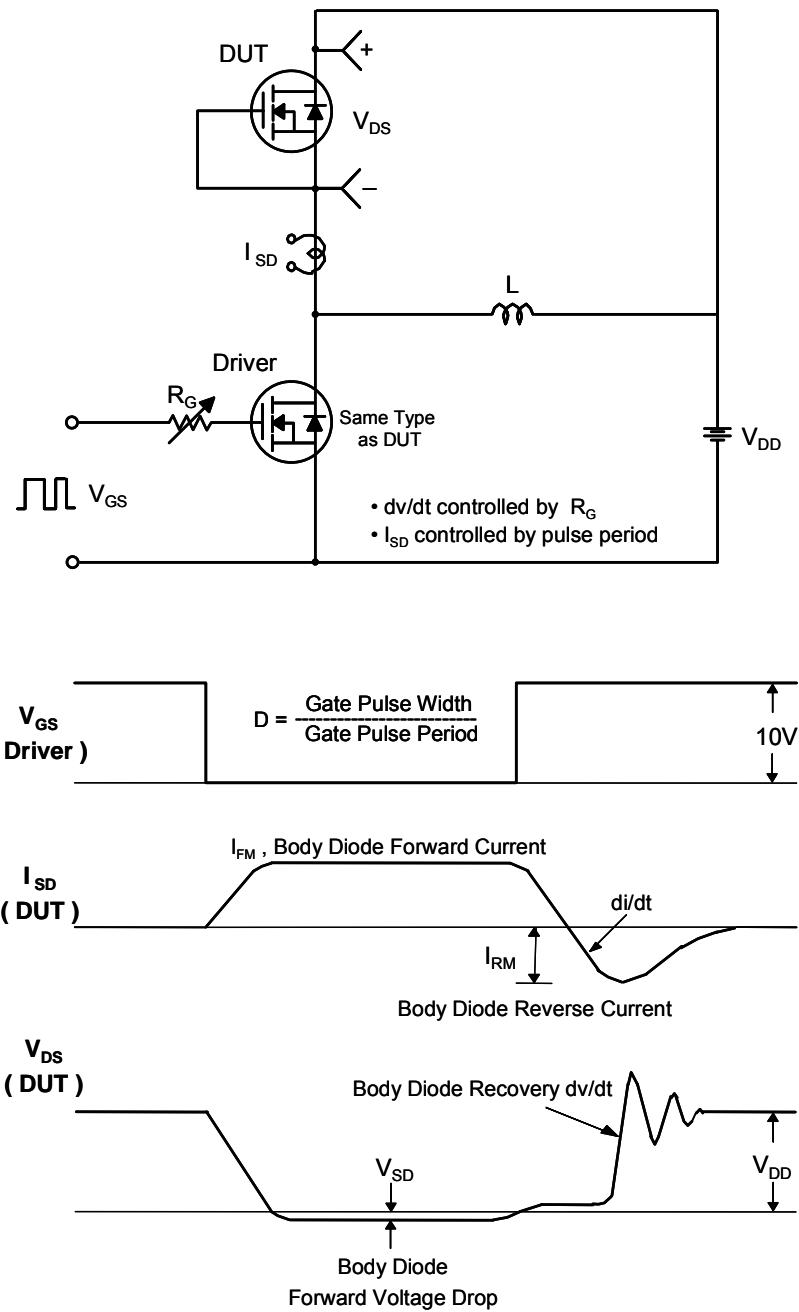
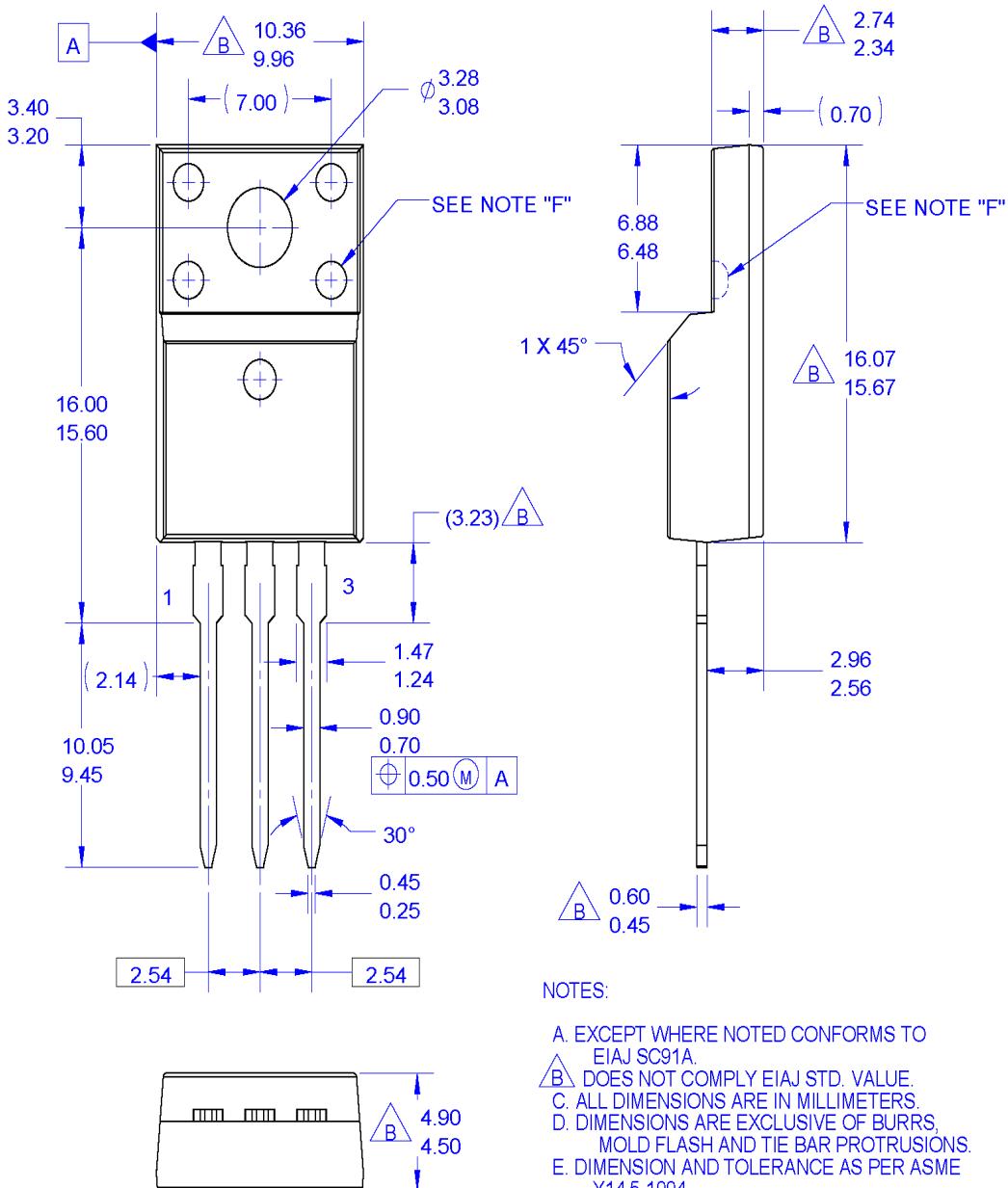


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



## Mechanical Dimensions

## TO-220F



**Figure 16. TO-220F 3L - TO220, Molded, 3LD, Full Pack, EIAJ SC91, Straight lead**

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#### Dimensions in Millimeters

## Mechanical Dimensions

## TO-220F (Y-formed)

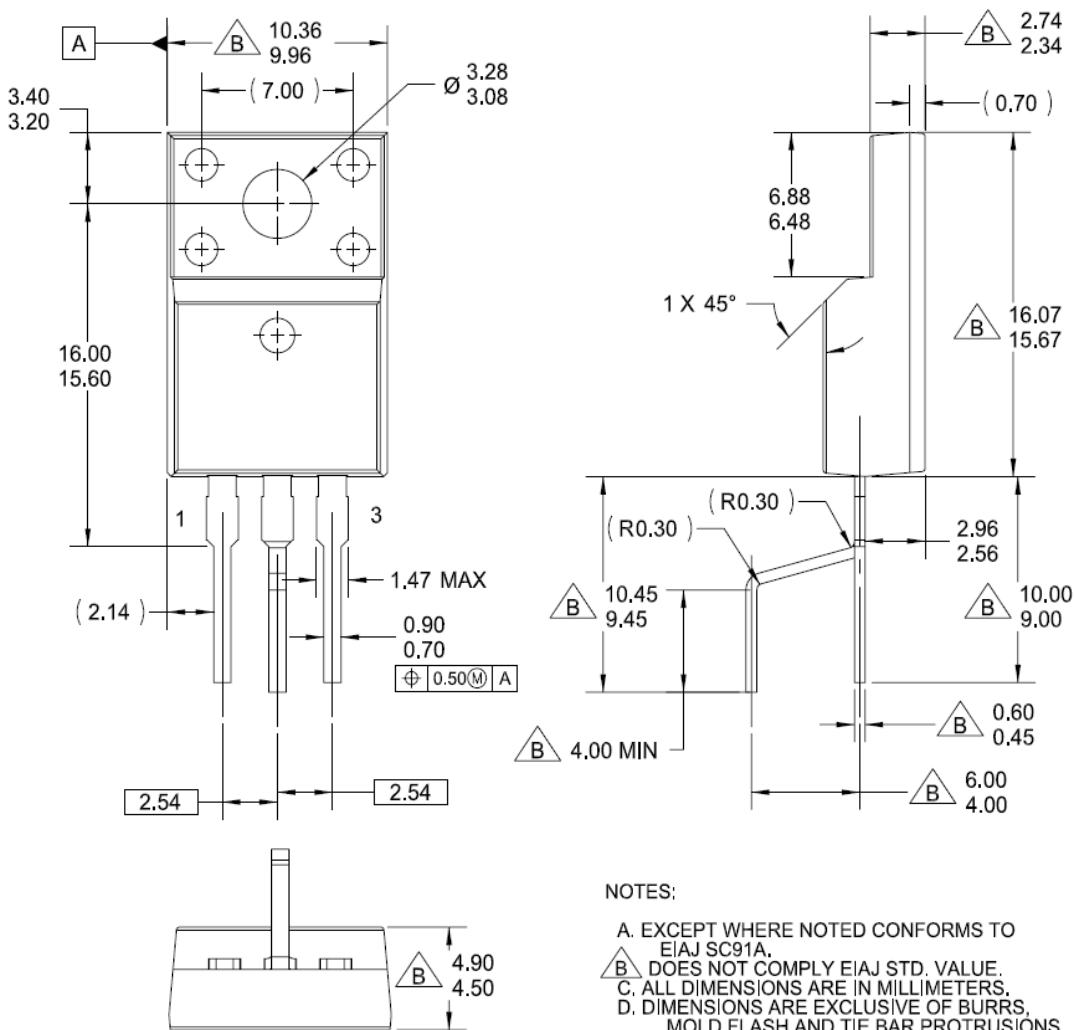


Figure 17. TO-220F 3L - TO220, Molded, 3LD, Full Pack, EIAJ SC91, Y formed lead

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Dimensions in Millimeters


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